

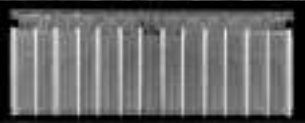
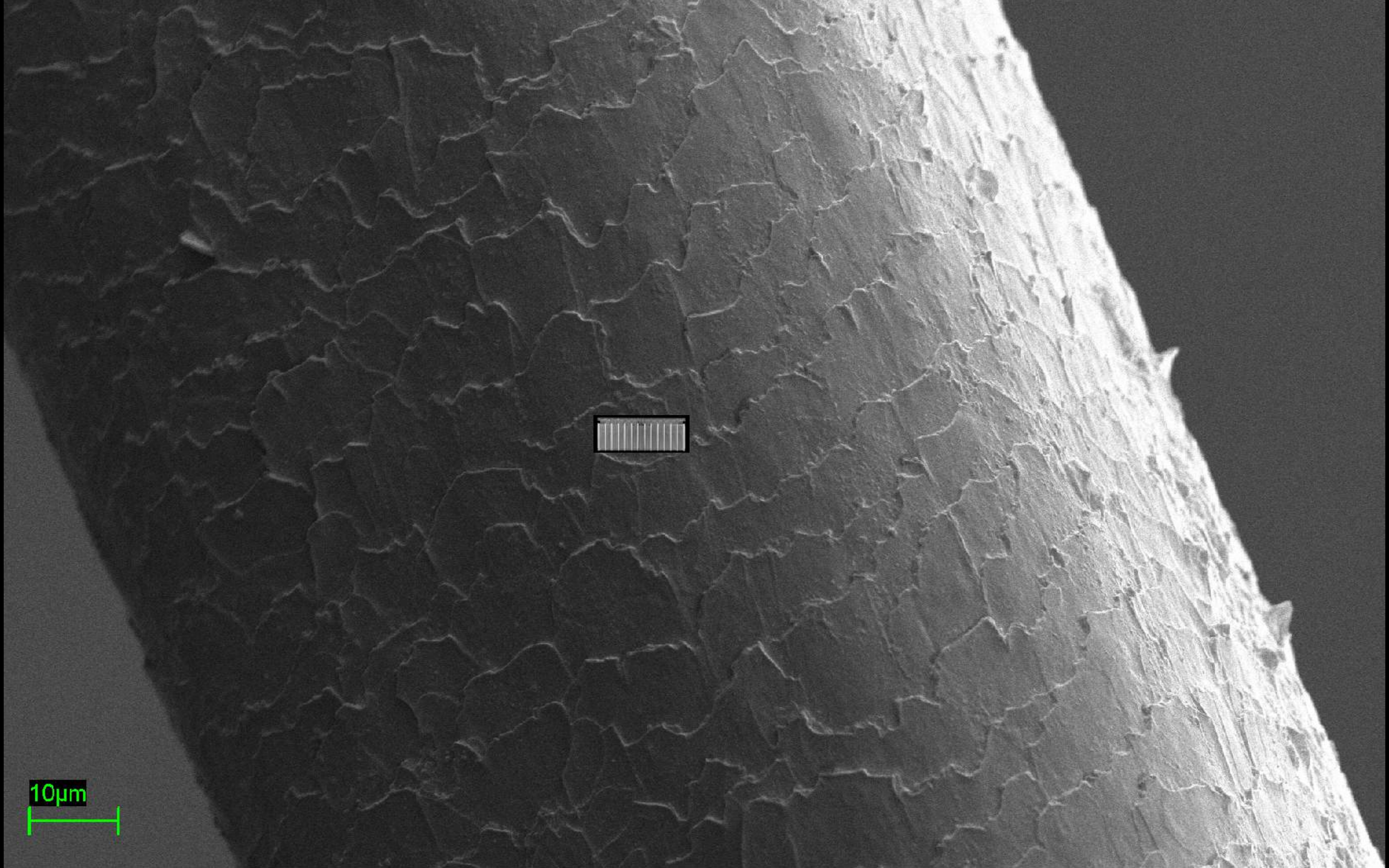
A microscopic view of several silicon chips, showing their intricate grid patterns and various colored regions. The chips are arranged in a slightly overlapping, circular pattern, with the central chip being the most prominent and in focus. The background is dark, making the metallic and colored patterns of the chips stand out.

HOW ADVANCED CHIPS ARE REDEFINING GLOBAL DOMINANCE

---

# FROM SILICON TO SOVEREIGNTY

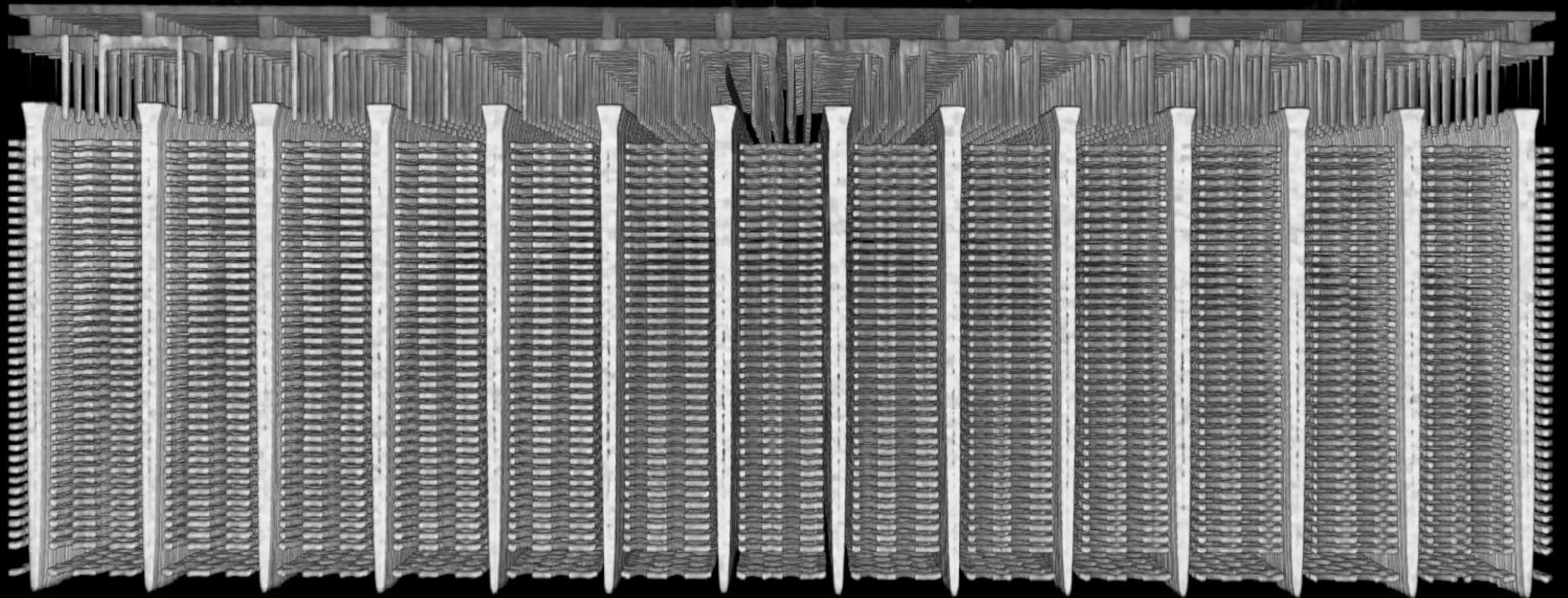
THORSTEN HELLERT



10μm

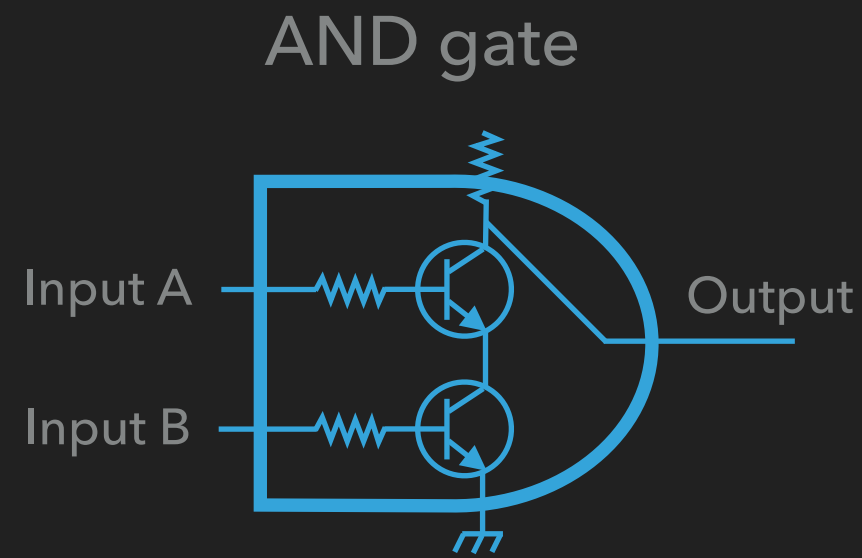


SAMSUNG V-NAND SSD, ELECTRON MICROSCOPY BY ZEISS CROSSBEAM (2015)

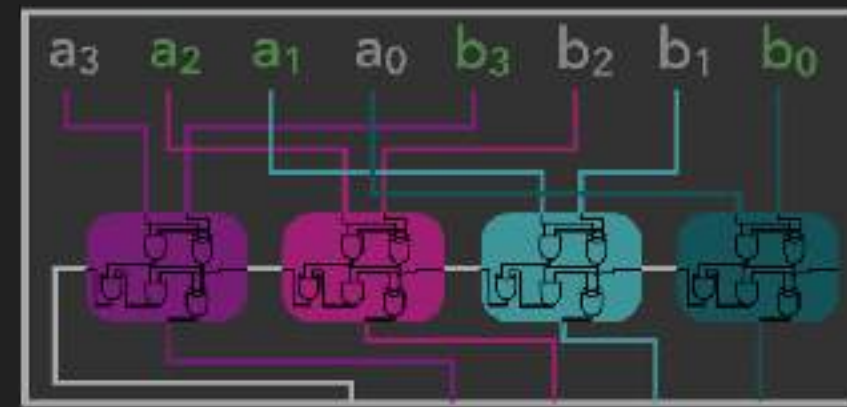


**10 $\mu$ m**

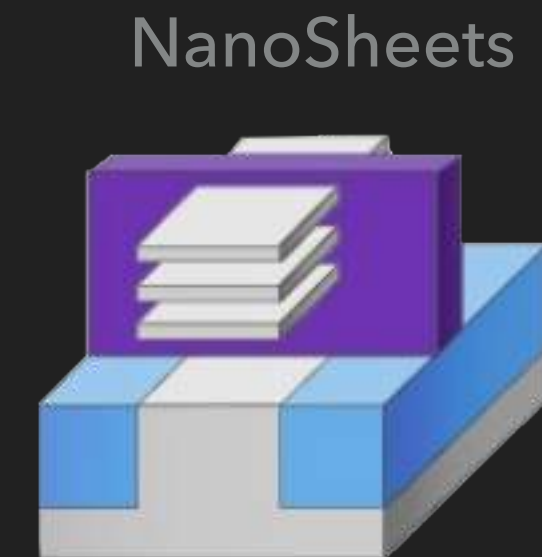
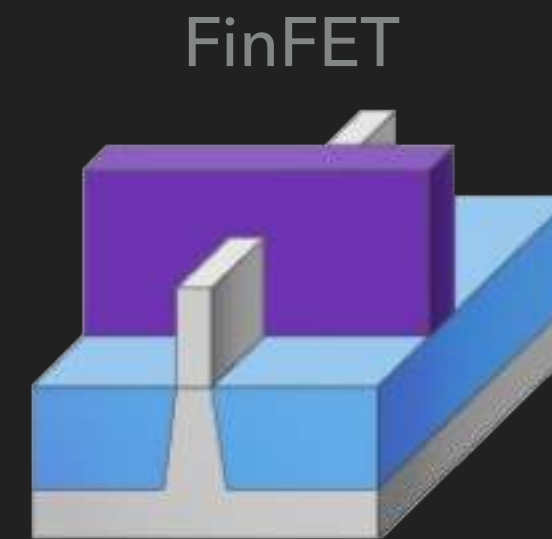
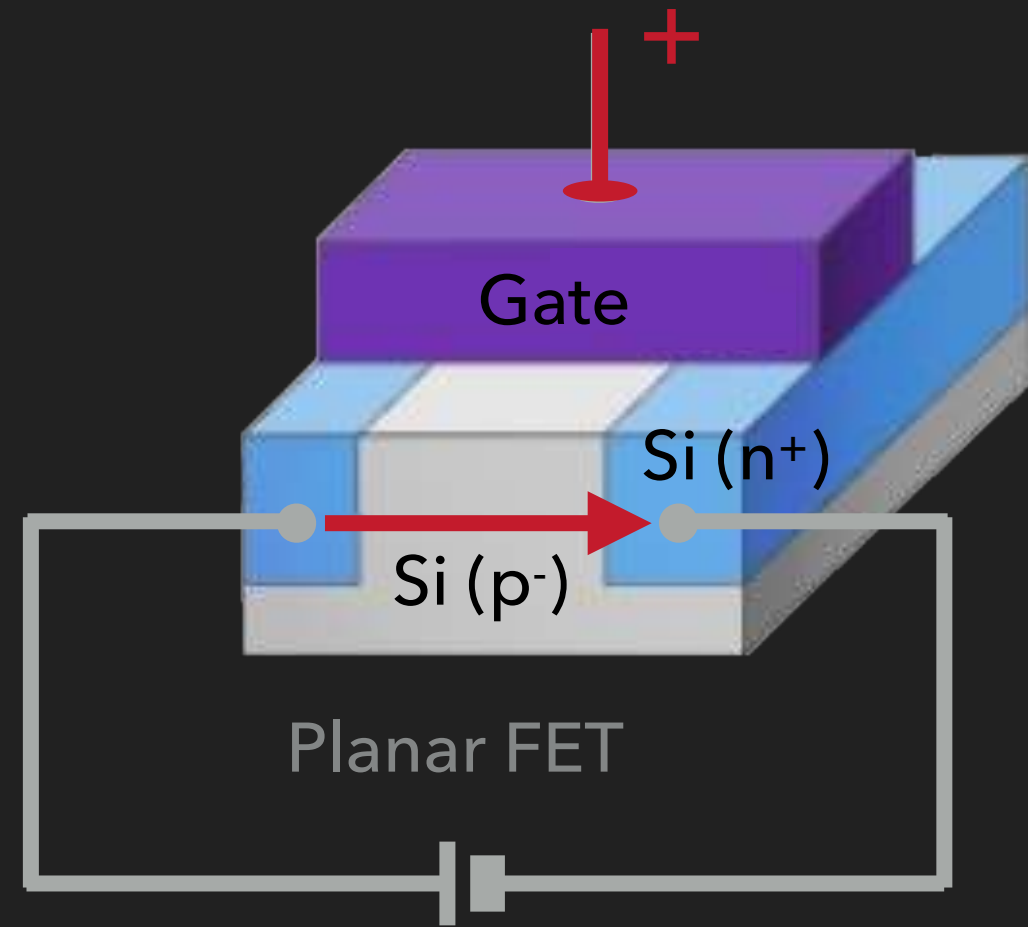
# PRINCIPLES OF TRANSISTORS



$A = 6 = [0\ 1\ 1\ 0]$  plus  $B = 9 = [1\ 0\ 0\ 1]$



is equal to  $P = 15 = [0\ 1\ 1\ 1\ 1]$



1950

1960

1970

1980

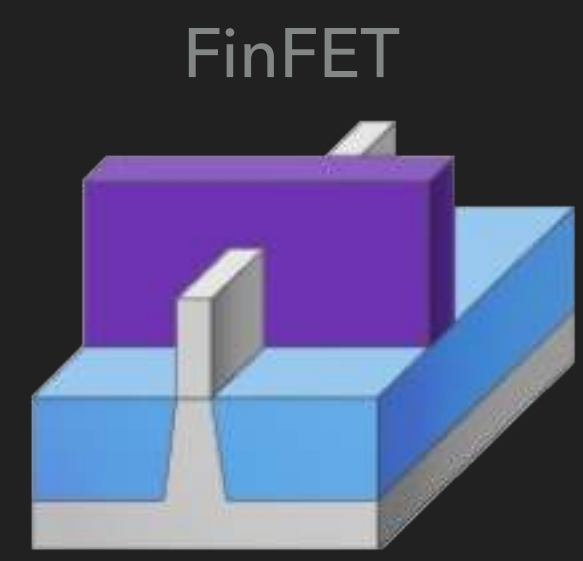
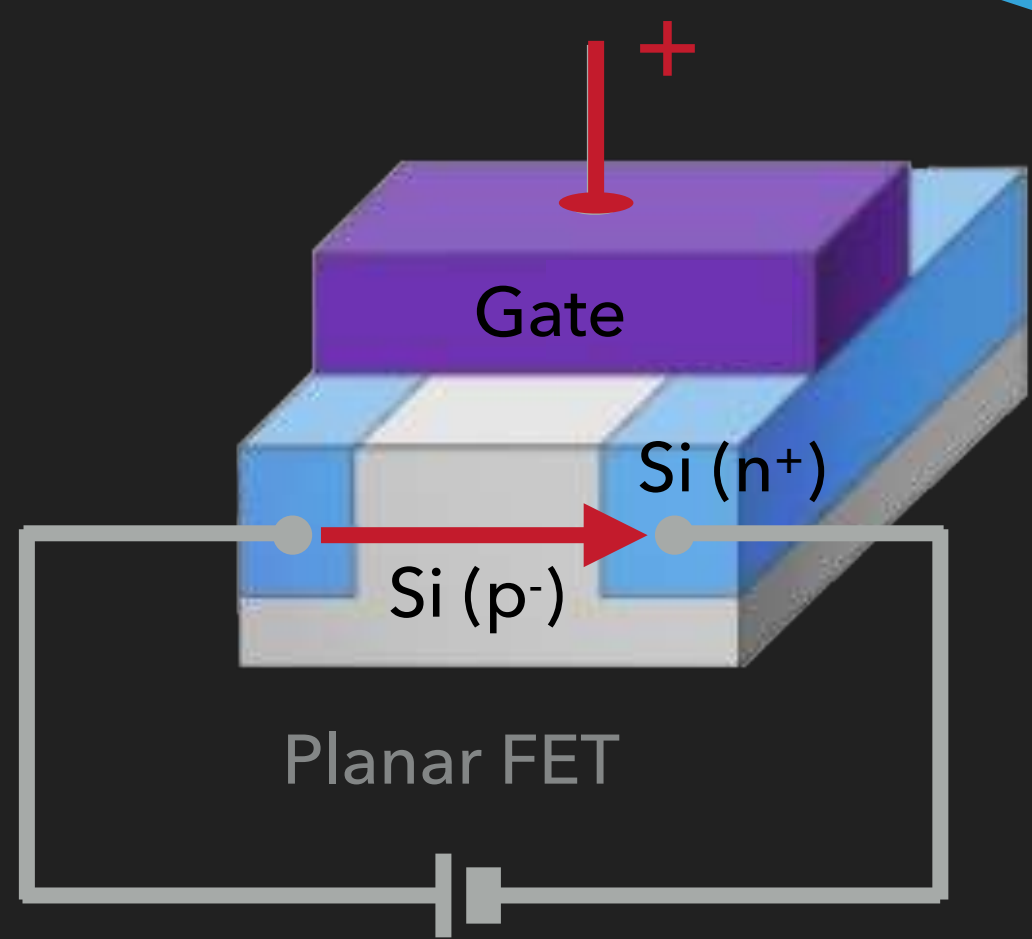
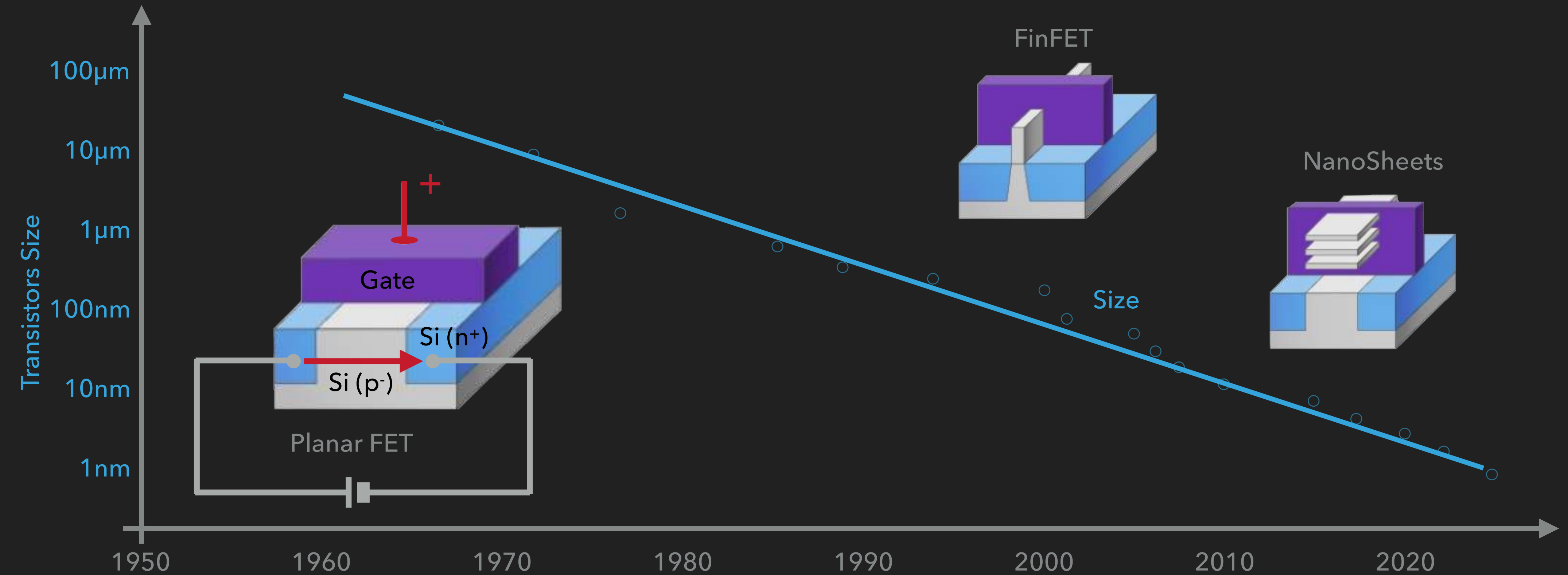
1990

2000

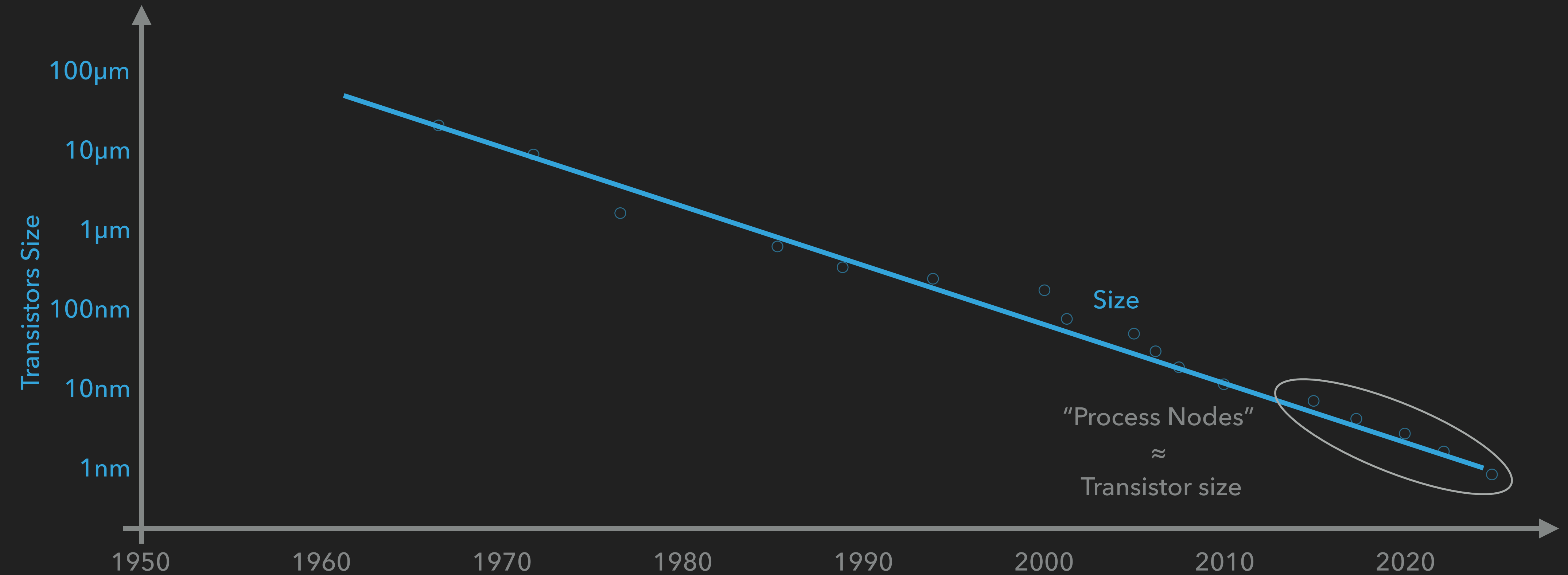
2010

2020

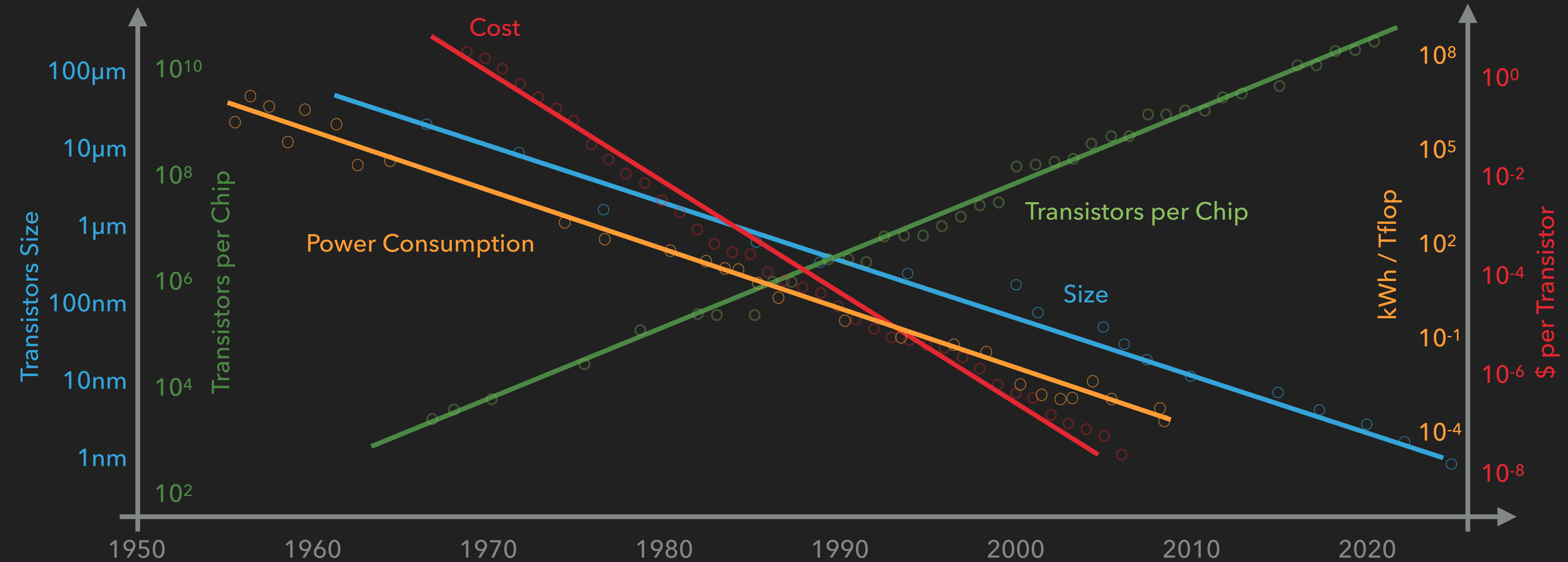
# HUMAN MANUFACTURING OF TRANSISTORS



# HUMAN MANUFACTURING OF TRANSISTORS

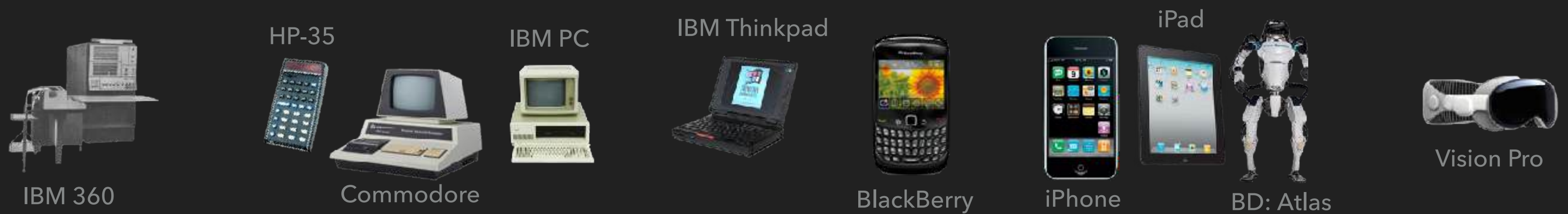
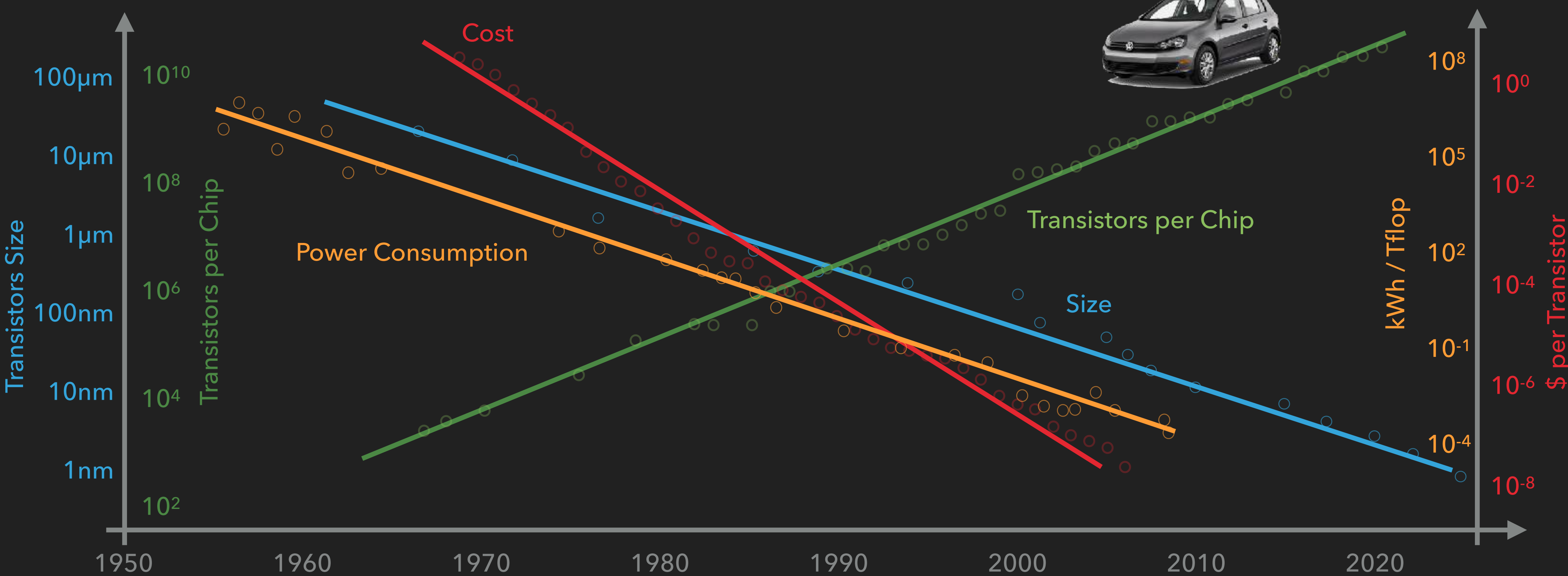


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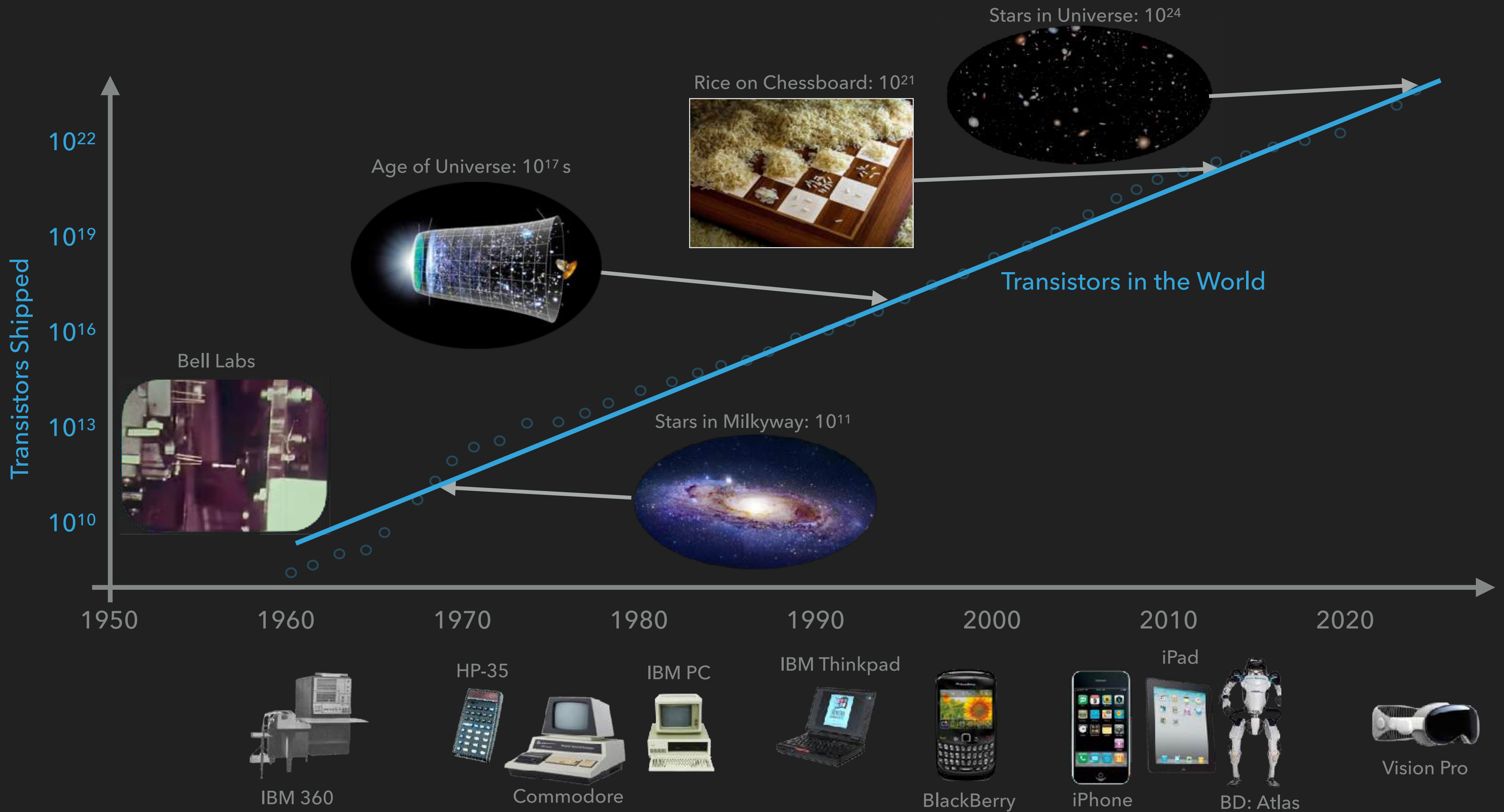
2010 VW Golf => \$100



<https://ourworldindata.org>  
<https://aiimpacts.org/electrical-efficiency-of-computing/>  
<https://semiengineering.com/>

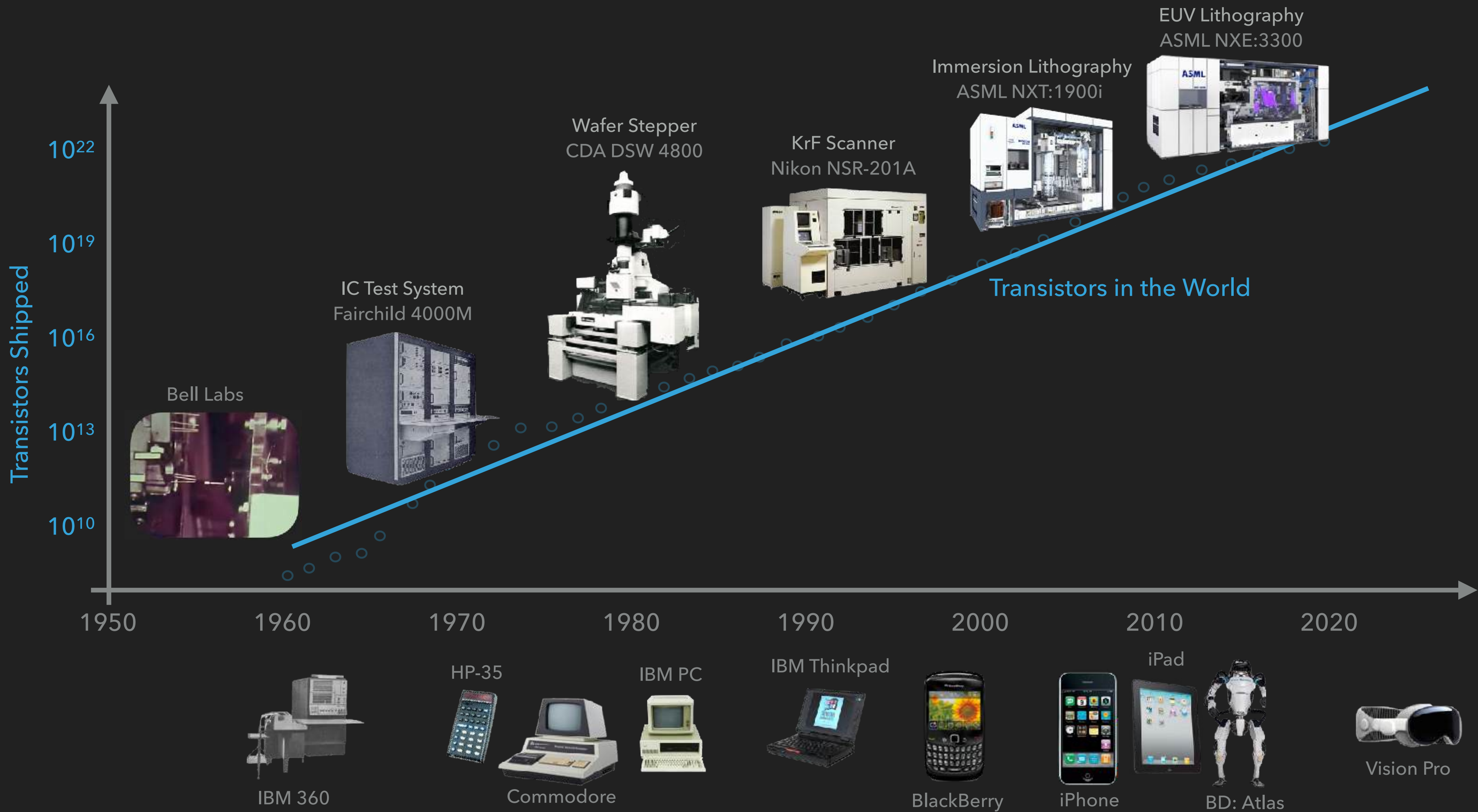


# HUMAN MANUFACTURING OF TRANSISTORS

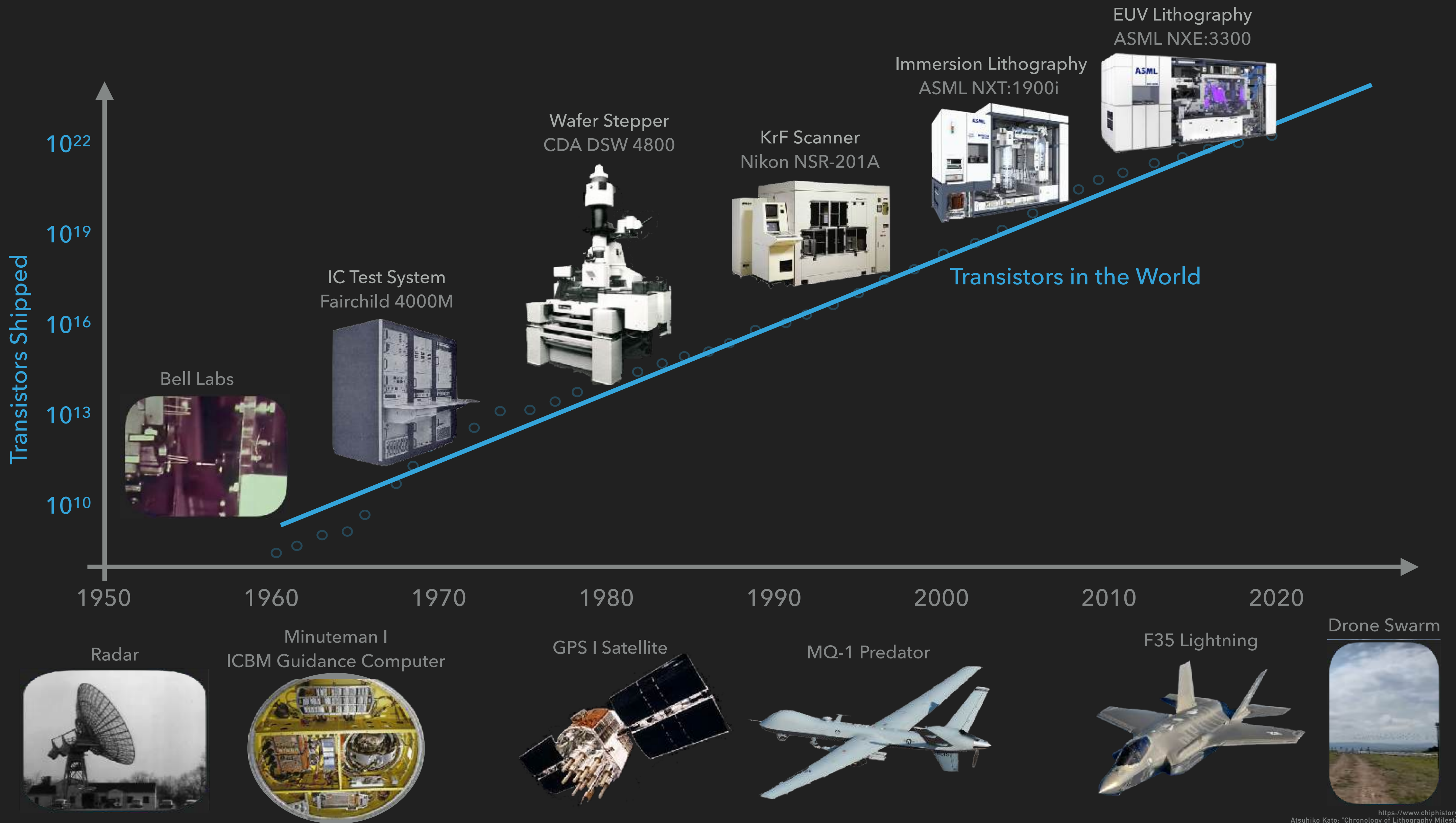


<https://ourworldindata.org>  
<https://aiimpacts.org/electrical-efficiency-of-computing/>  
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<https://science.nasa.gov/universe/stars/>

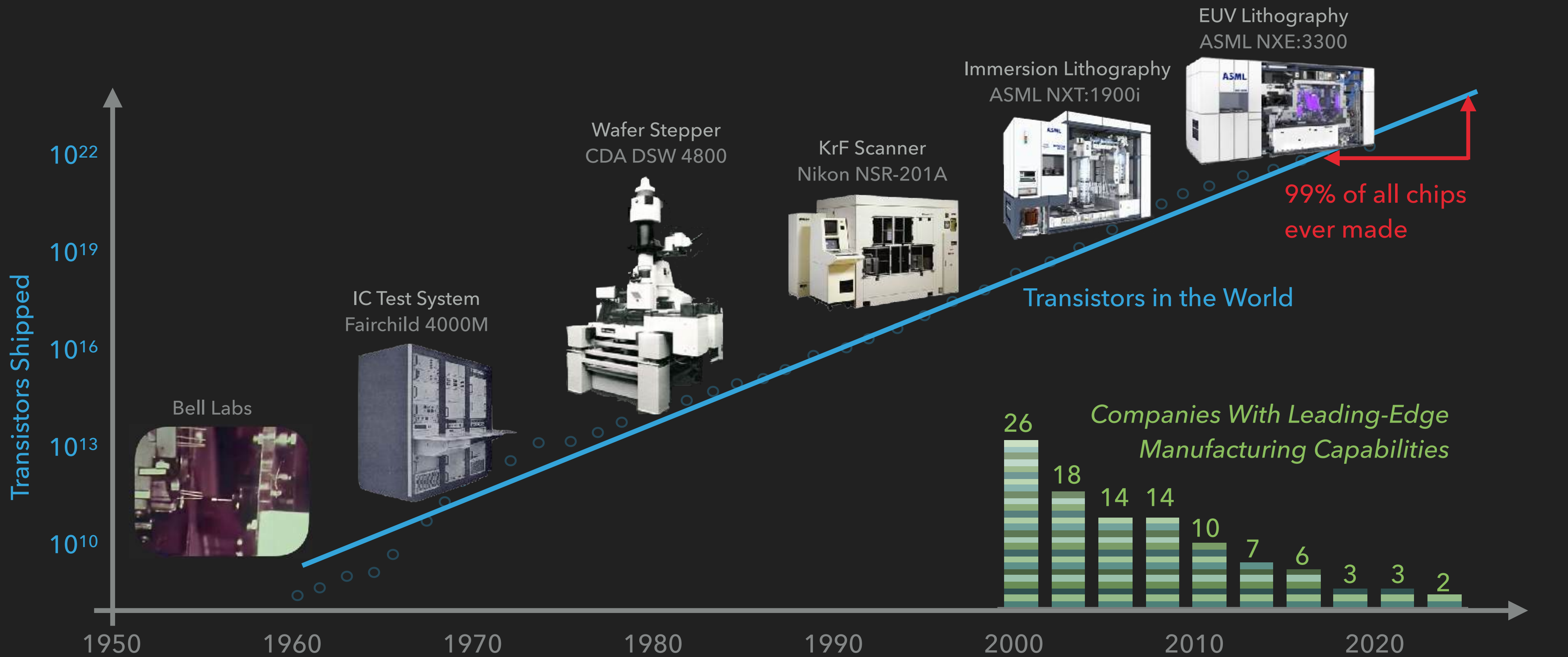
# HUMAN MANUFACTURING OF TRANSISTORS



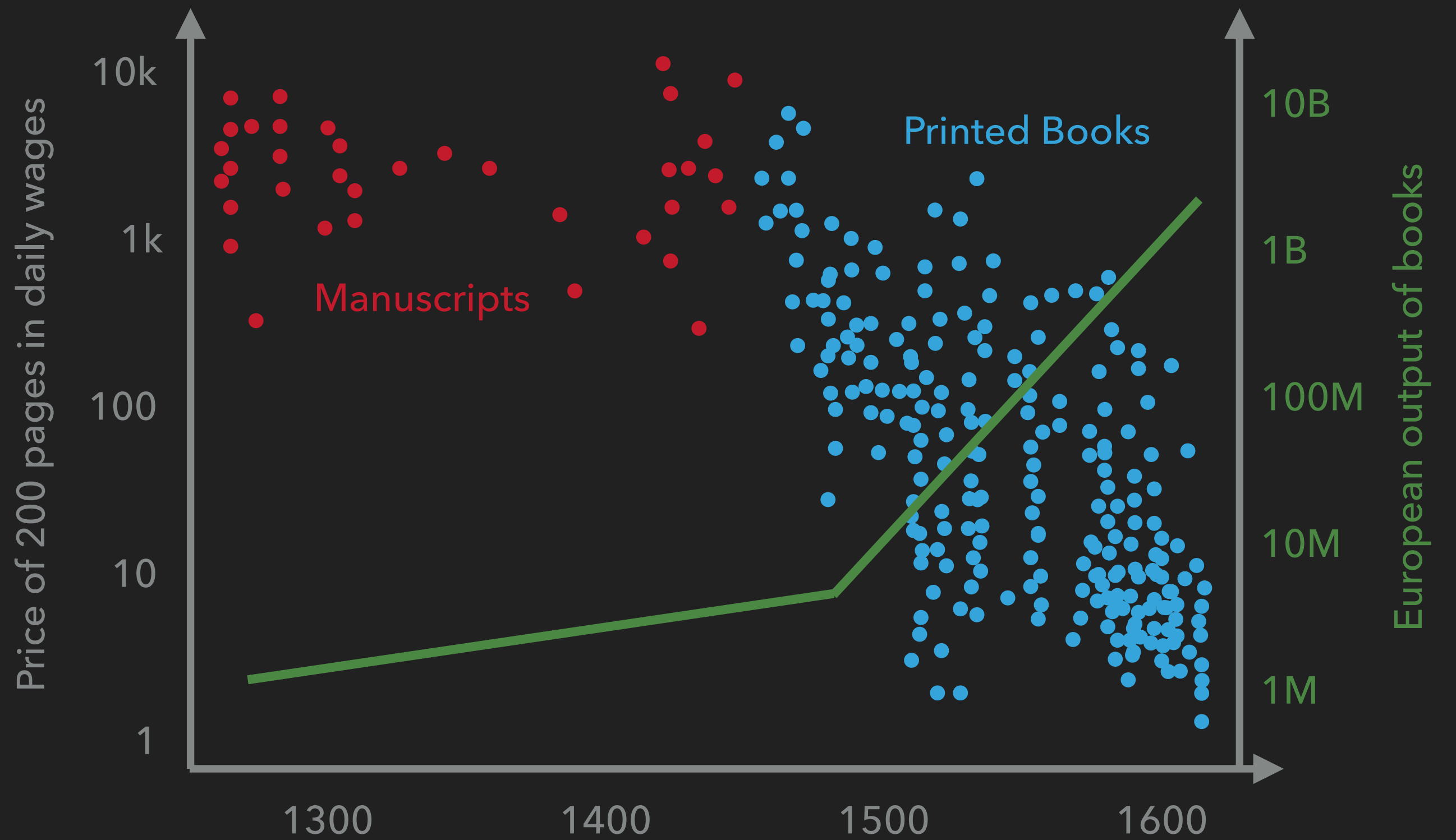
# HUMAN MANUFACTURING OF TRANSISTORS



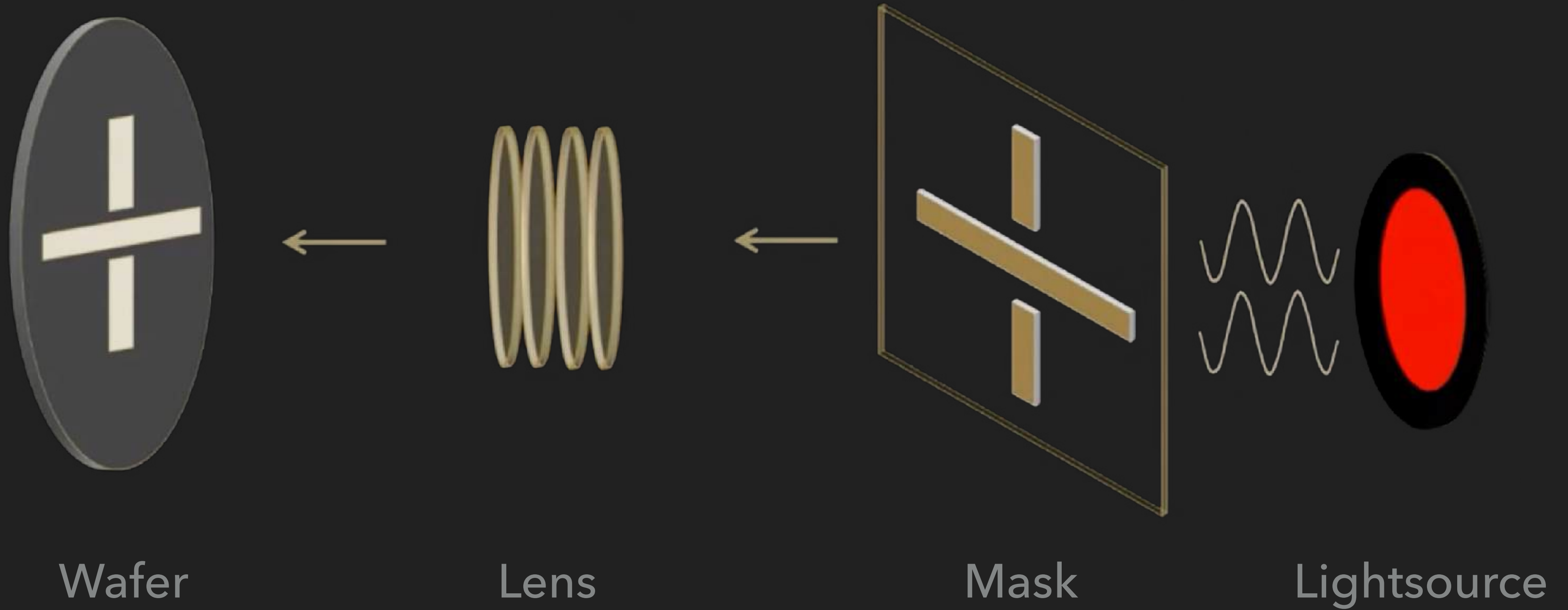
# HUMAN MANUFACTURING OF TRANSISTORS



# THE PRINTING PRESS – A REVOLUTION IN MASS PRODUCTION



# PRINCIPLES OF PHOTOLITHOGRAPHY

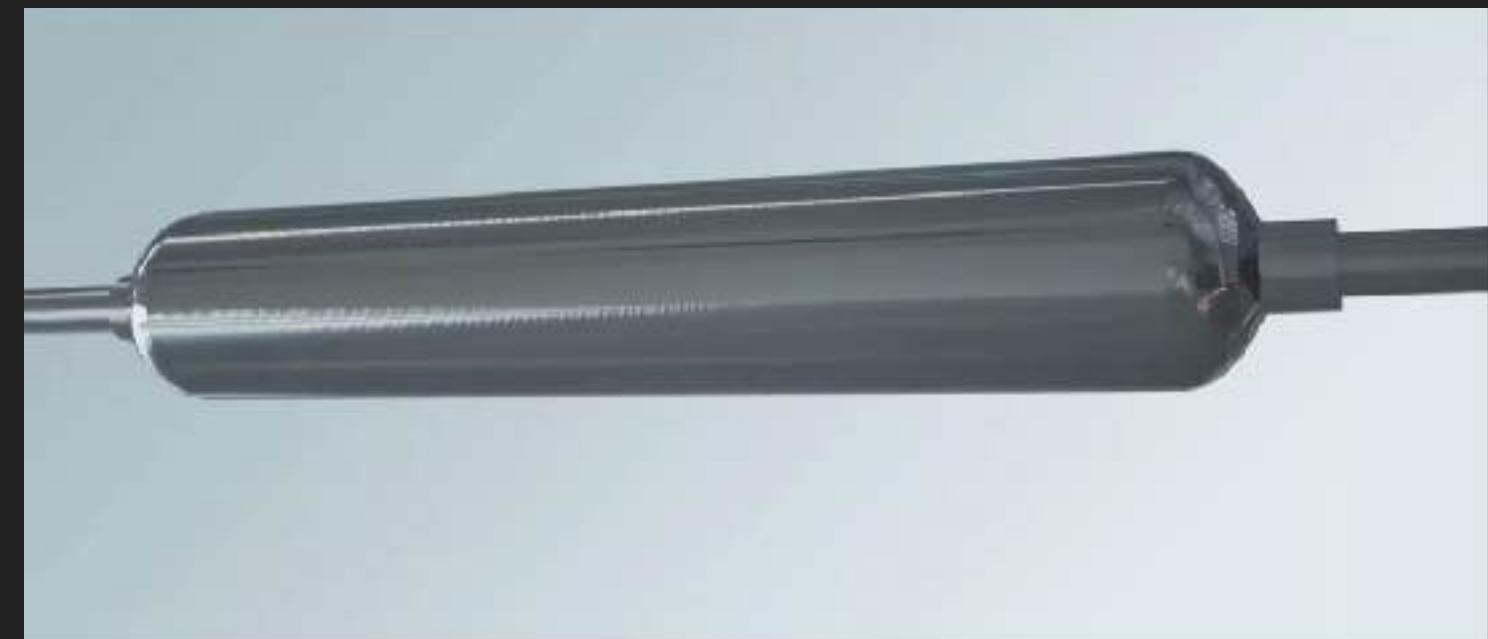


# SILICON WAFER PRODUCTION

Ultra pure quartz mining



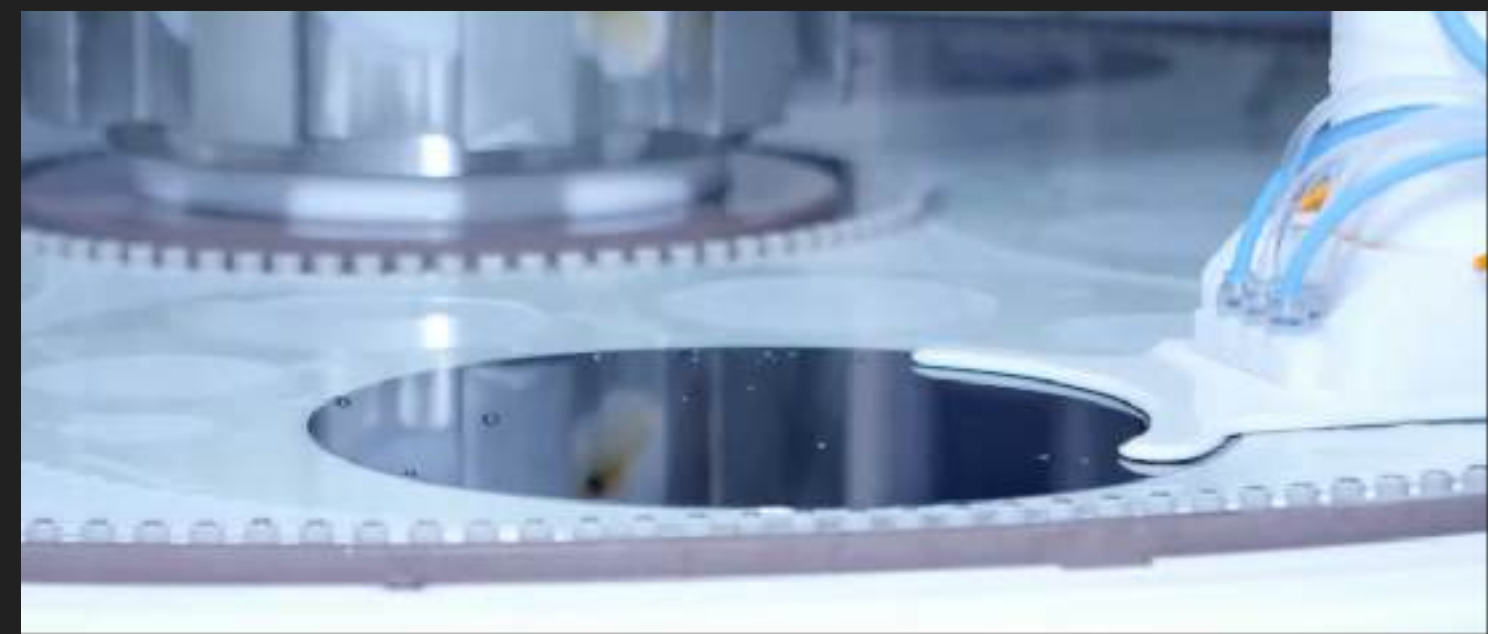
Ingot polishing and slicing



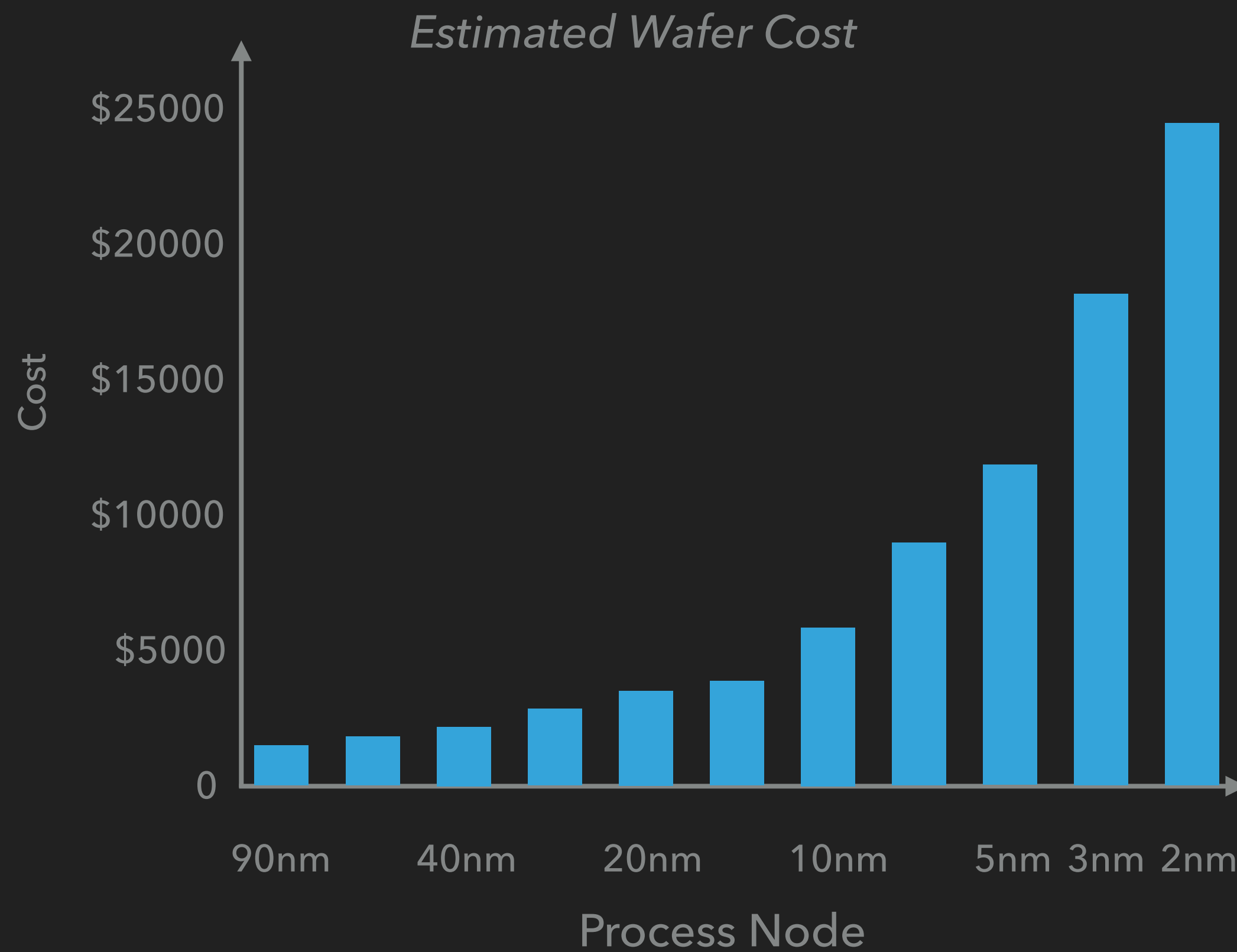
Silikon ingot crystal growing



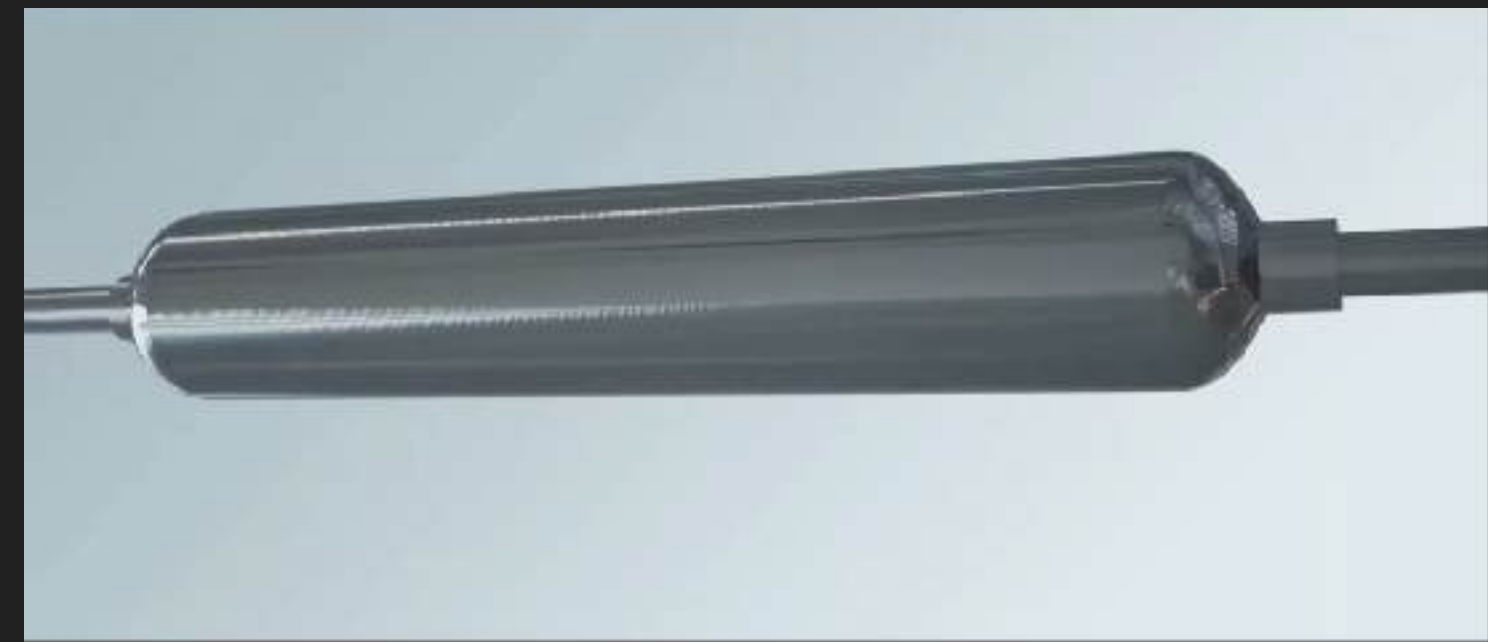
Wafer chemical and mechanical processing



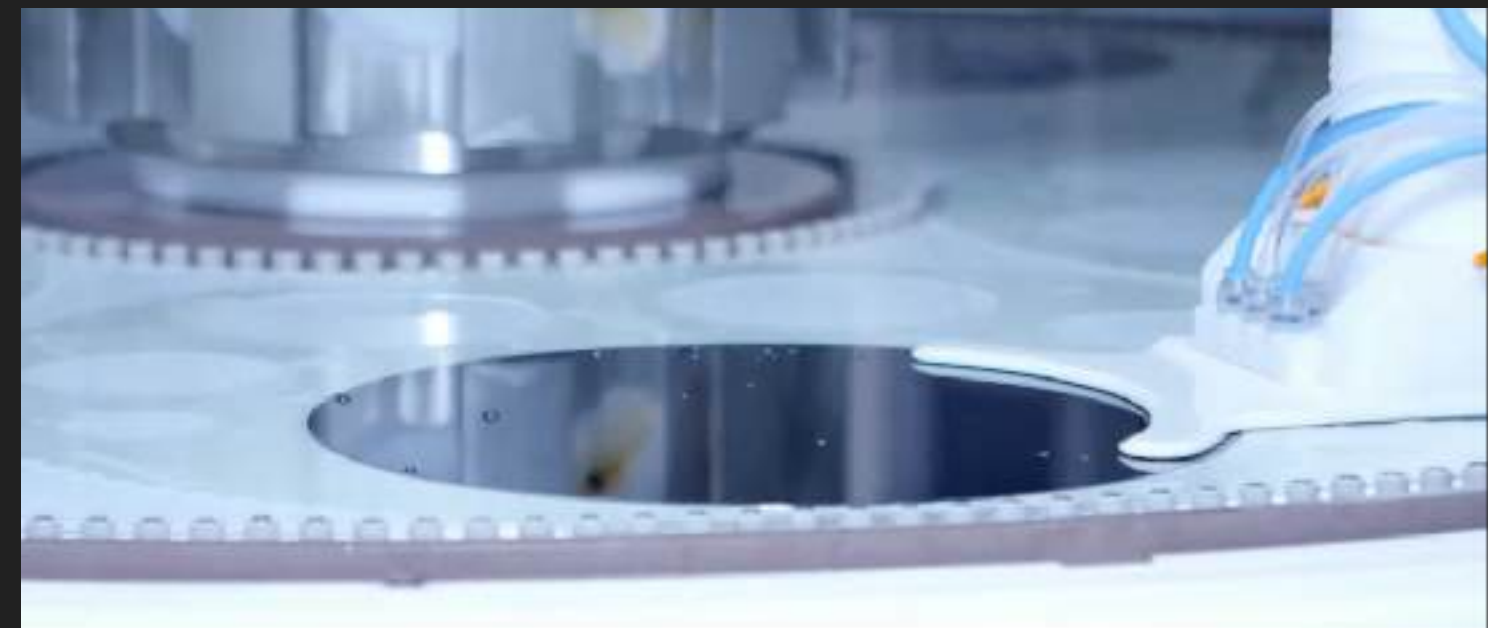
# SILICON WAFER PRODUCTION



Ingots polishing and slicing



Wafer chemical and mechanical processing







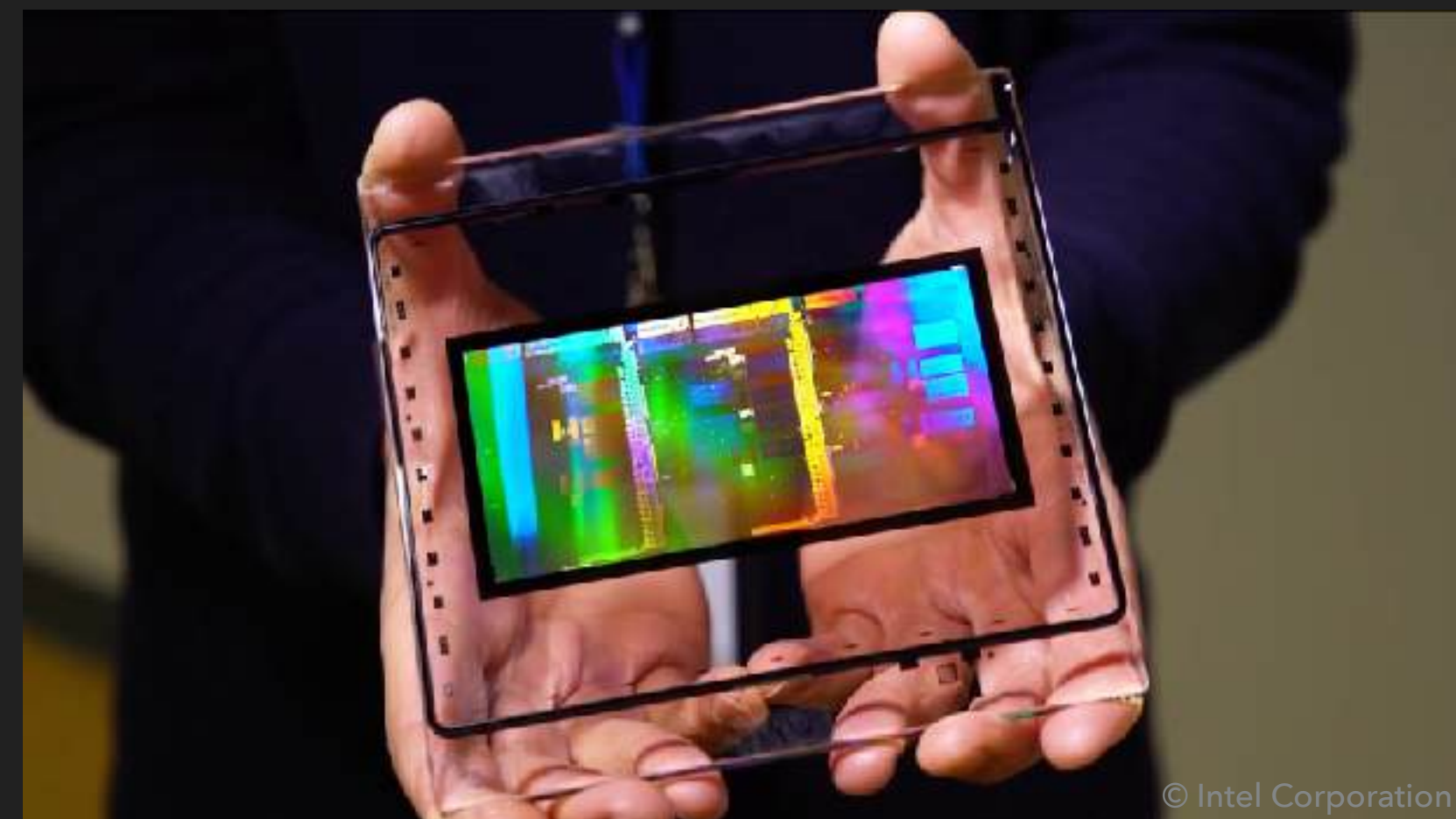
## PHOTOMASK

- ▶ Electron beam printing
- ▶ Zero defects
- ▶ X:  $33\text{mm}/13\text{nm} = 2.5 \times 10^6$  pixel
- ▶ Y:  $26\text{mm}/13\text{nm} = 2 \times 10^6$  pixel
- ▶ 5T Pixel Screen

How it started...



...and how it's going.



# PHOTOMASK

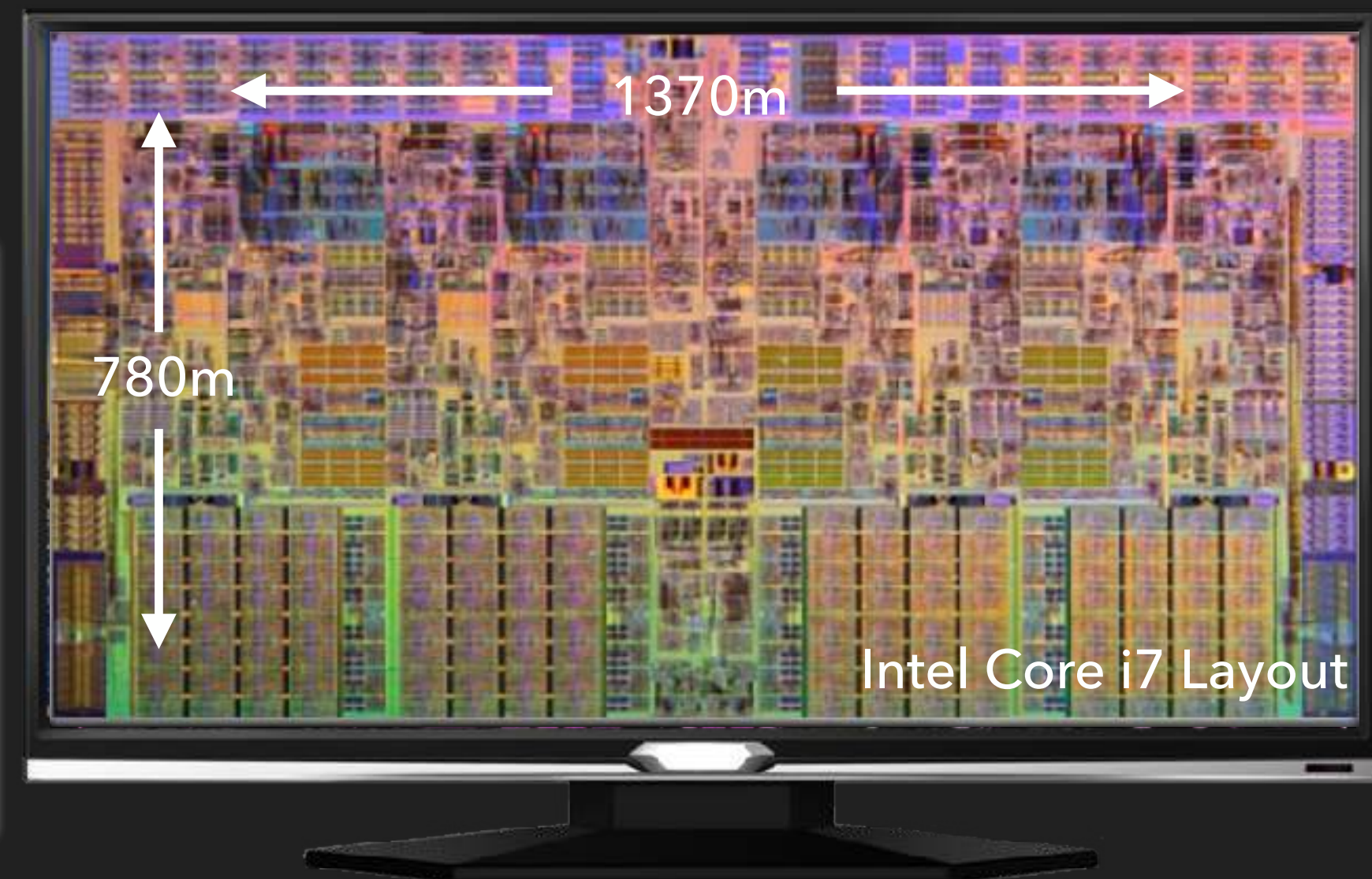
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How it started...

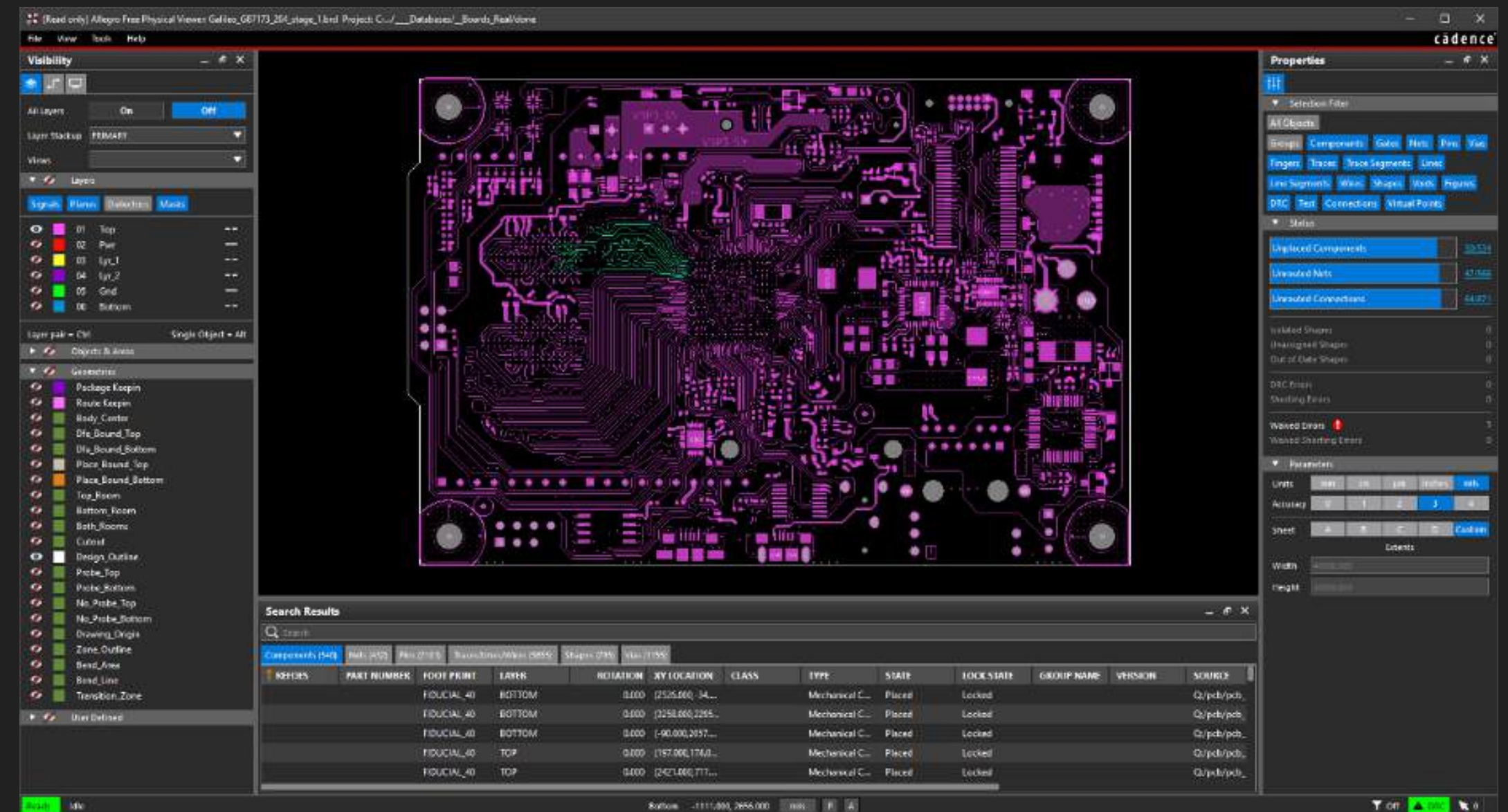


...and how it's going.



## ELECTRONIC DESIGN AUTOMATION

- ▶ NVIDIA 'Hopper': 80B Transistors
- ▶ Designed in about 2 years
- ▶ '100M transistors per day'
- ▶ 1-Layer Photomask File: 150GB
- ▶ About 100 Layers in latest 3nm Process



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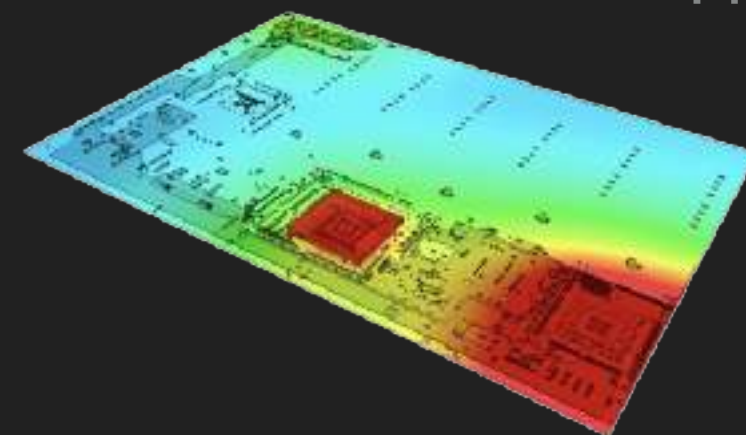


Prototyping on FPGAs

**SYNOPSYS**<sup>®</sup>

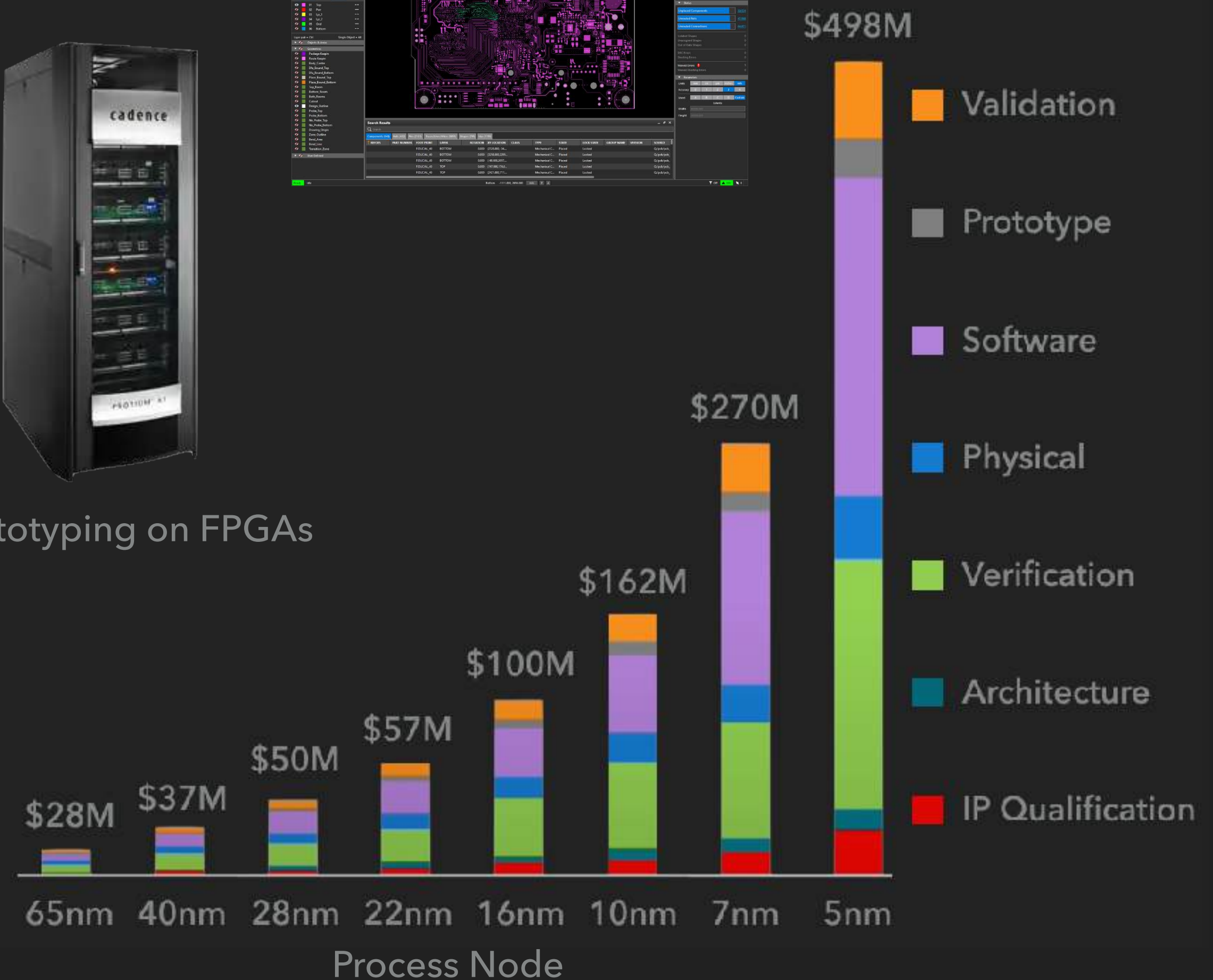
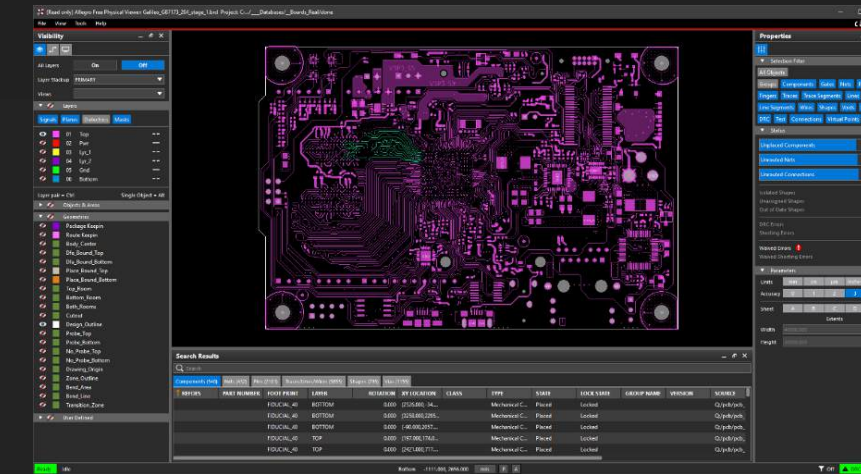
≈100% market share

**cadence**<sup>®</sup>



Thermal Simulations

Layout Optimization

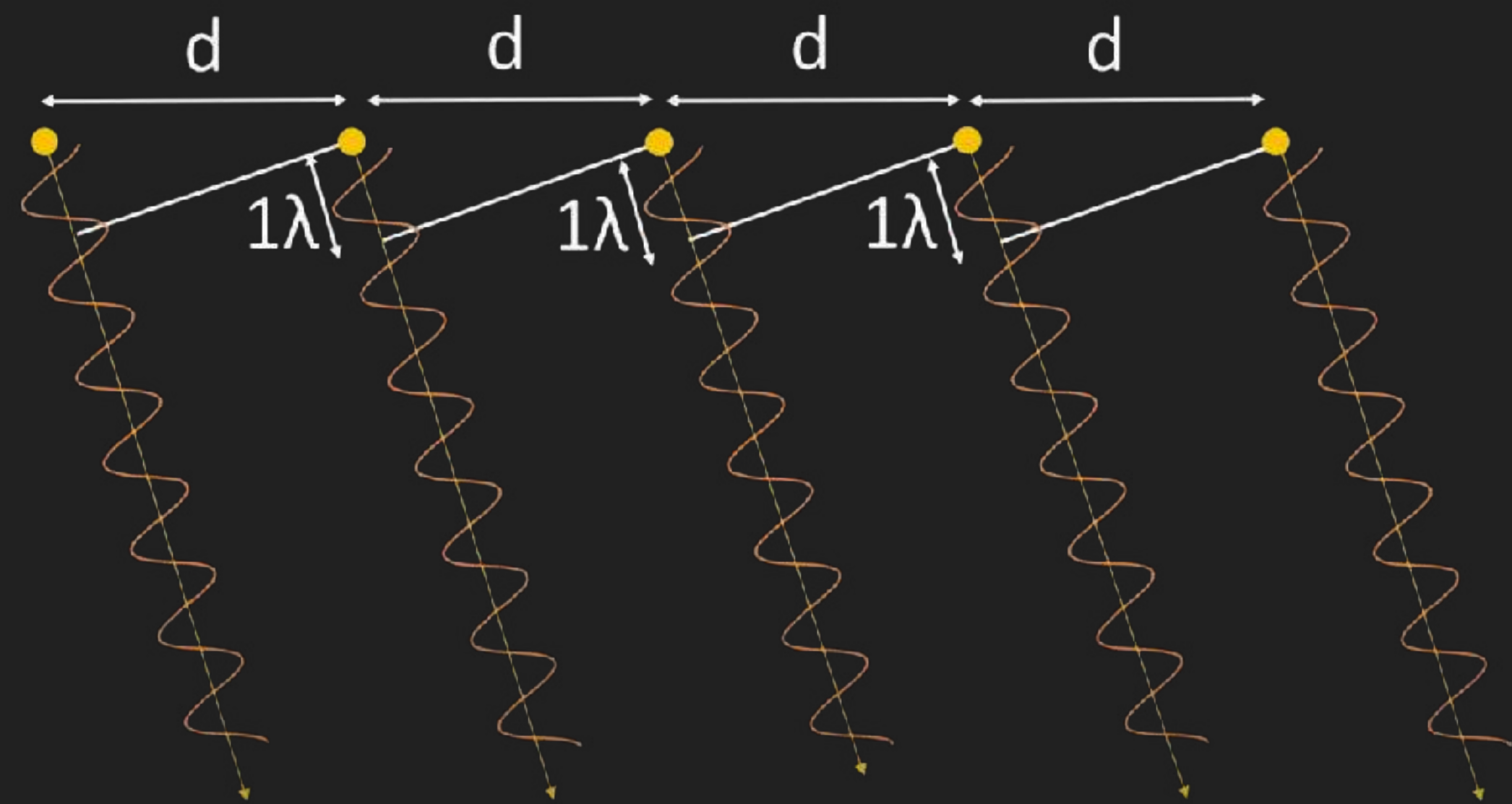
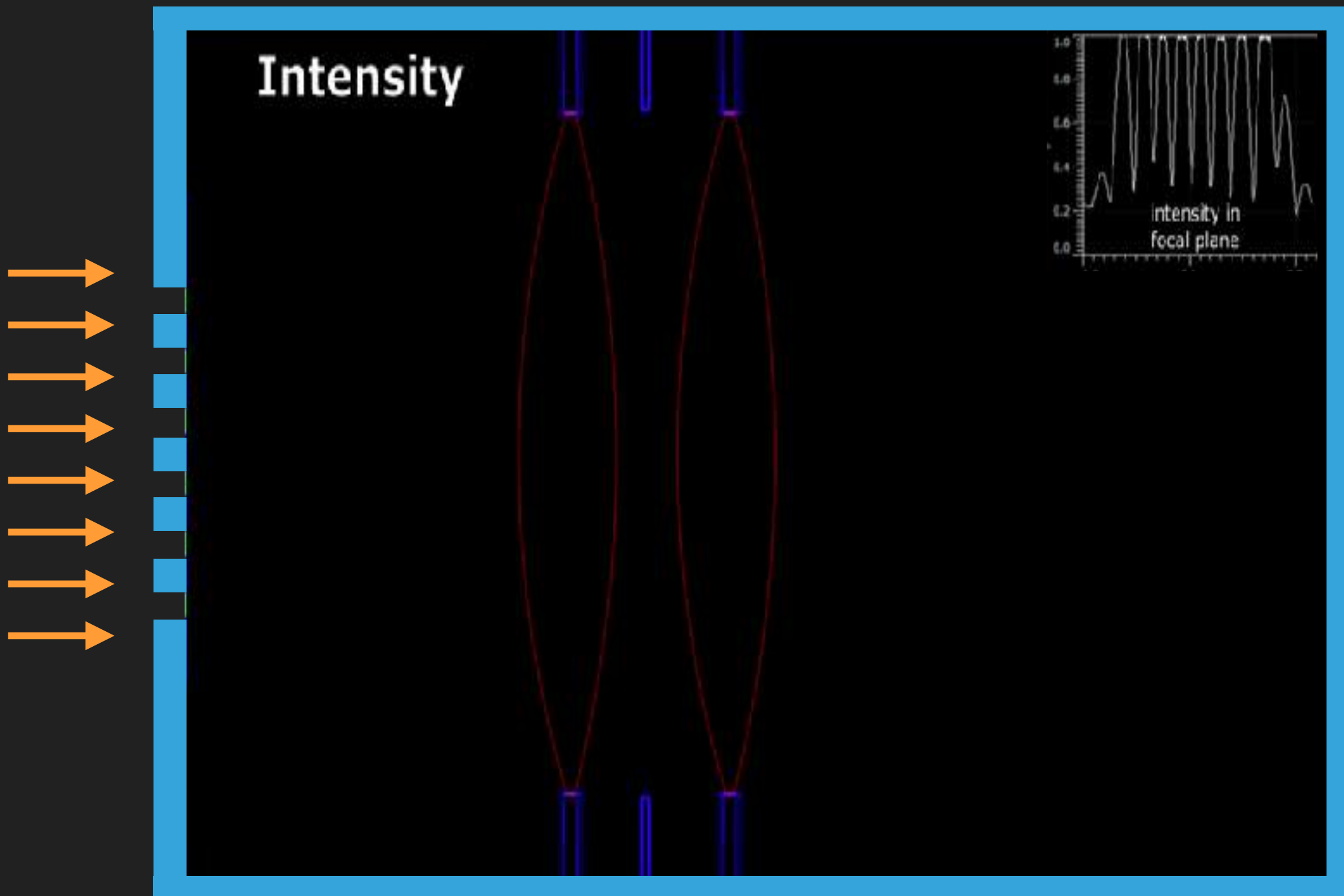


# DIFFRACTION

$k_1$  factor      Wavelength

$$\text{Smallest Feature Size} = k_1 \cdot \frac{\lambda}{\text{NA}}$$

Numerical Aperture

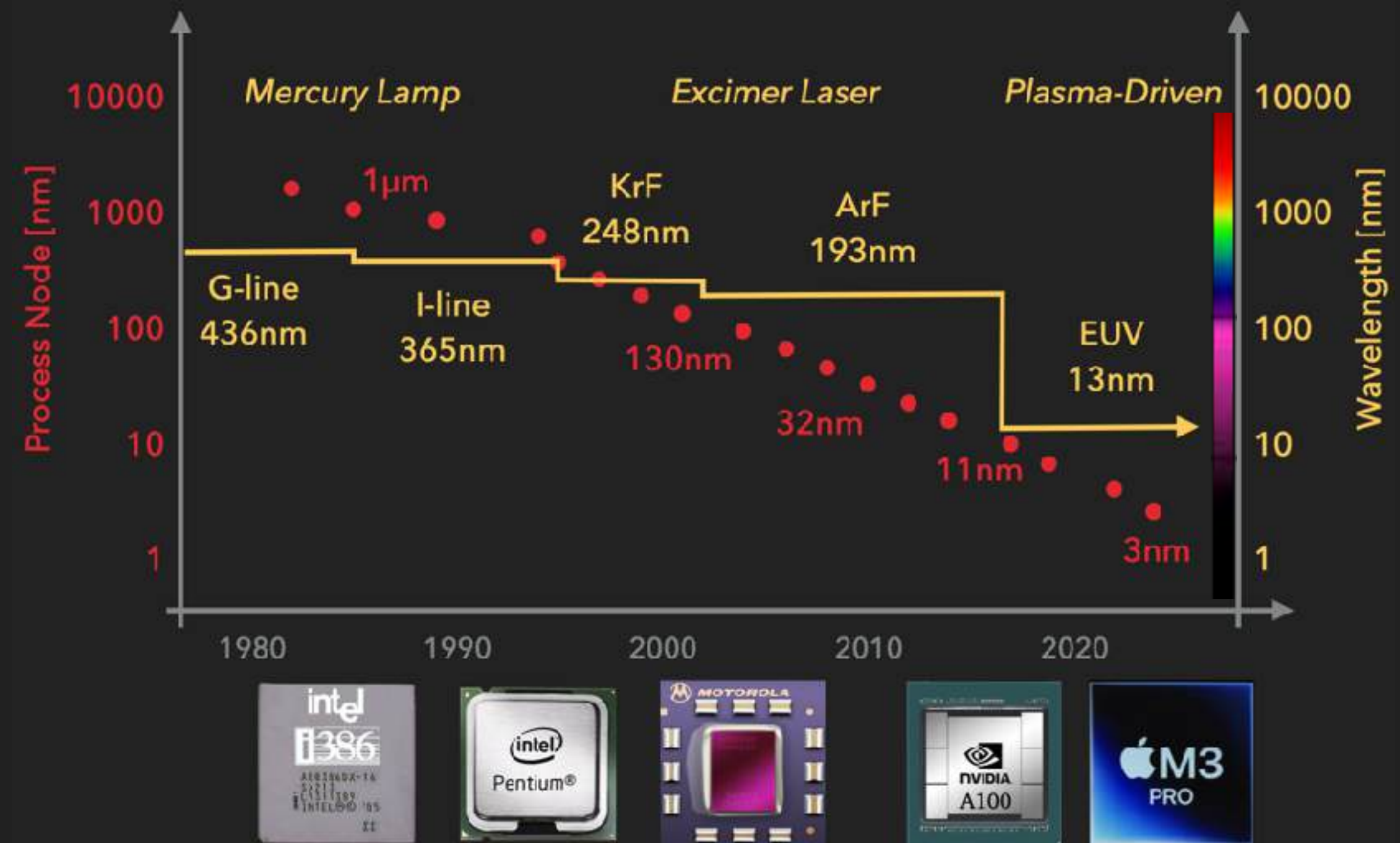
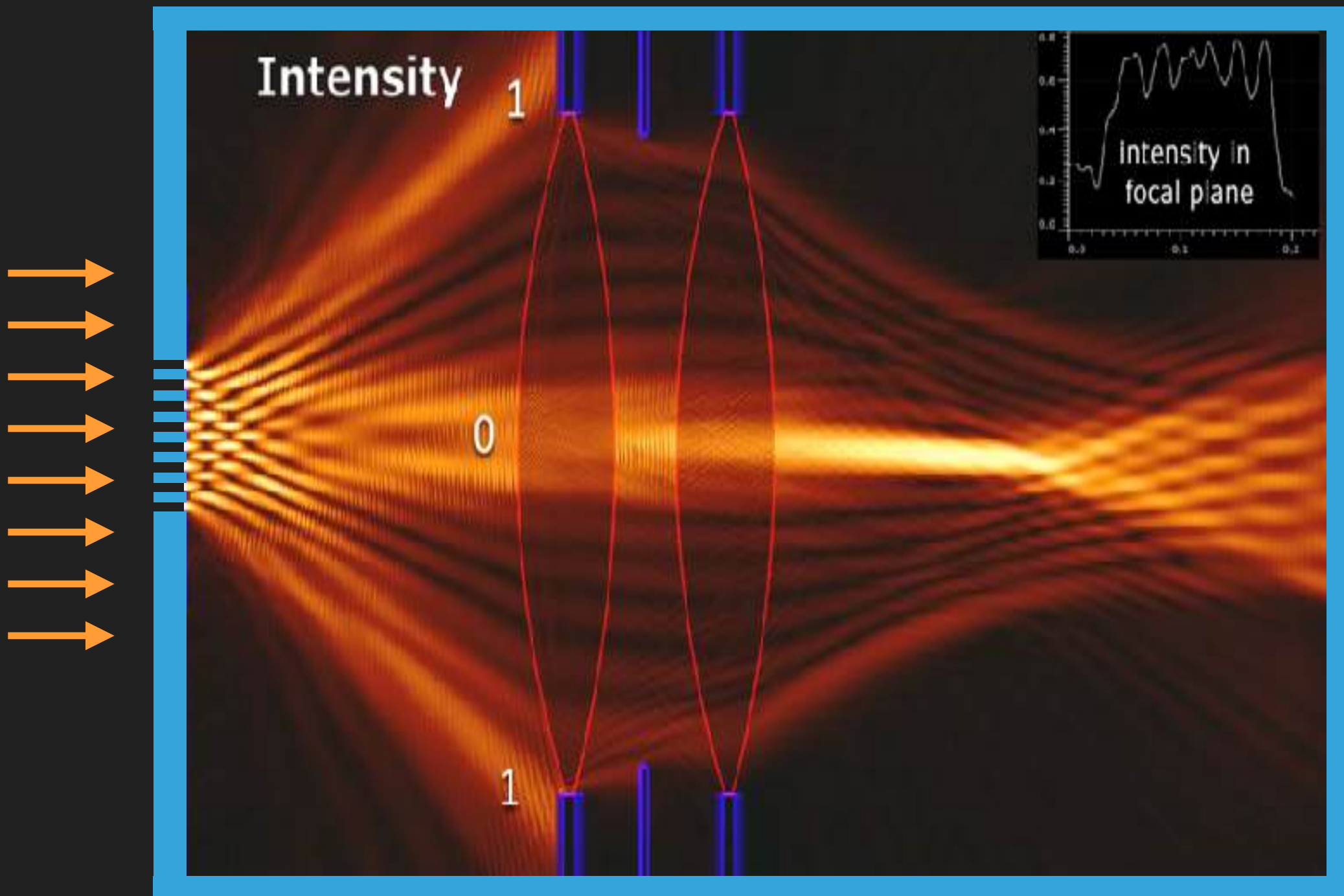


# DIFFRACTION

Illumination Tricks

Physics and Geometry

$$\text{Smallest Feature Size} = k_1 \cdot \frac{\lambda}{NA}$$

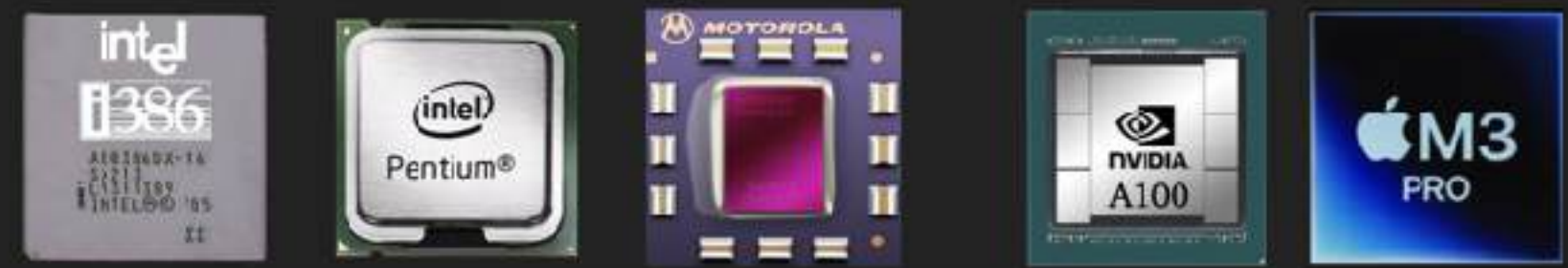
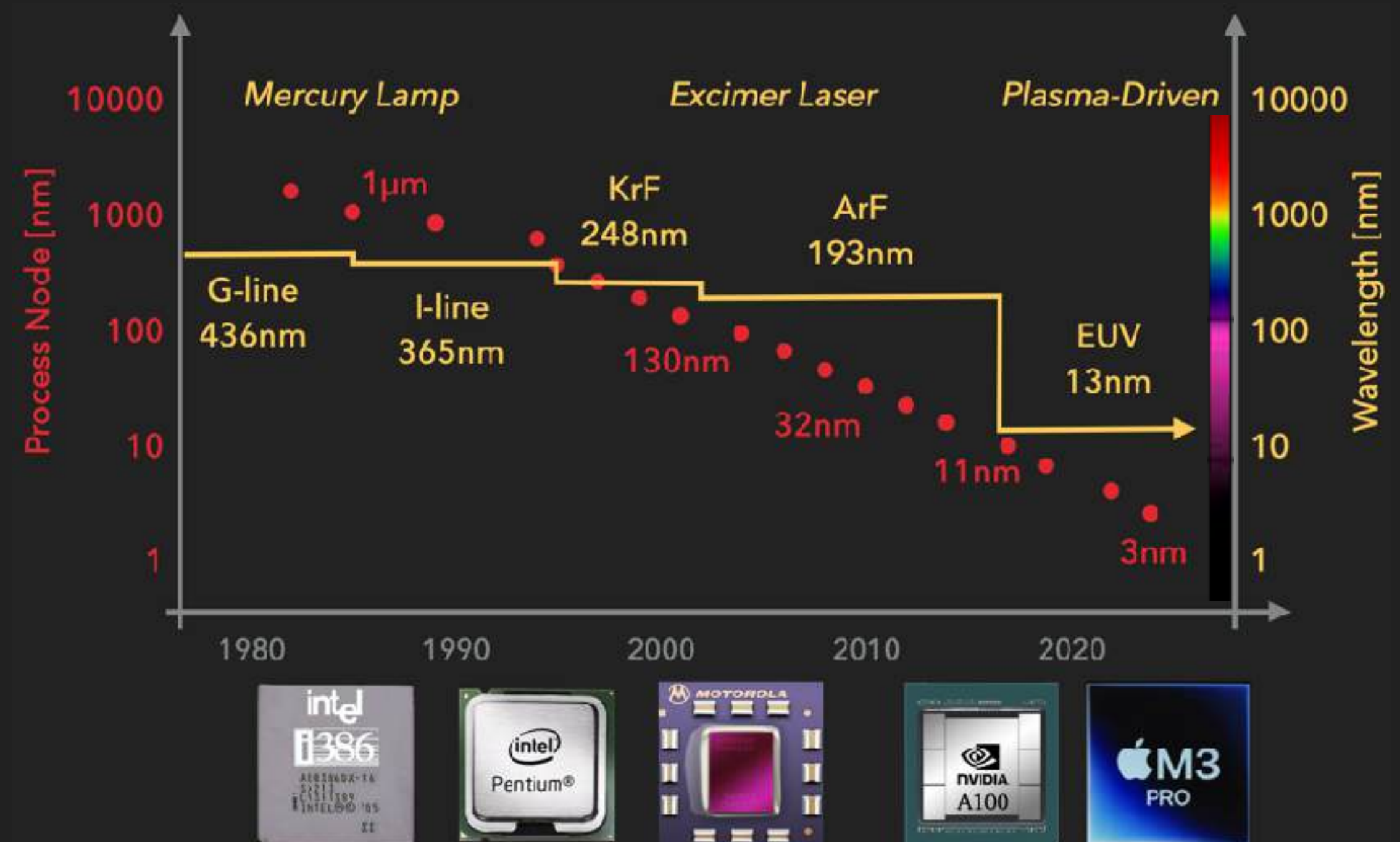
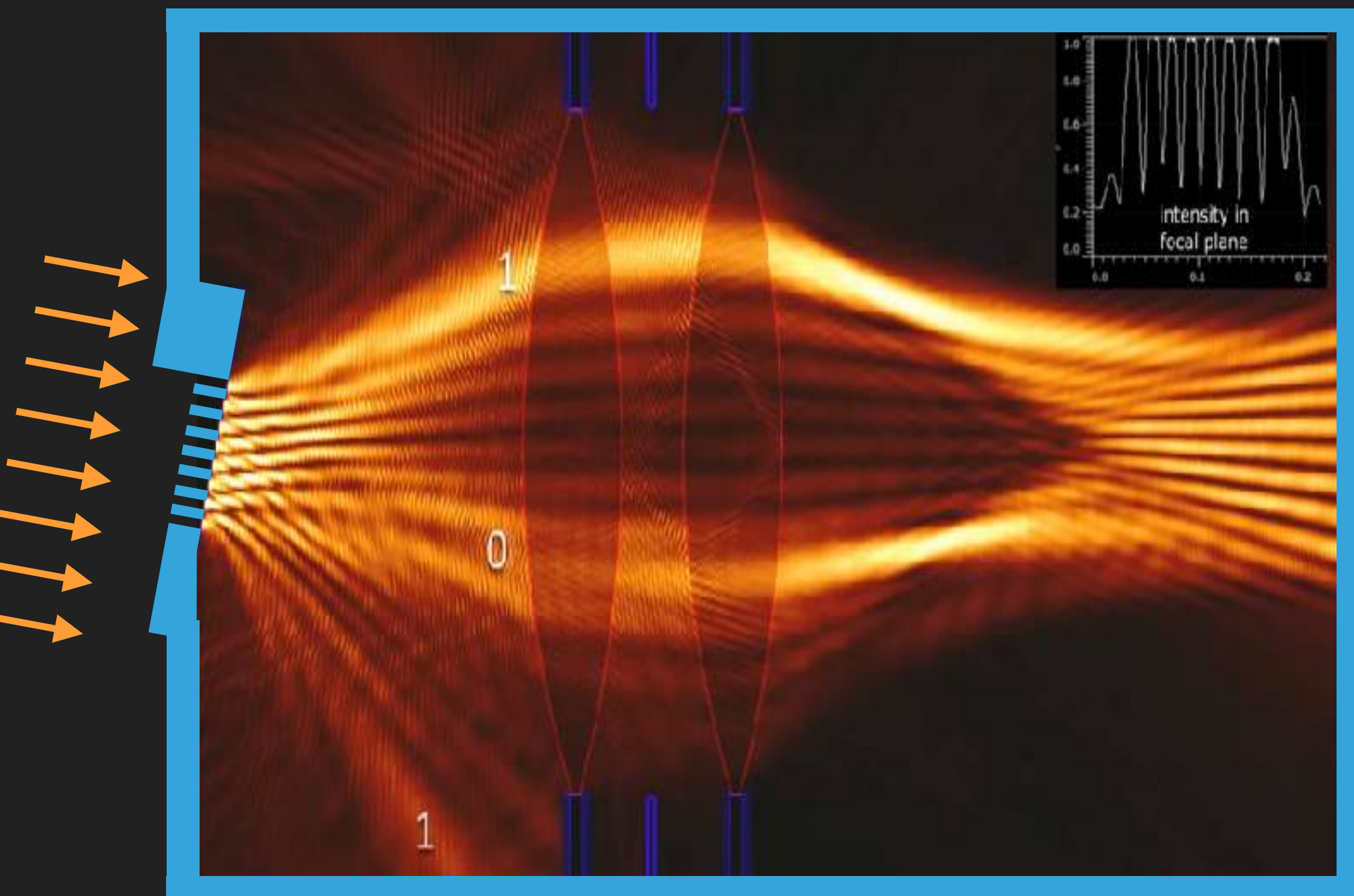


# DIFFRACTION

Illumination Tricks

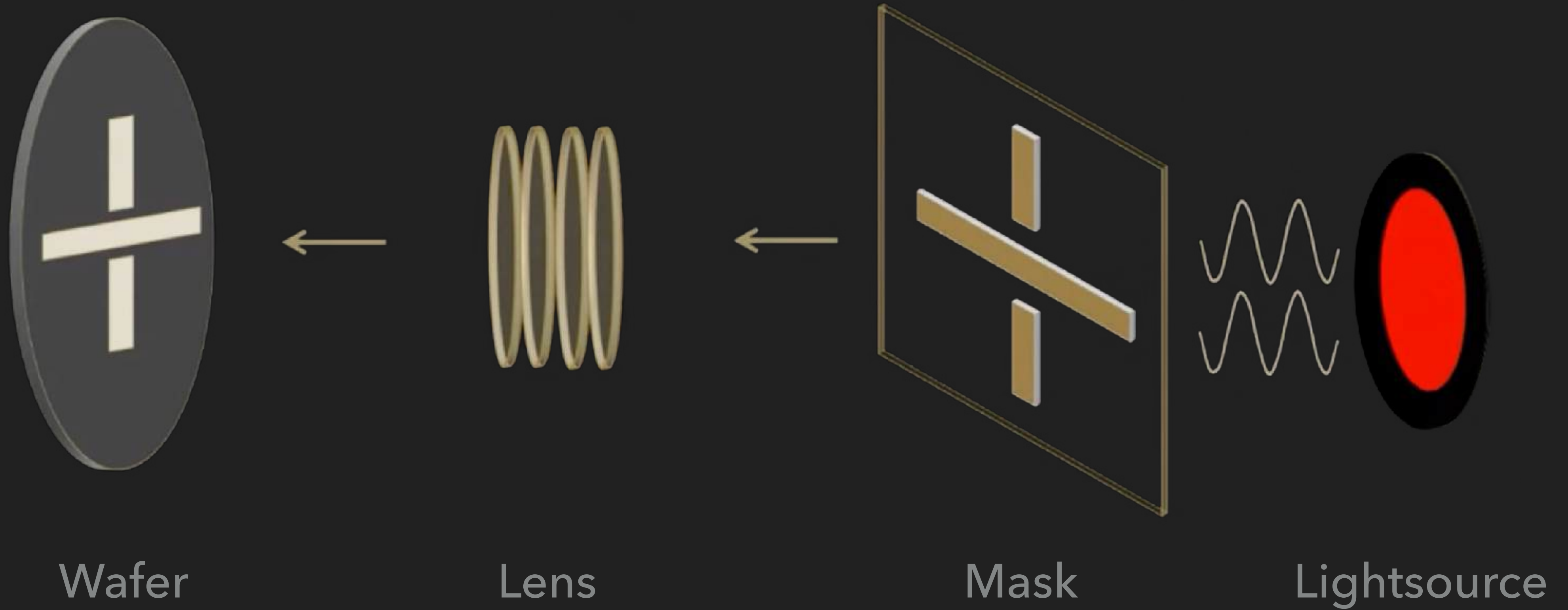
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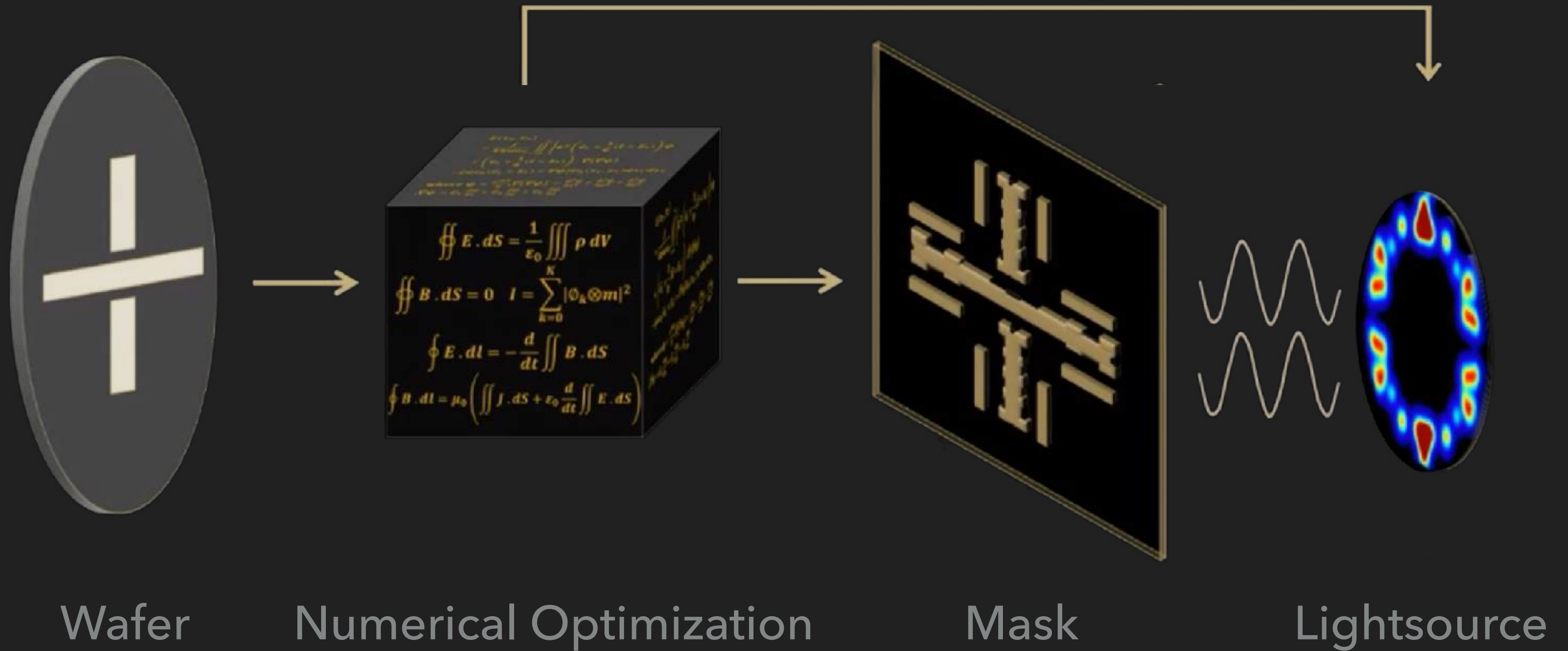




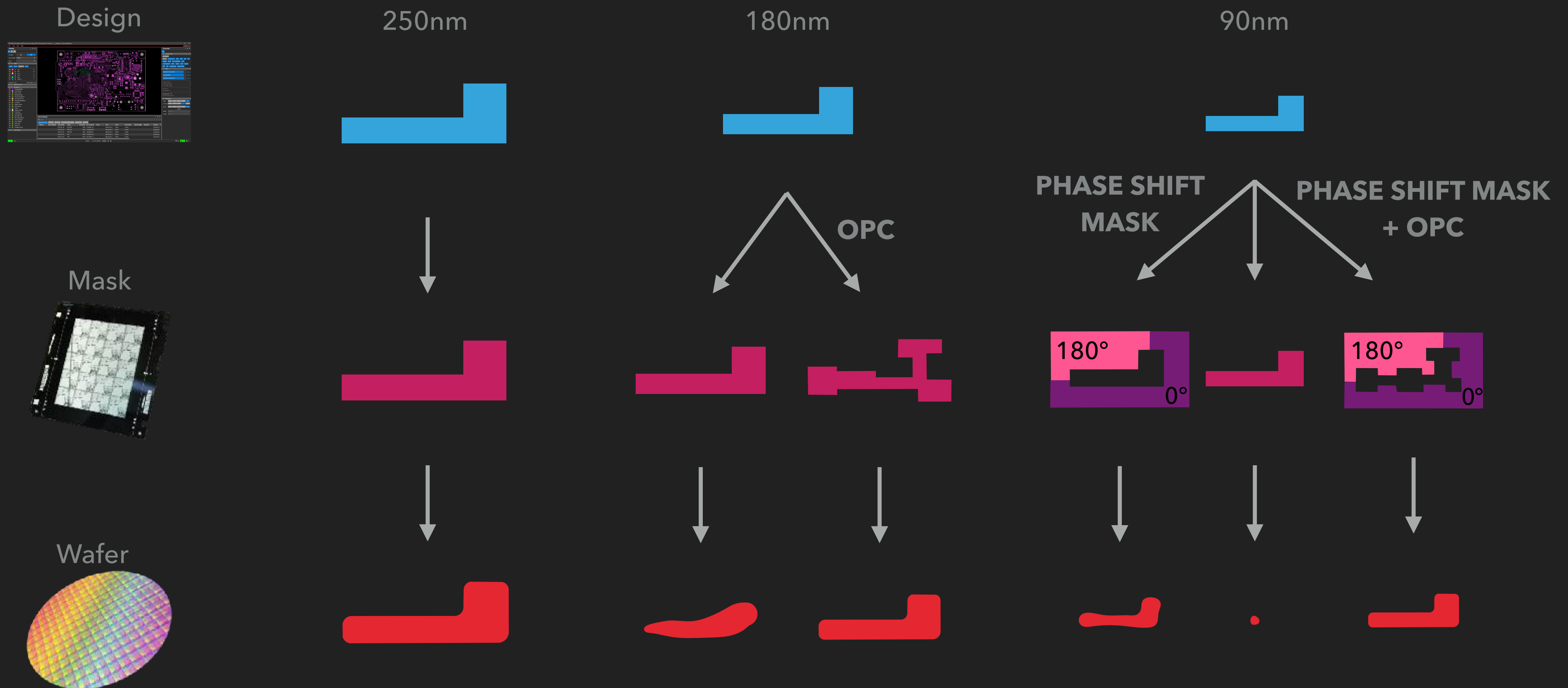
# PRINCIPLES OF PHOTOLITHOGRAPHY



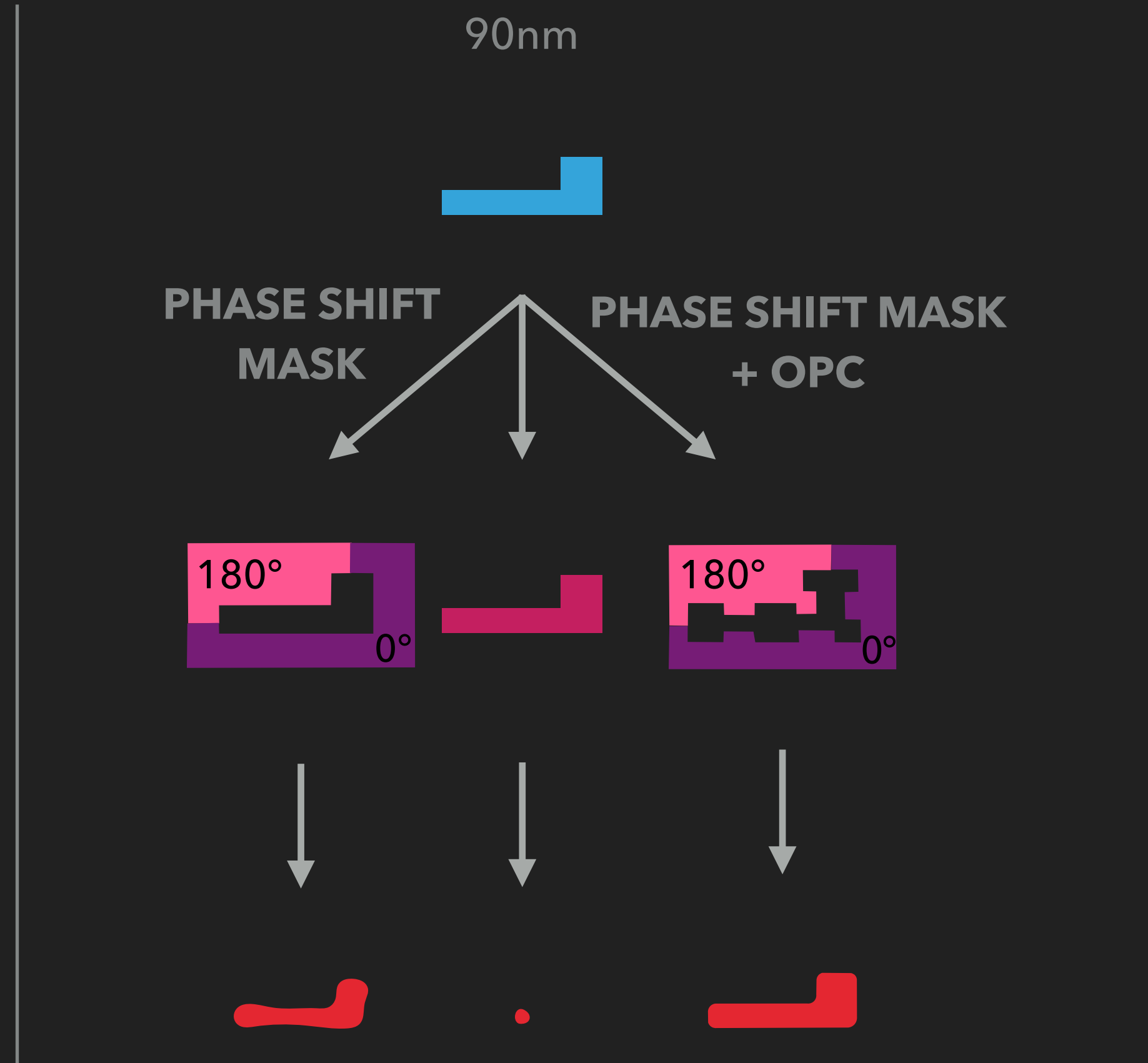
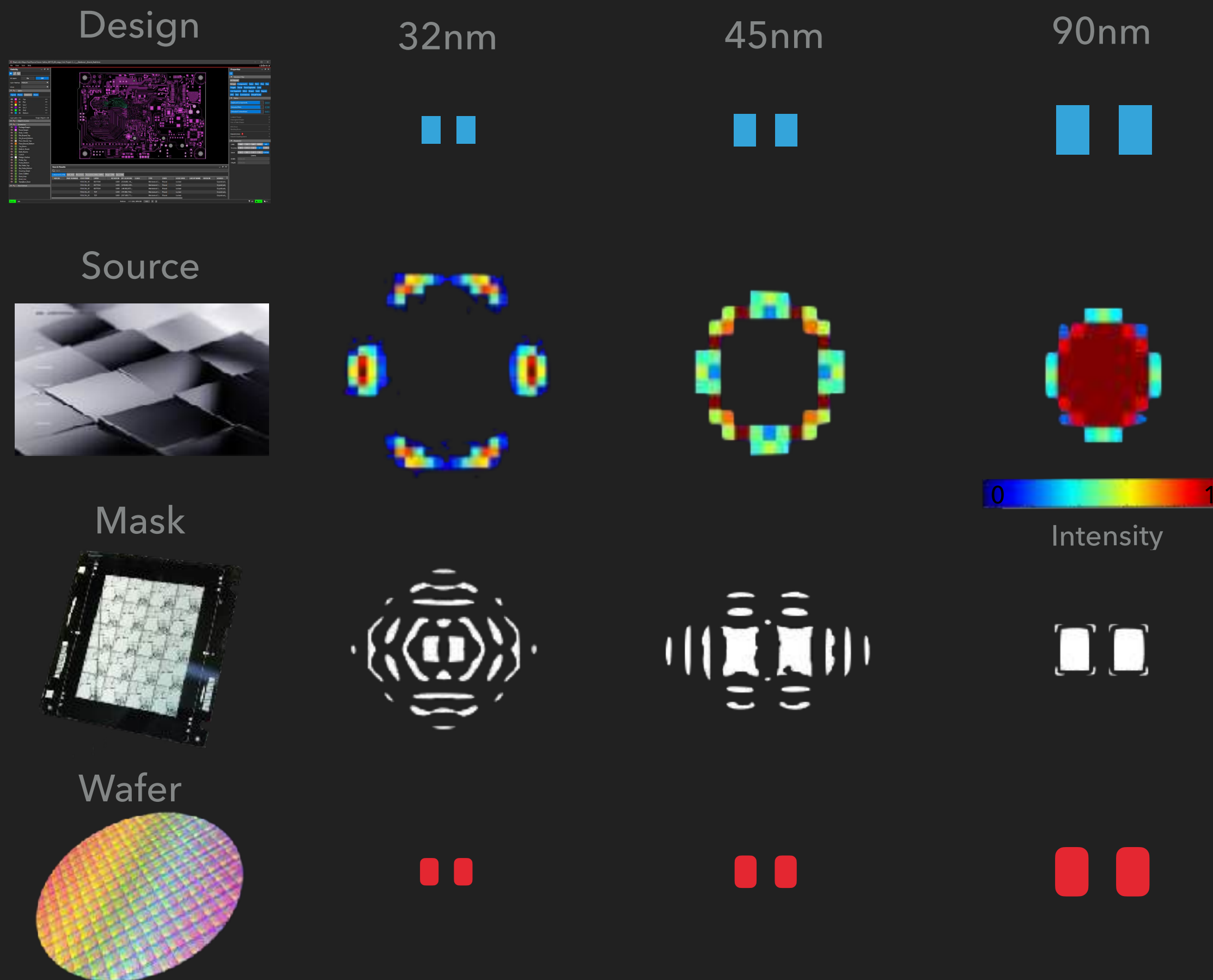
# COMPUTATIONAL LITHOGRAPHY



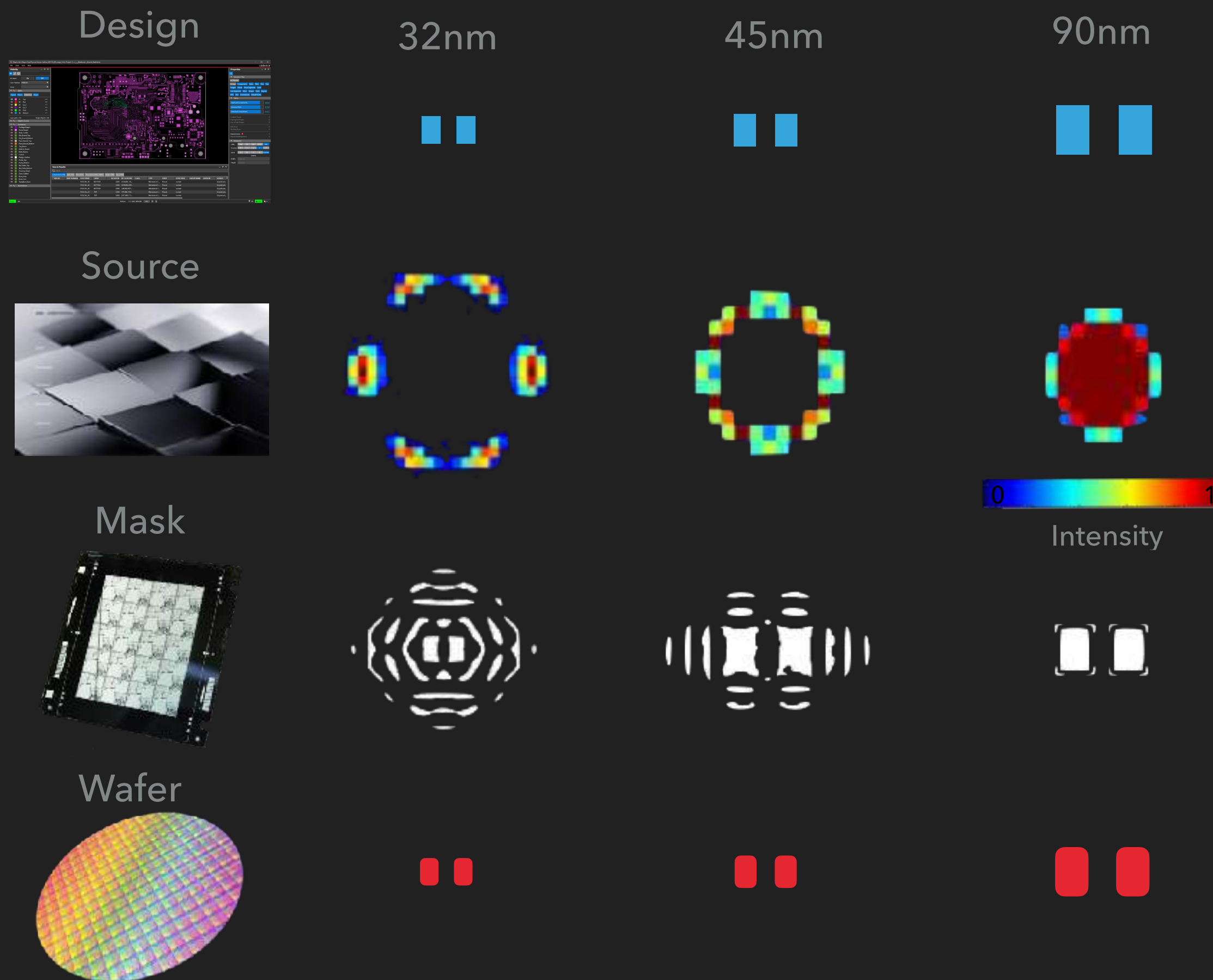
# OPTICAL PROXIMITY CORRECTION



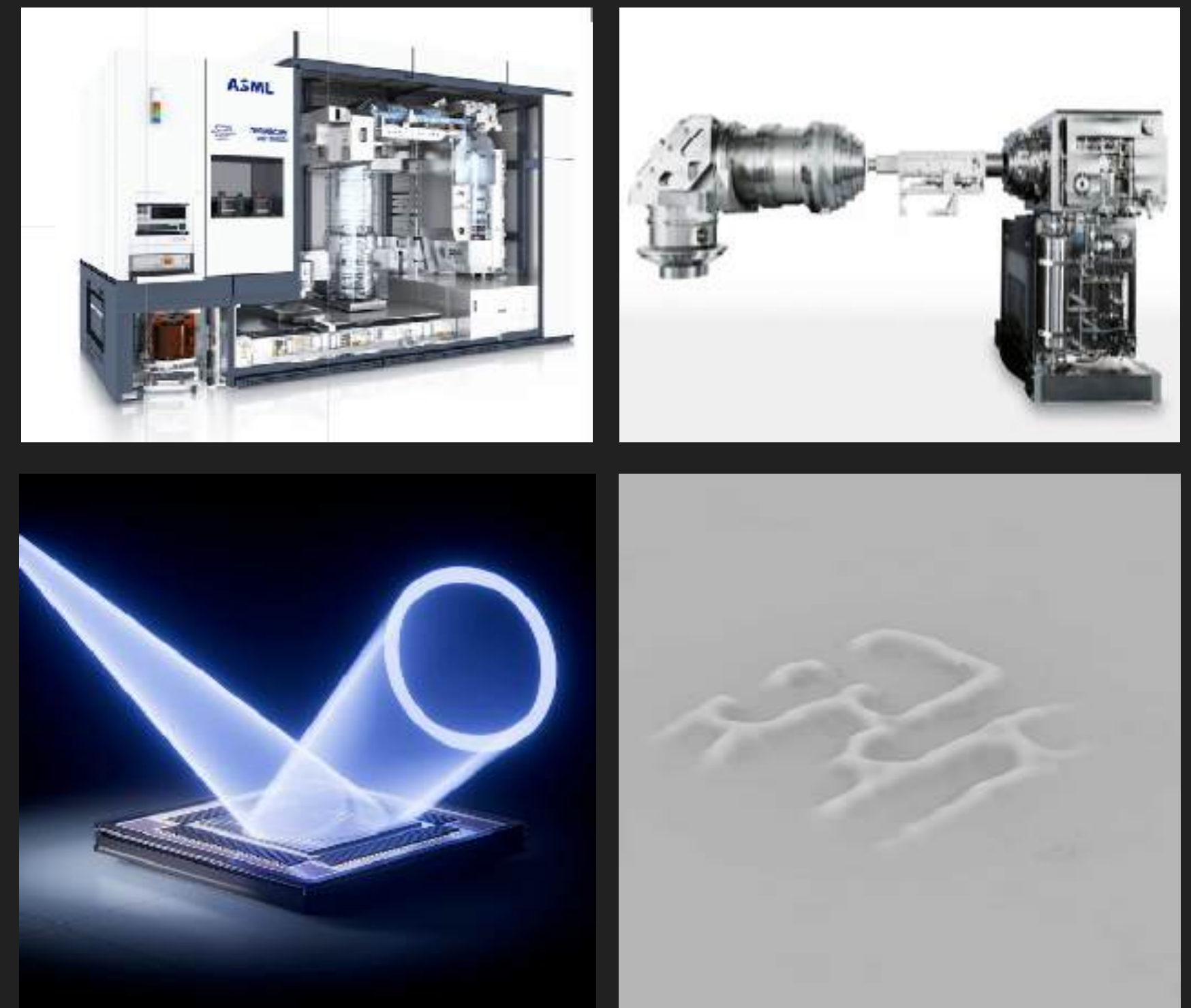
# OPTICAL PROXIMITY CORRECTION & SOURCE-MASK OPTIMIZATION



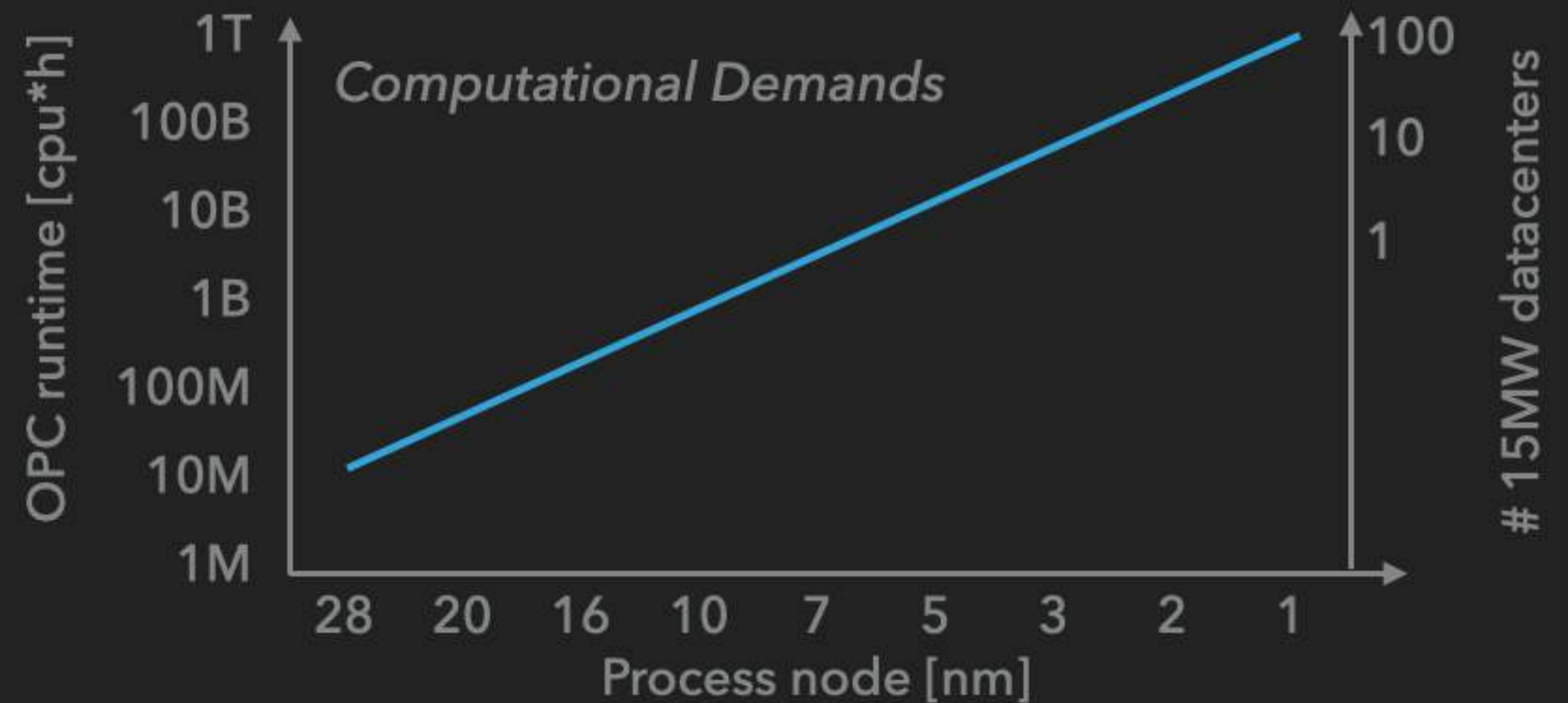
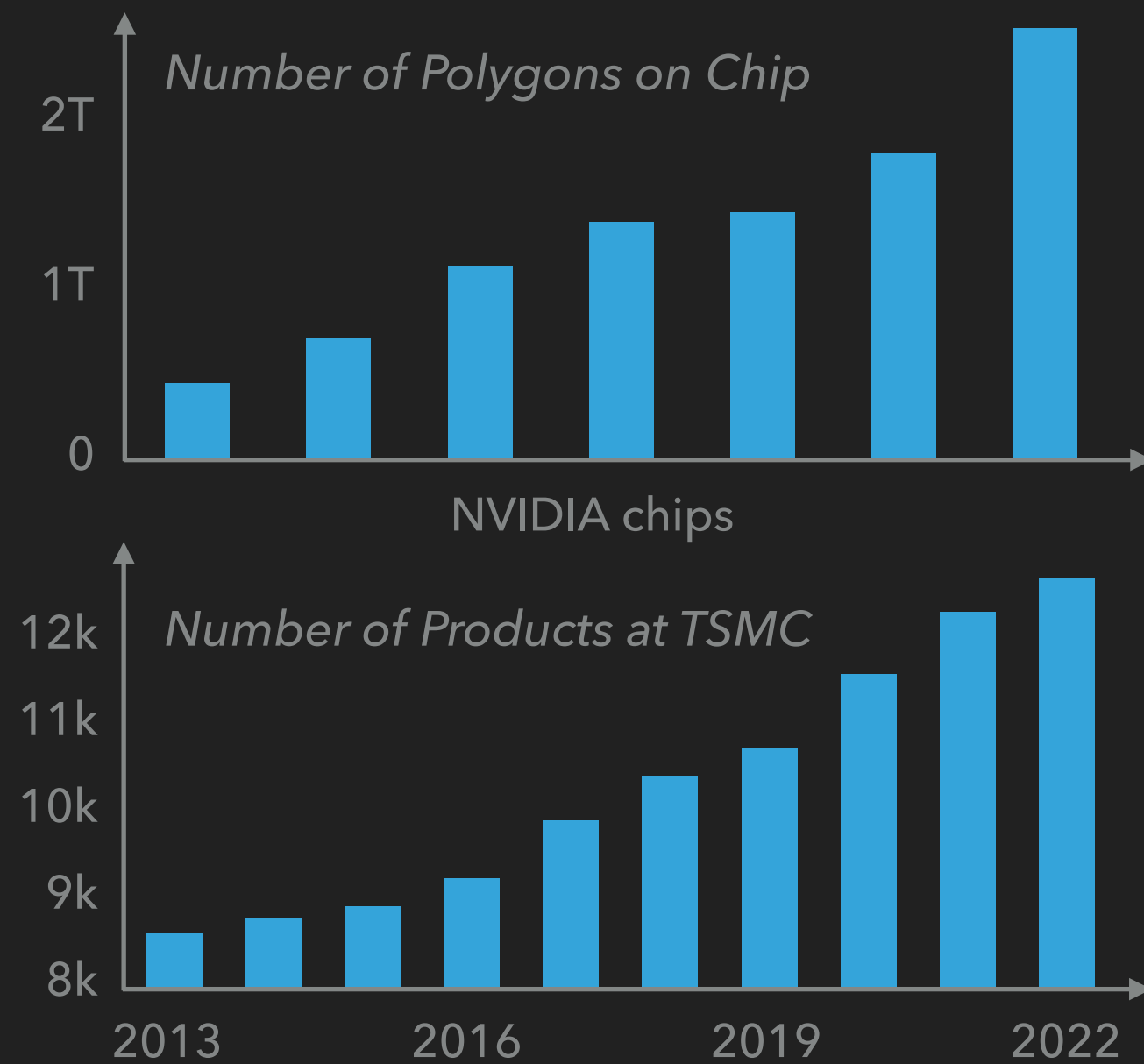
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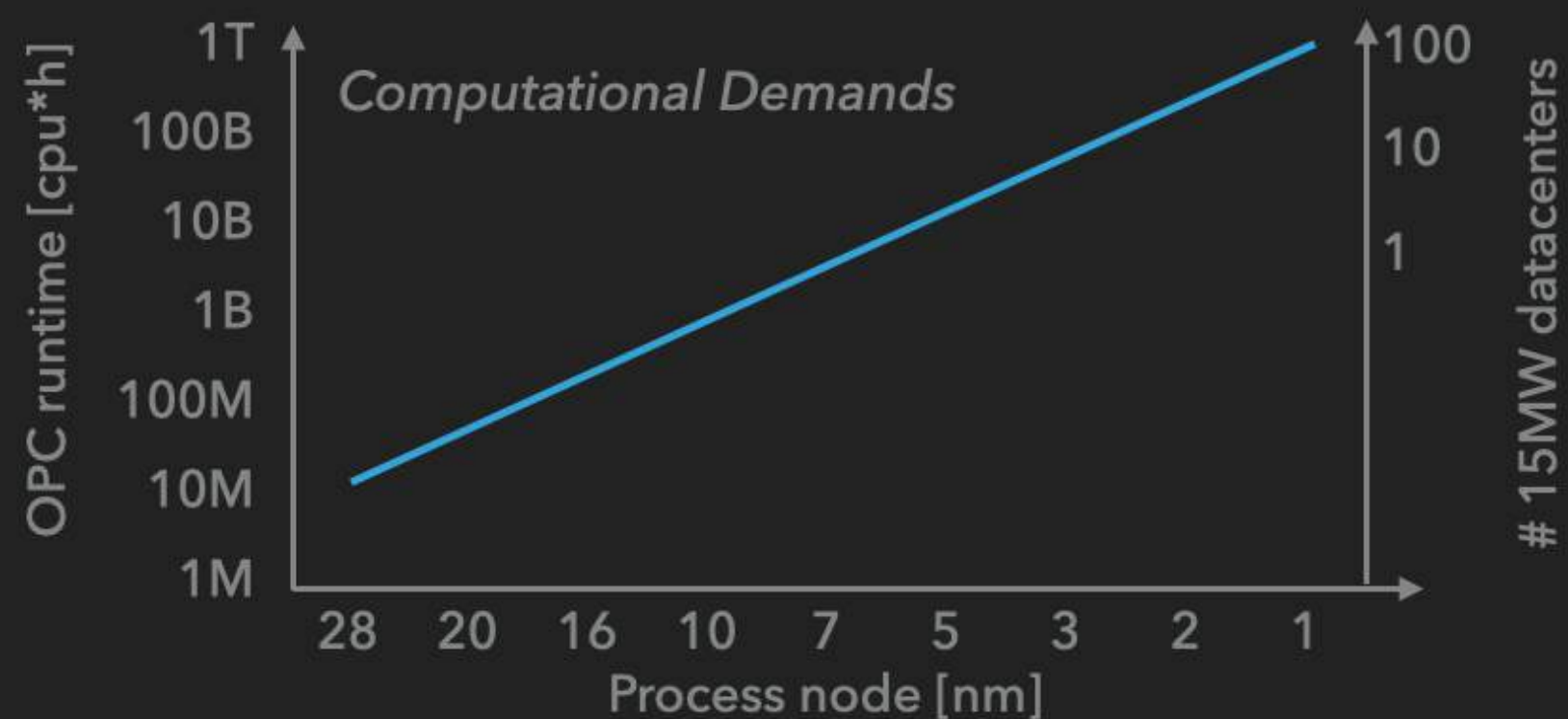
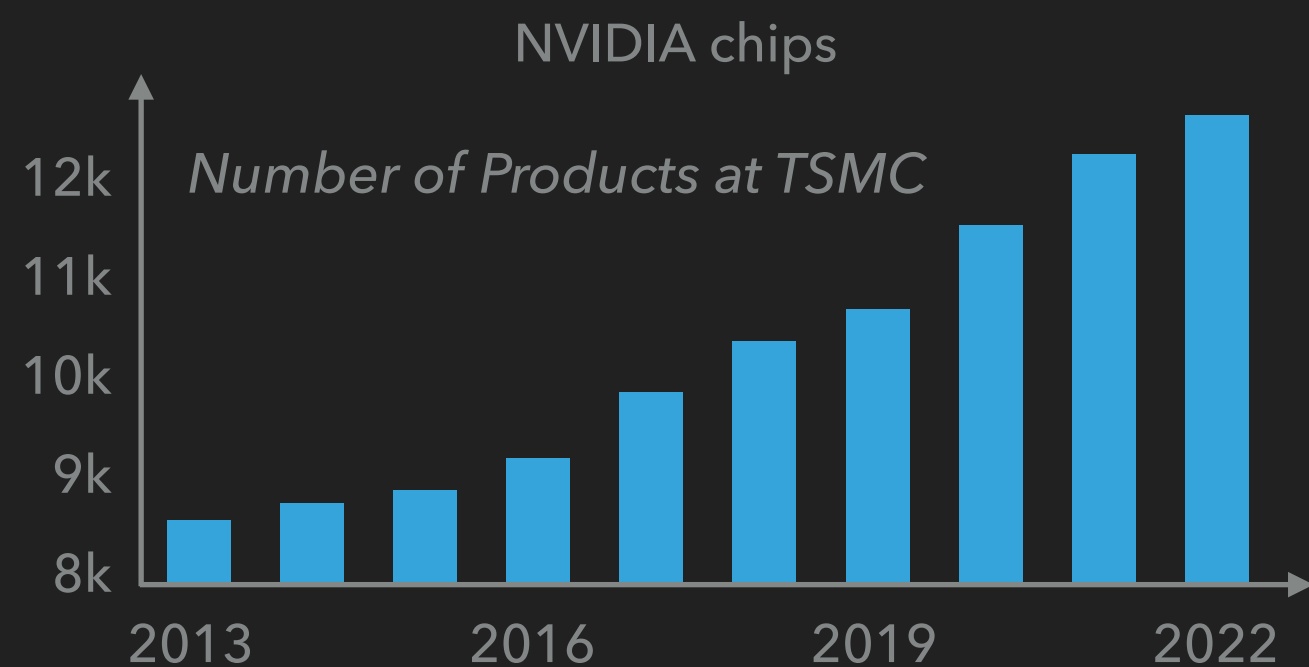
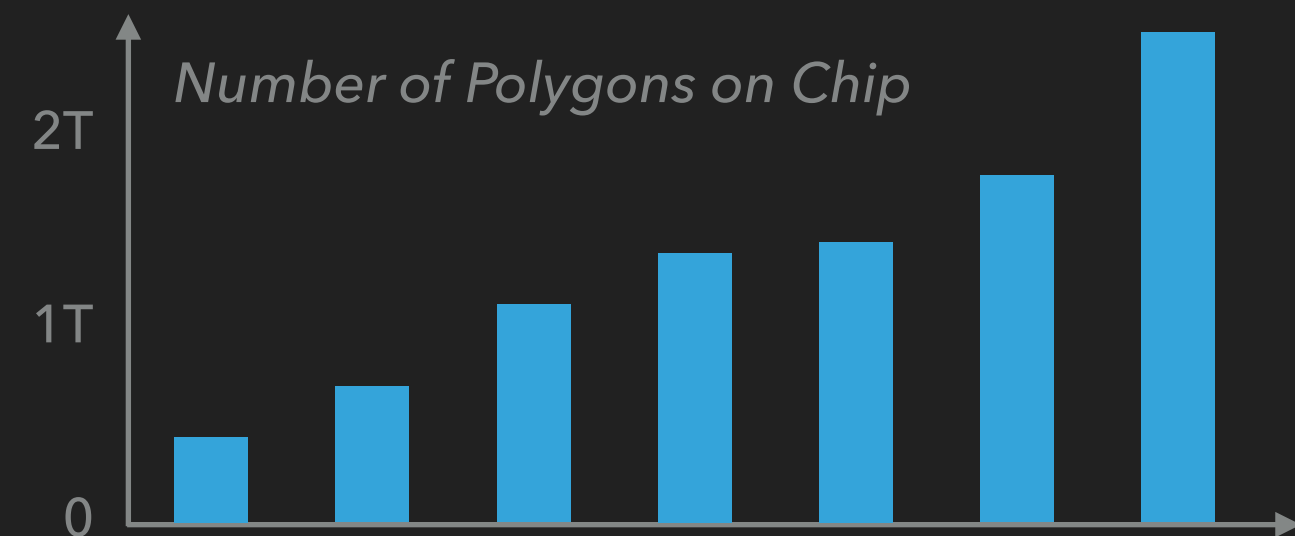
ASML TwinScan NXT:1980Di



# COMPUTATIONAL DEMANDS IN CHIP DESIGN



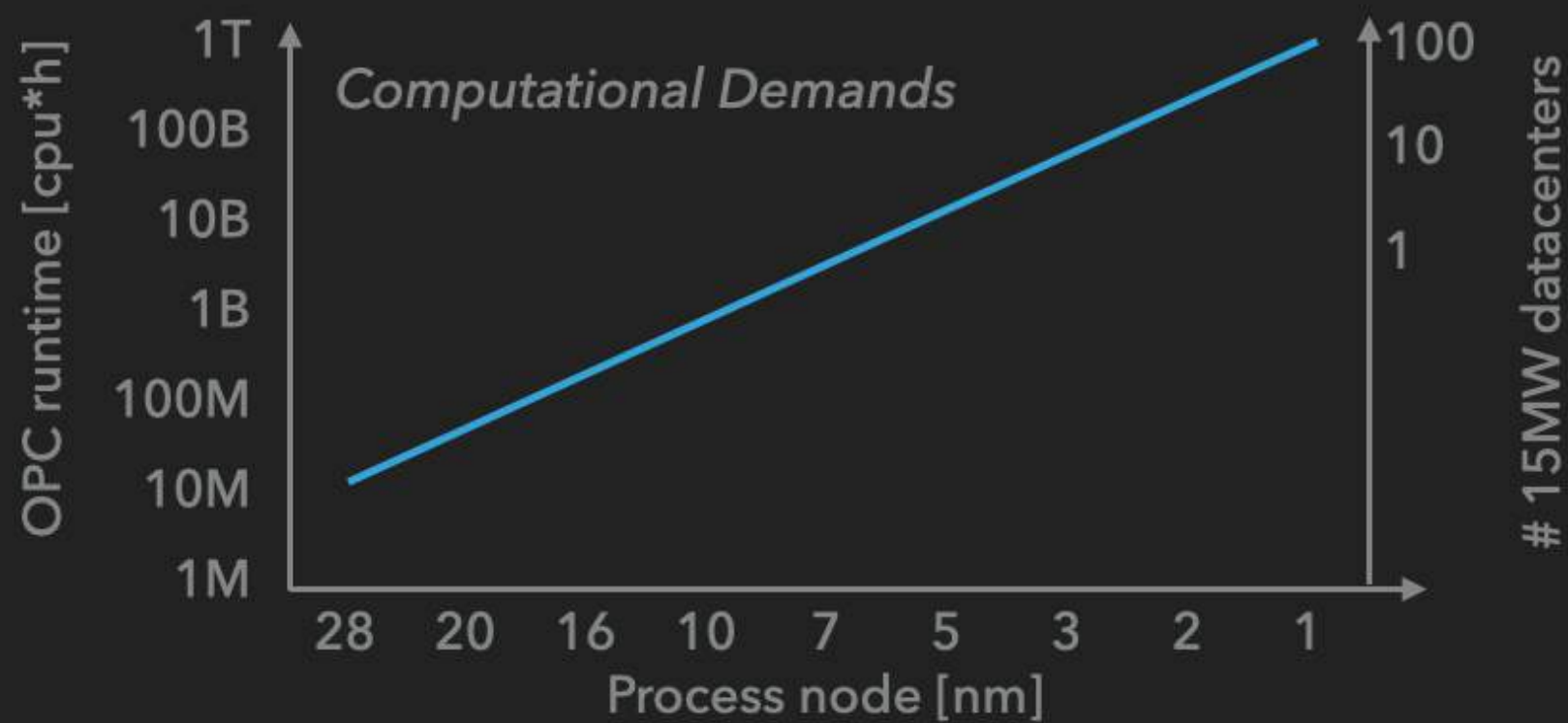
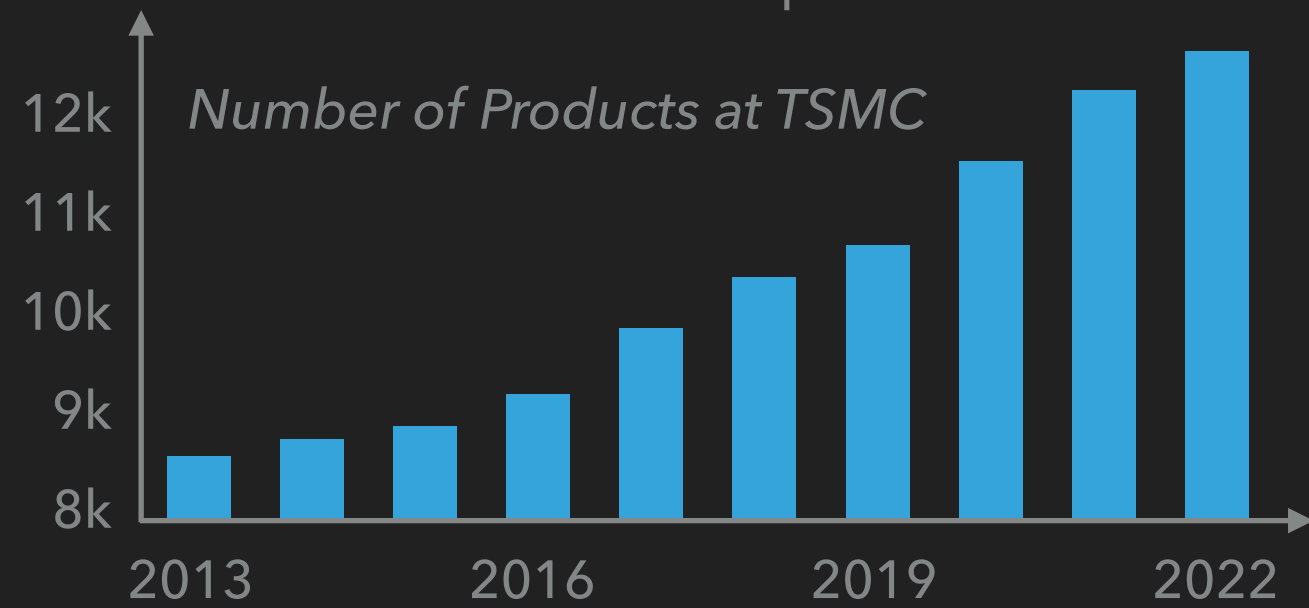
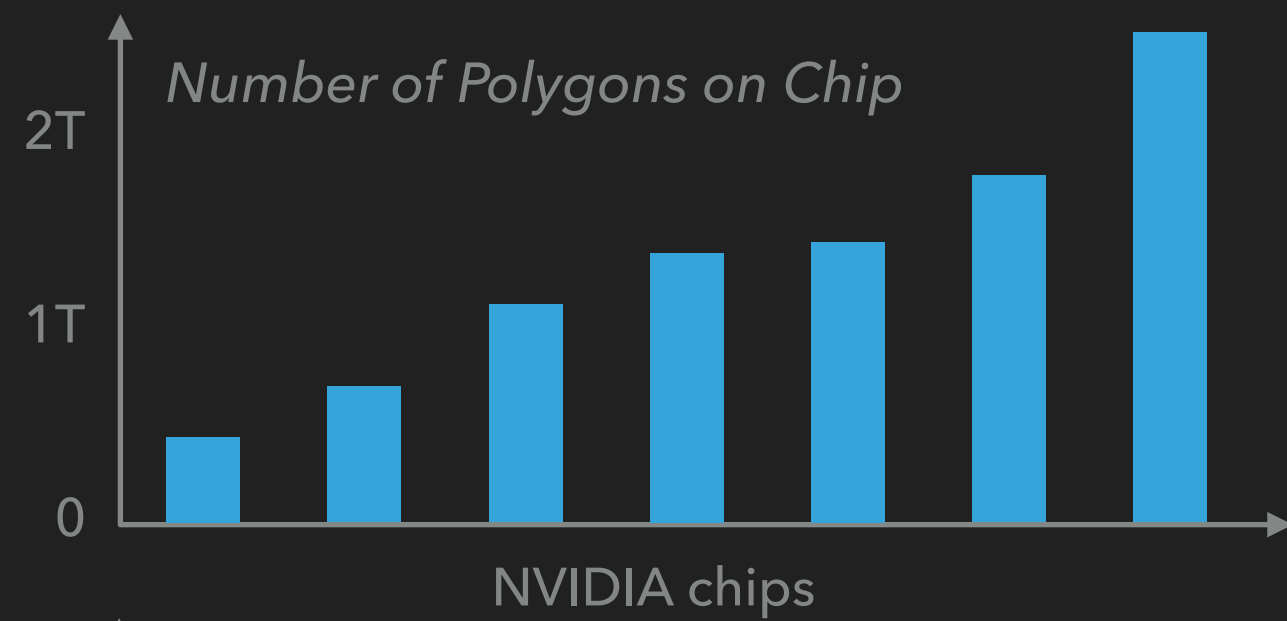
# COMPUTATIONAL DEMANDS IN CHIP DESIGN



NVIDIA cuLITHO: Parallel Algorithms for Primitive Operations

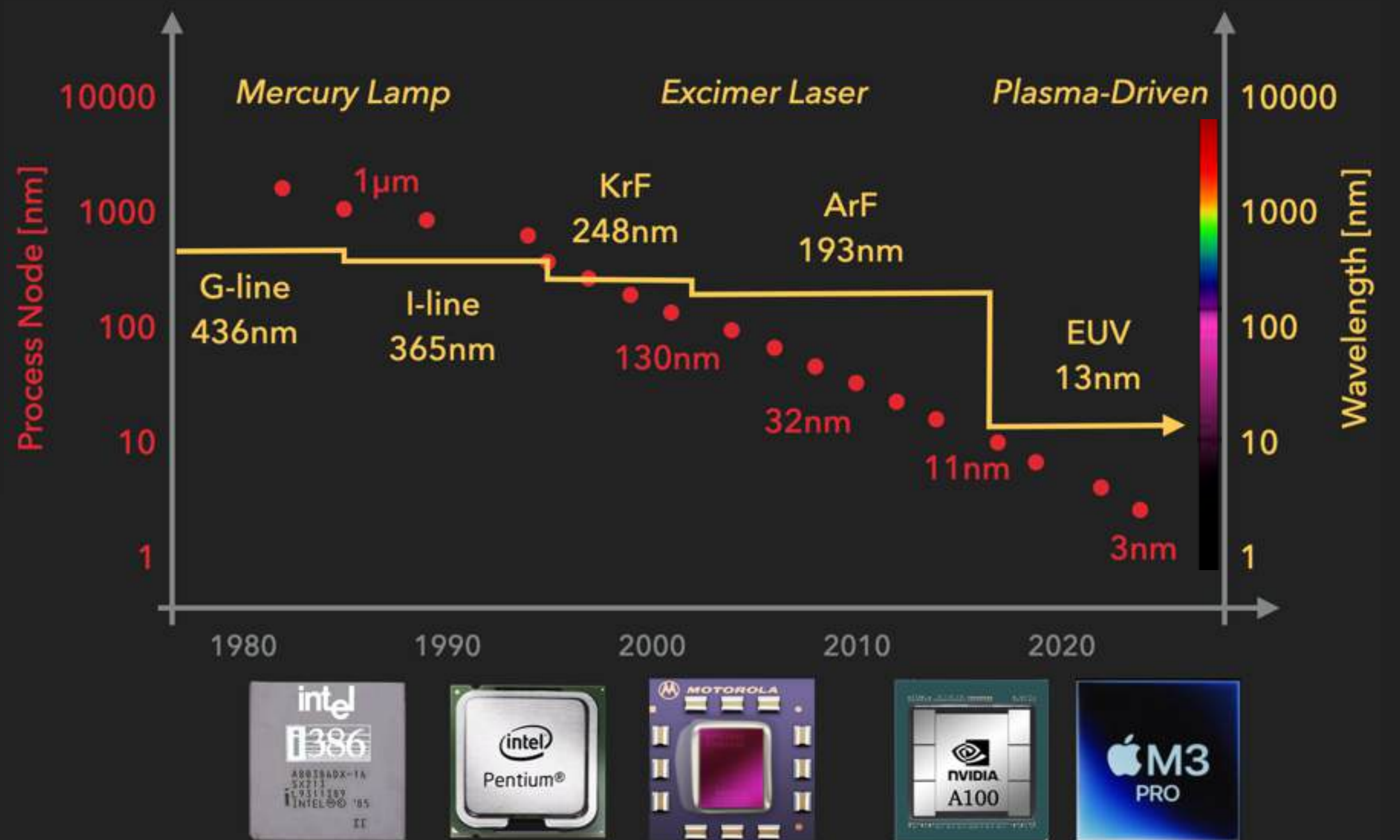
<b>Edge search</b> 	<b>Edge Visibility</b> 	<b>Edge sort &amp; group</b> 	<b>OR</b> $L1 = L1 \text{ OR } L2$ 	<b>AND</b> $L1 = L1 \text{ AND } (L2 \text{ OR } L3)$ 	<b>SUB</b> $L1 = L1 \text{ SUB } L2$ 	<b>XOR</b> $L1 = L1 \text{ XOR } L2$ 
<b>Grow/Shrink</b> $L1 = \text{Shrink}(L1, S)$ 	<b>Under/Over</b> $L1 = \text{UnderOver}(L1, -S, S)$ 	<b>Interact</b> $L1 = L1 \text{ Interact } L2$ 	<b>Inside/Outside</b> $L1 = L1 \text{ Inside } L2$ 	<b>E2E/C2C meas.</b> 	<b>Clipping</b> 	<b>Rotation</b> $L1 = \text{Rotate}(L1, 30)$ 
<b>Rasterization</b> 	<b>Contour extraction</b> 	<b>Contour dist. field</b> 	<b>Contour visibility</b> 	<b>Low-pass filters</b> 	<b>TCC convolutions</b> 	<b>Image slope</b> 
<b>Image Laplacian</b> 	<b>Image curvature</b> 	<b>Image local min/max</b> 	<b>Bridge/Pinch meas.</b> 	<b>CD/EPE meas.</b> $\text{EPE}(L1, C)$ 	<b>Data manipulation</b> 	

# COMPUTATIONAL DEMANDS IN CHIP DESIGN



Smallest Feature Size =  $k_1 \cdot \frac{\lambda}{NA}$

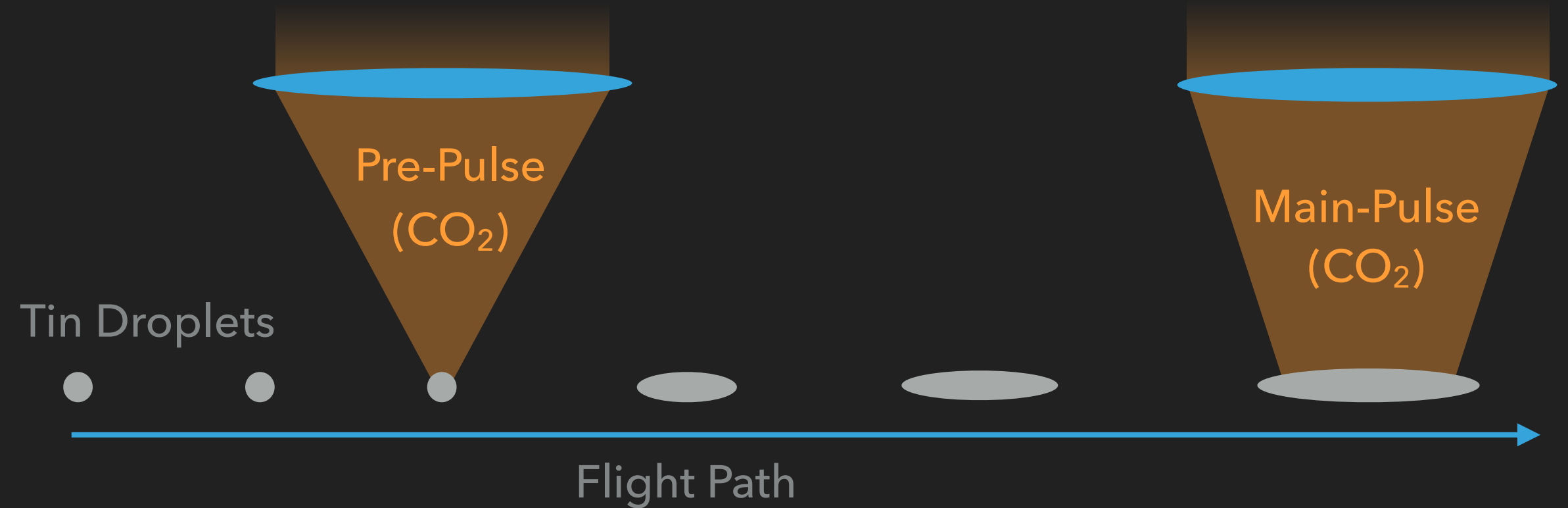
Labels: Illumination Tricks (pointing to  $k_1$ ), Physics and Geometry (pointing to  $\lambda$  and  $NA$ )





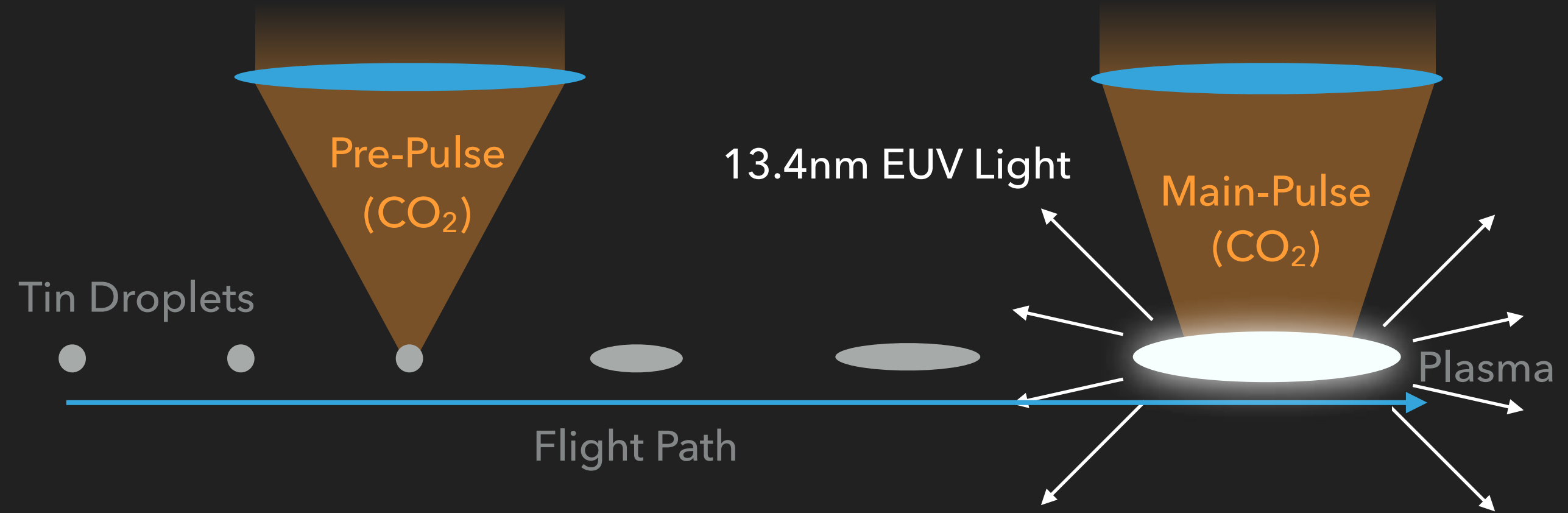
# THE EUV LIGHT SOURCE

- ▶ Droplet Size: 27  $\mu\text{m}$
- ▶ Droplet Frequency: 50kHz
- ▶ Droplet Speed: 280km/h
- ▶ Droplet Distance: 1.6mm
- ▶ CO2 Drive Laser Power: 30kW



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- ▶ Droplet Speed: 280km/h
- ▶ Droplet Distance: 1.6mm
- ▶ CO2 Drive Laser Power: 30kW
- ▶ Plasma Temperature: 220k K
- ▶ EUV Wavelength: 13.4nm



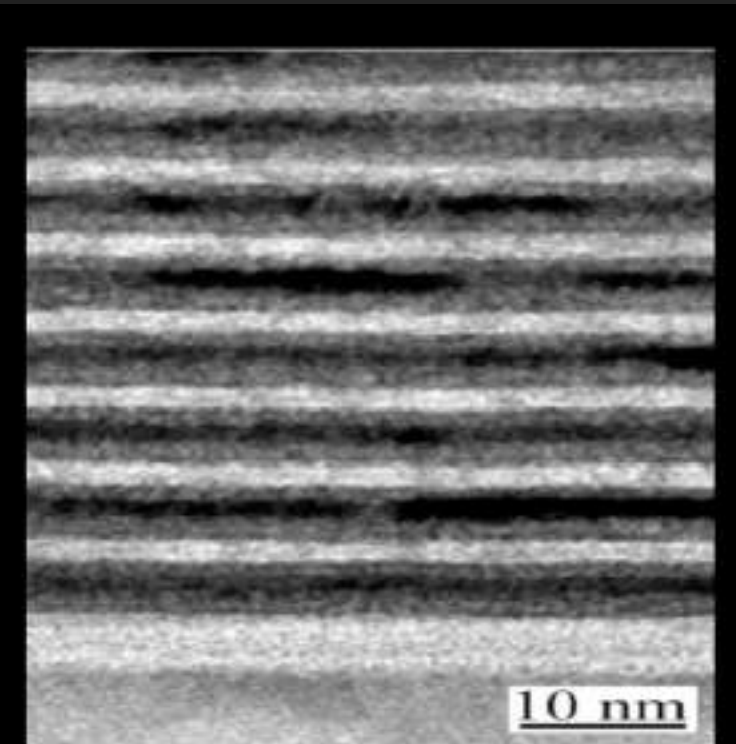
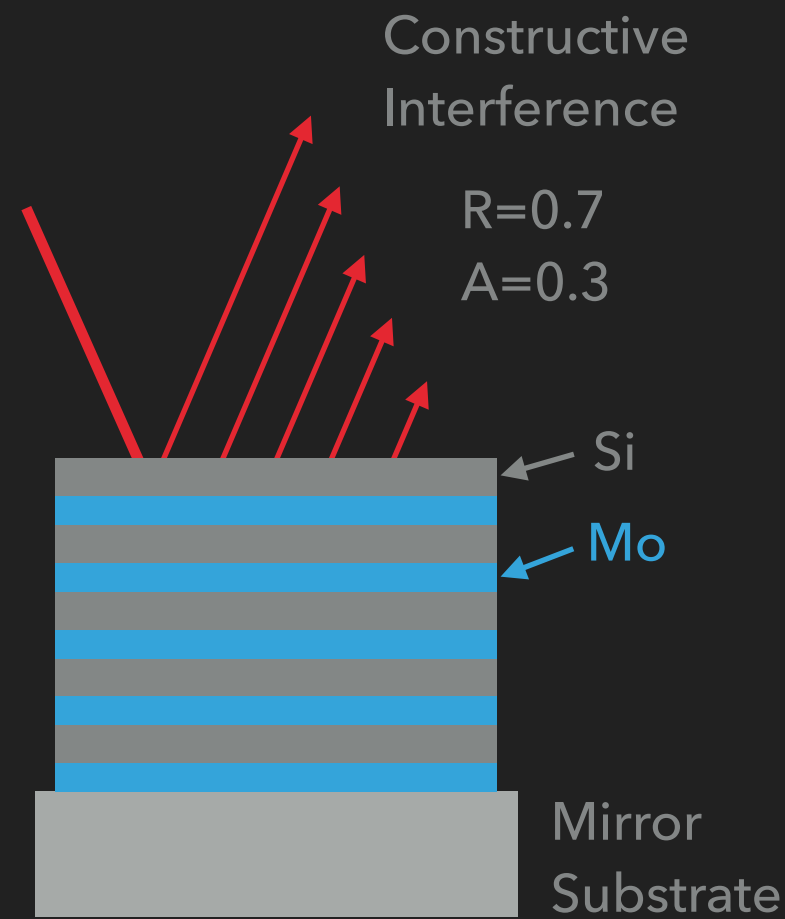
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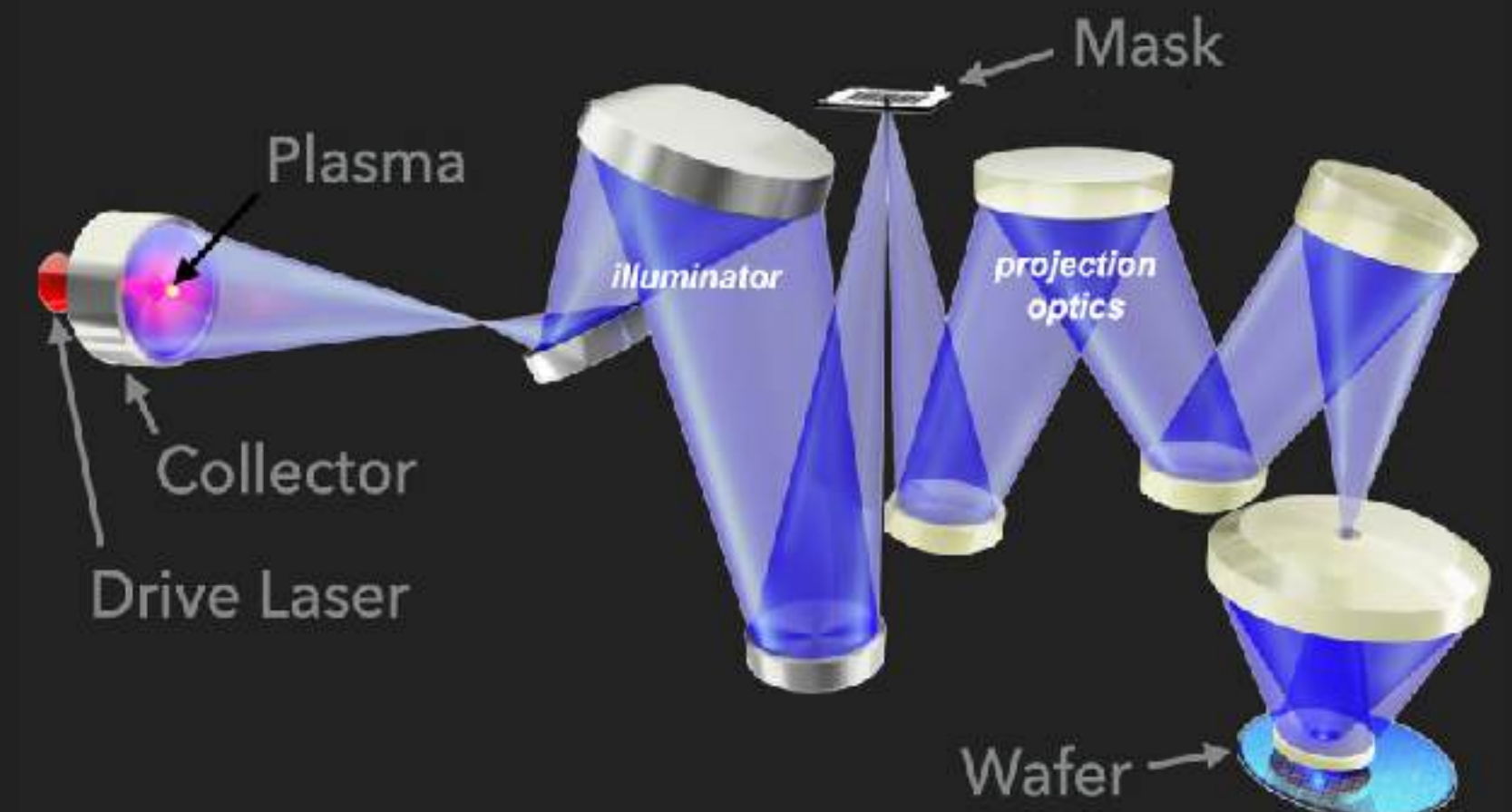


# EUV MIRRORS BY ZEISS

- ▶ EUV gets absorbed by matter
- ▶ Lenses => Bragg Mirrors
- ▶ Single atom layer deposition

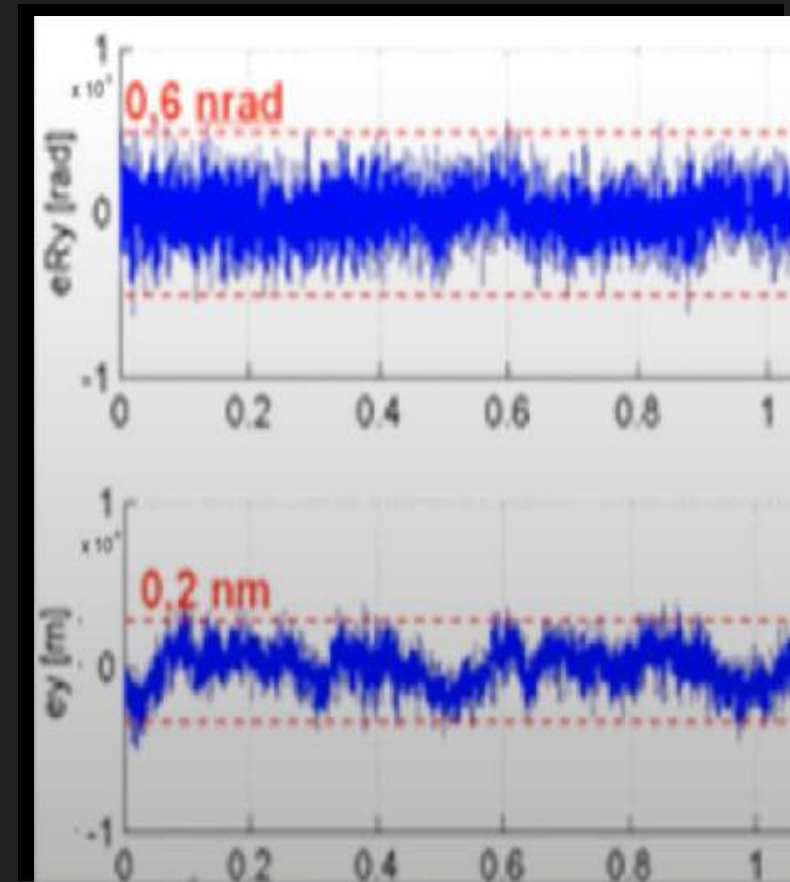
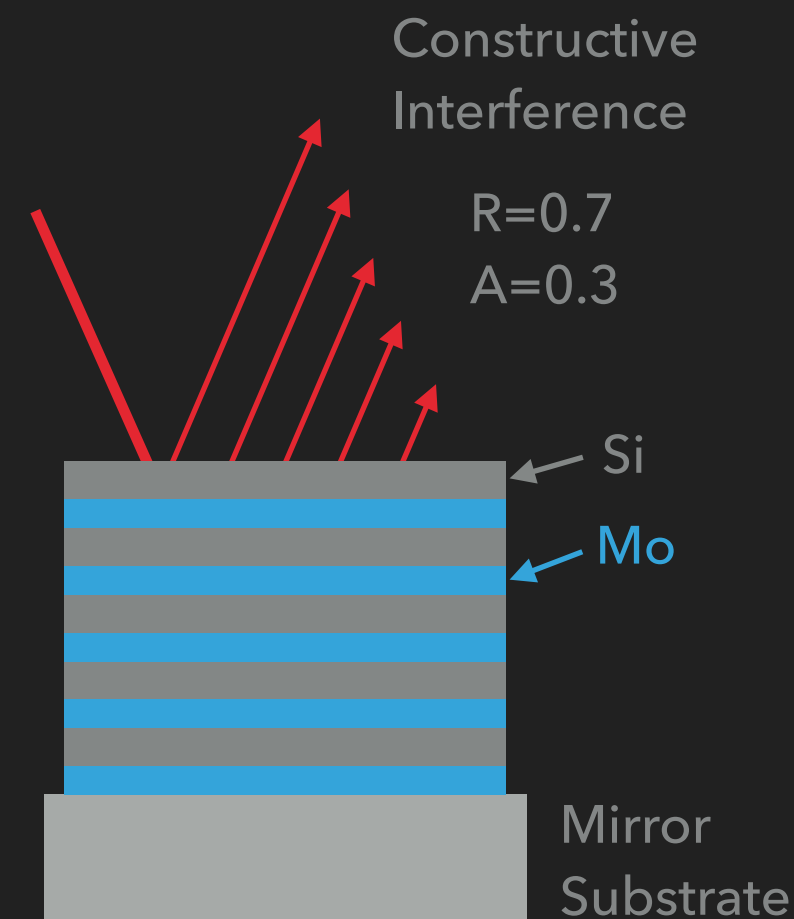


Cross section of an EUV mirror



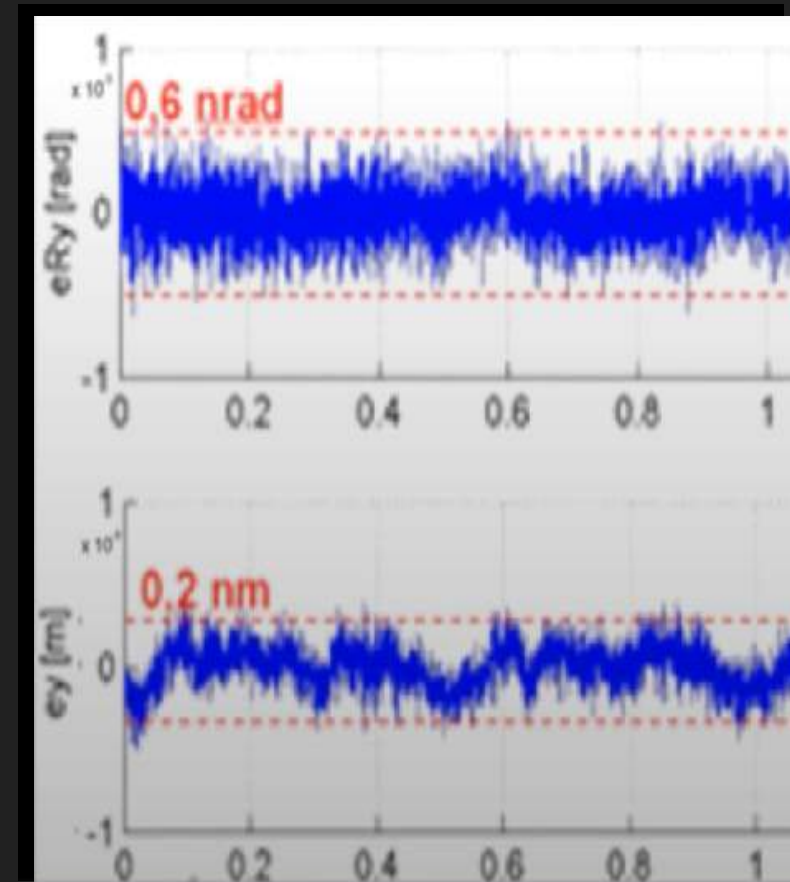
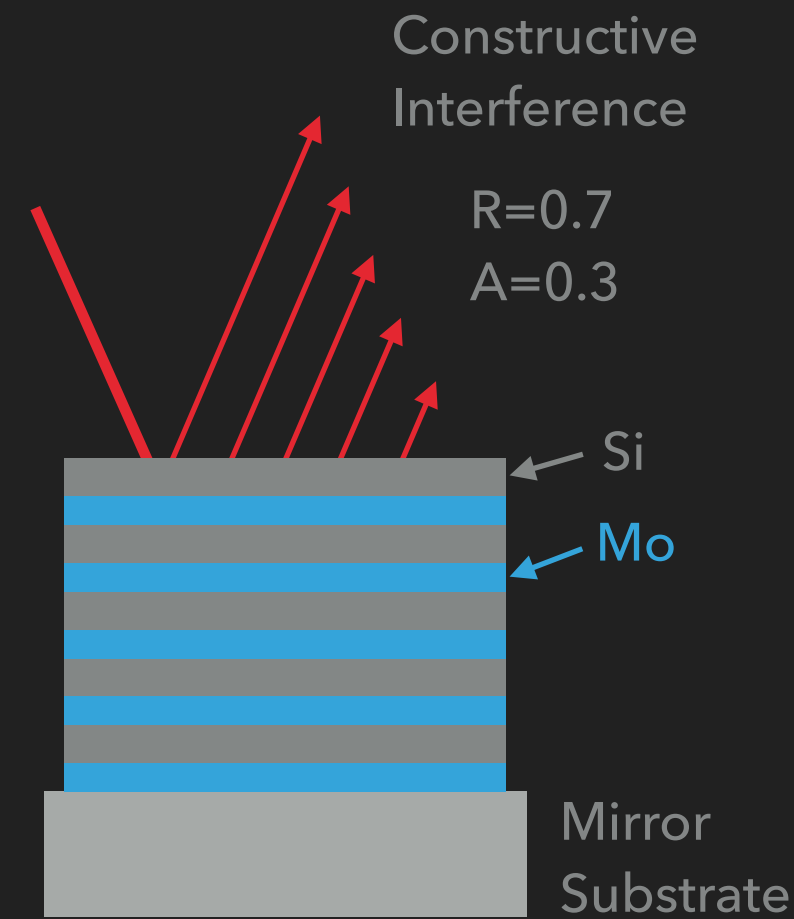
## EUV MIRRORS BY ZEISS

- ▶ EUV gets absorbed by matter
- ▶ Lenses => Bragg Mirrors
- ▶ Single atom layer deposition
- ▶ Position control < 1nm

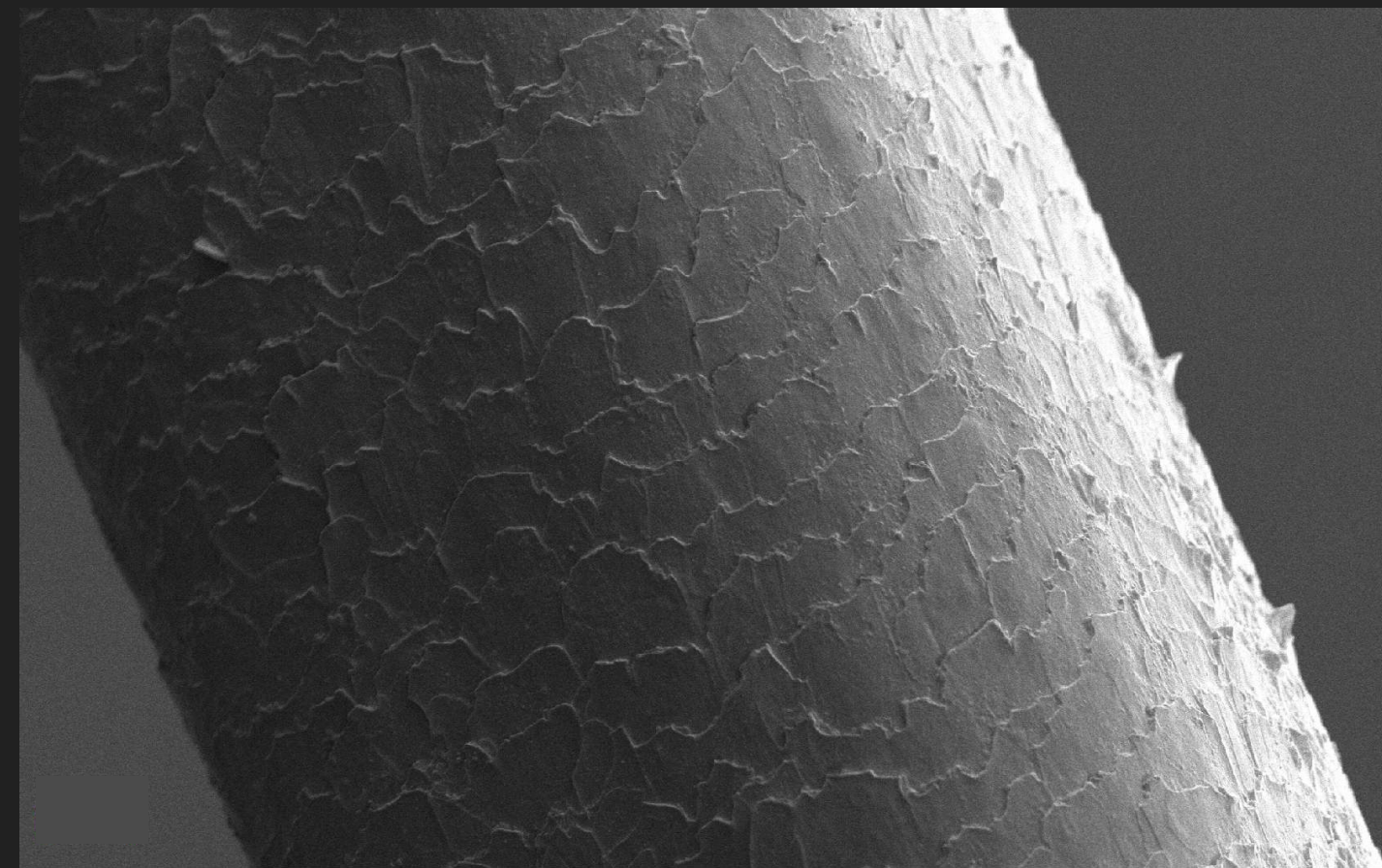
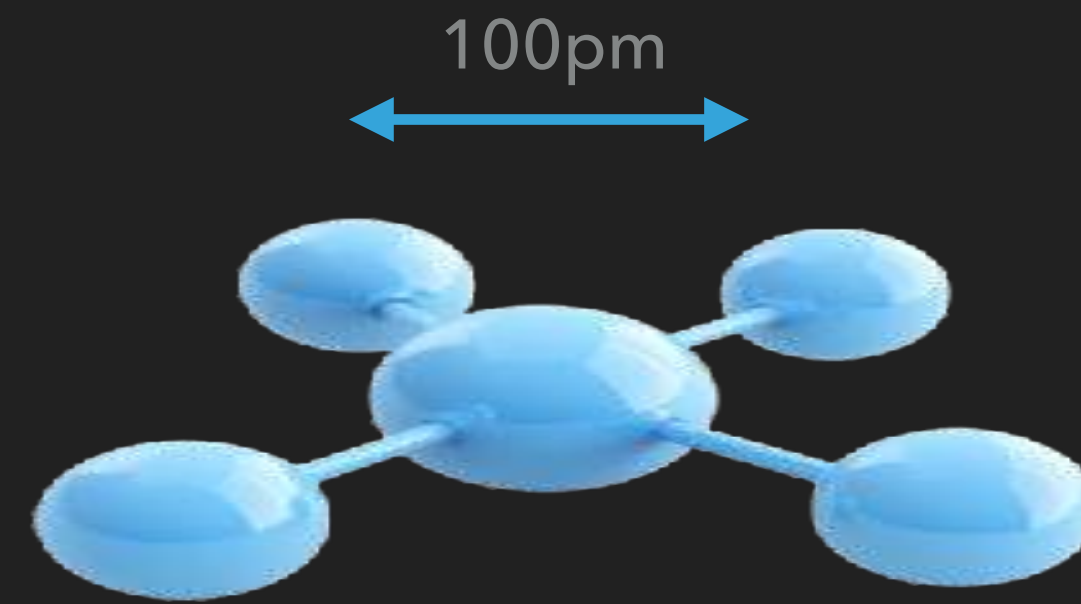


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- ▶ EUV gets absorbed by matter
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- ▶ Single atom layer deposition
- ▶ Position control < 1nm

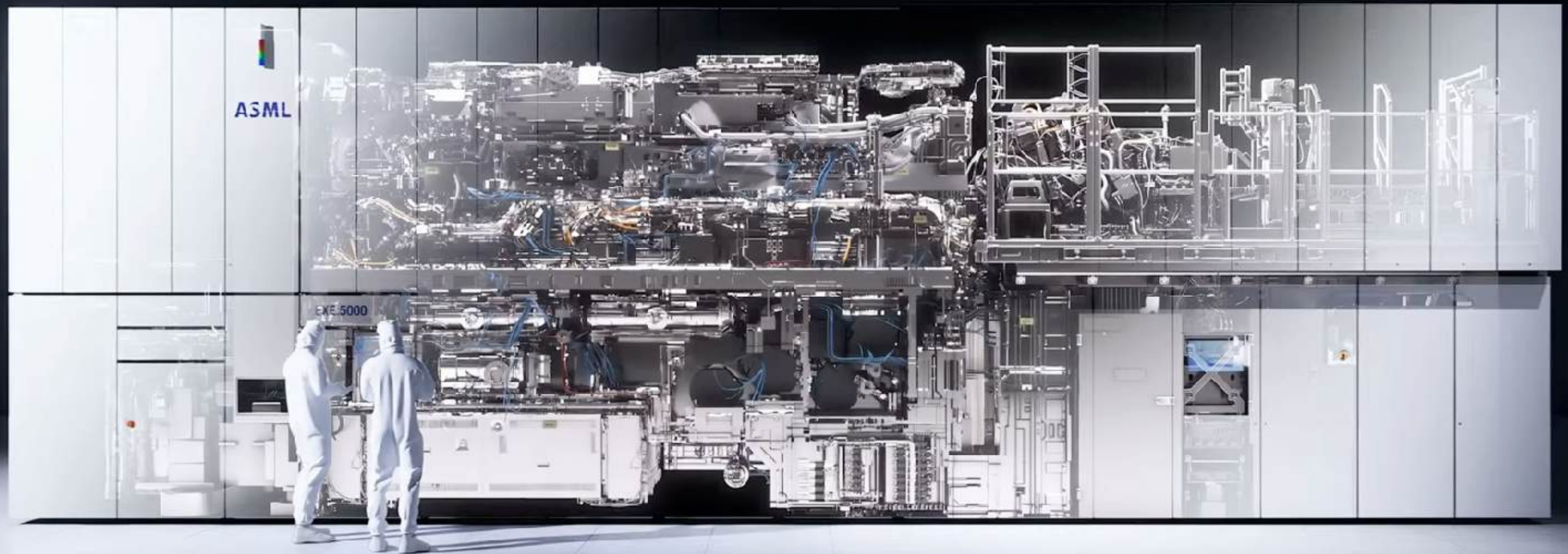


# EUV MIRRORS - 100PM ACCEPTABLE SURFACE DEVIATION



# ASML TWINSCAN EXE:5000 - HIGH NUMERICAL APERTURE EUV

**PRICE: \$350M**





## ASML

- ▶ Founded by Philipps in 1984
- ▶ Today worth \$400B



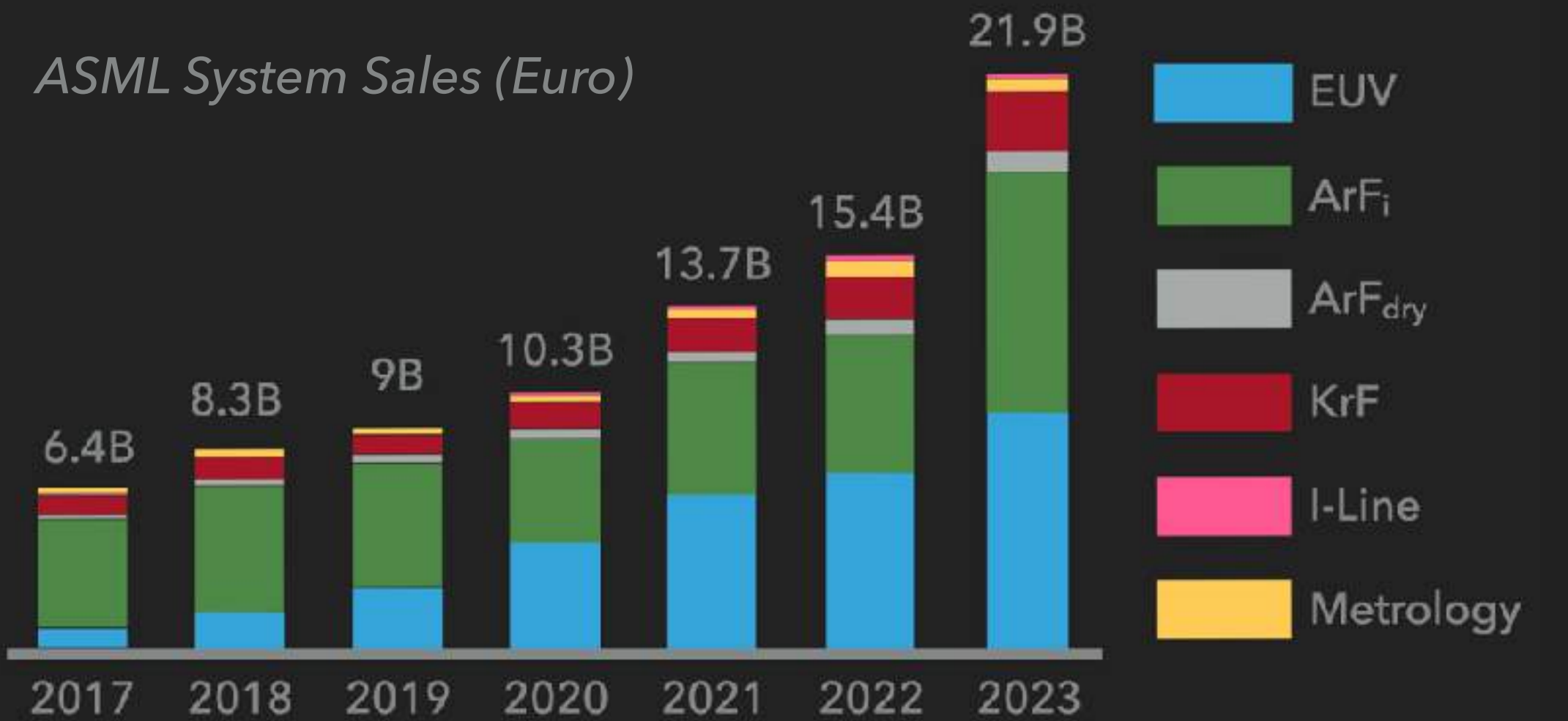
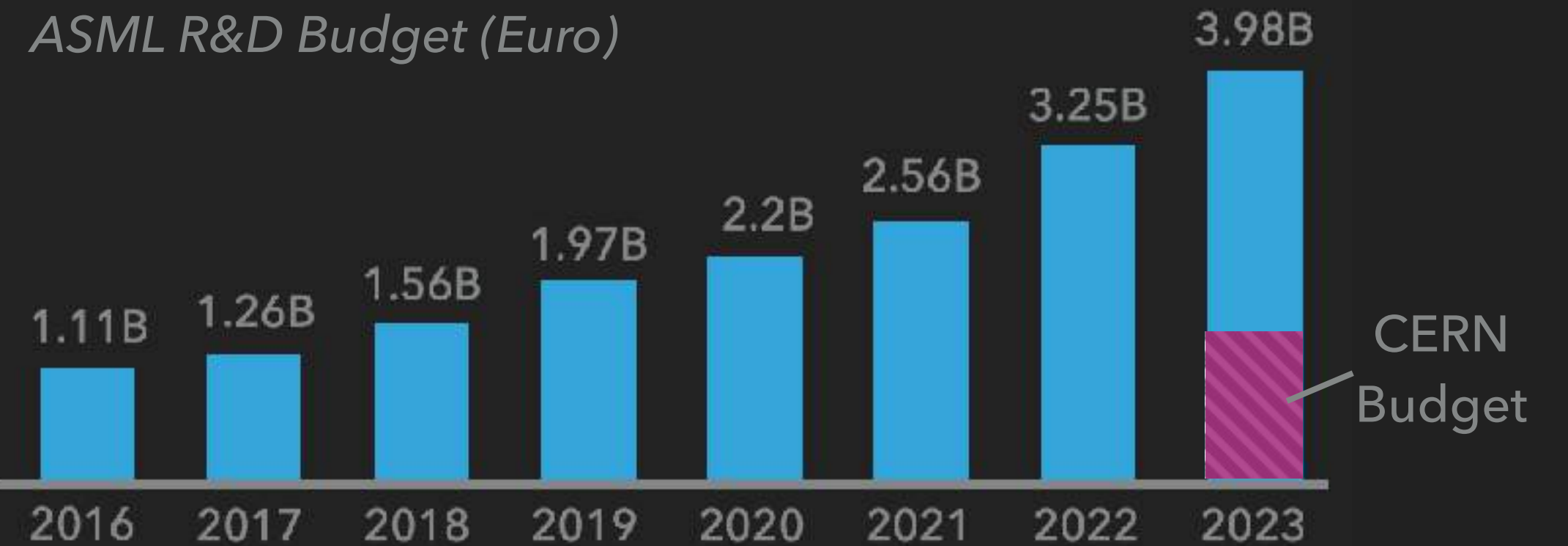
ASML headquarter in 1984



ASML headquarter in 2024

## ASML

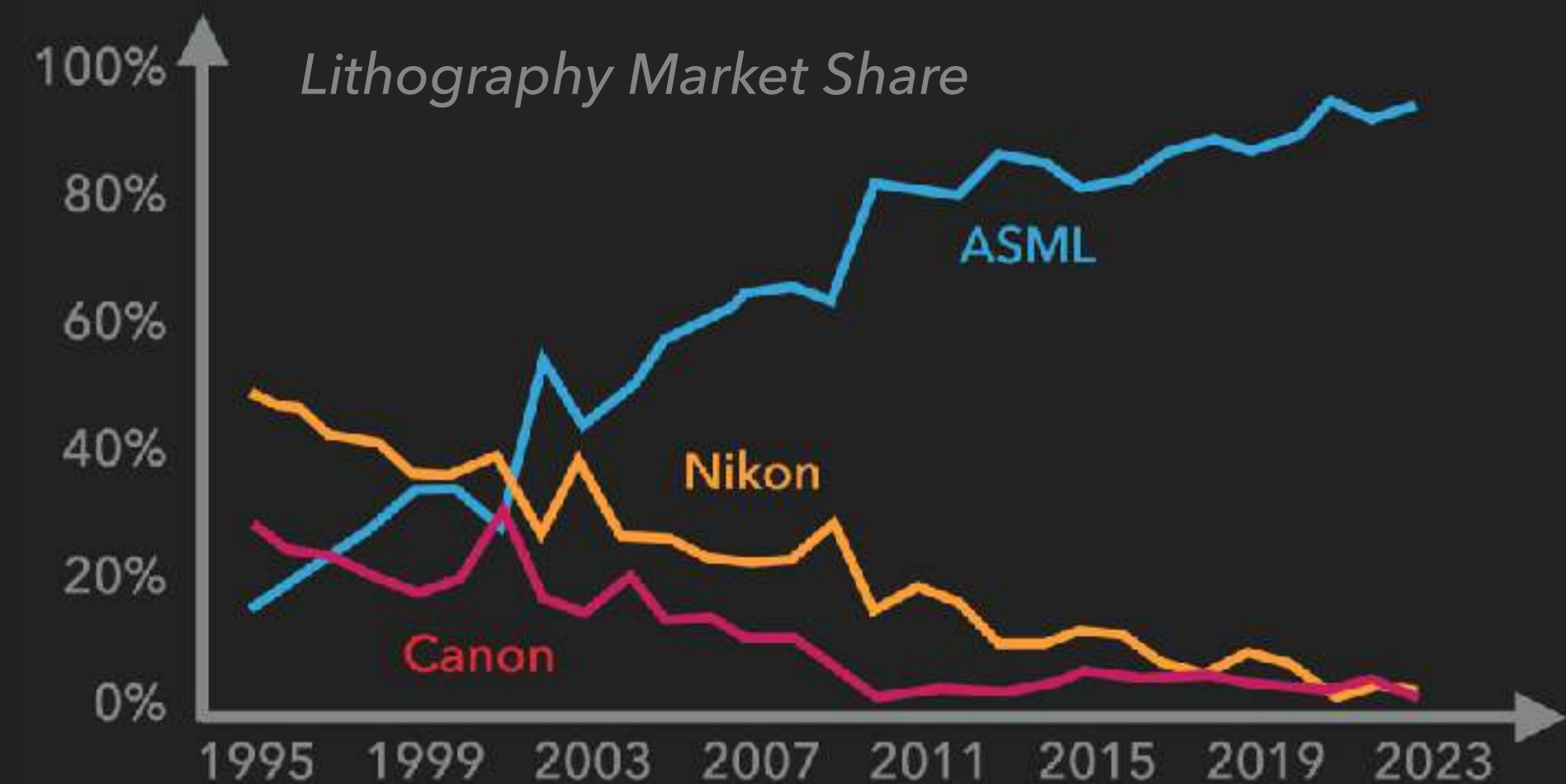
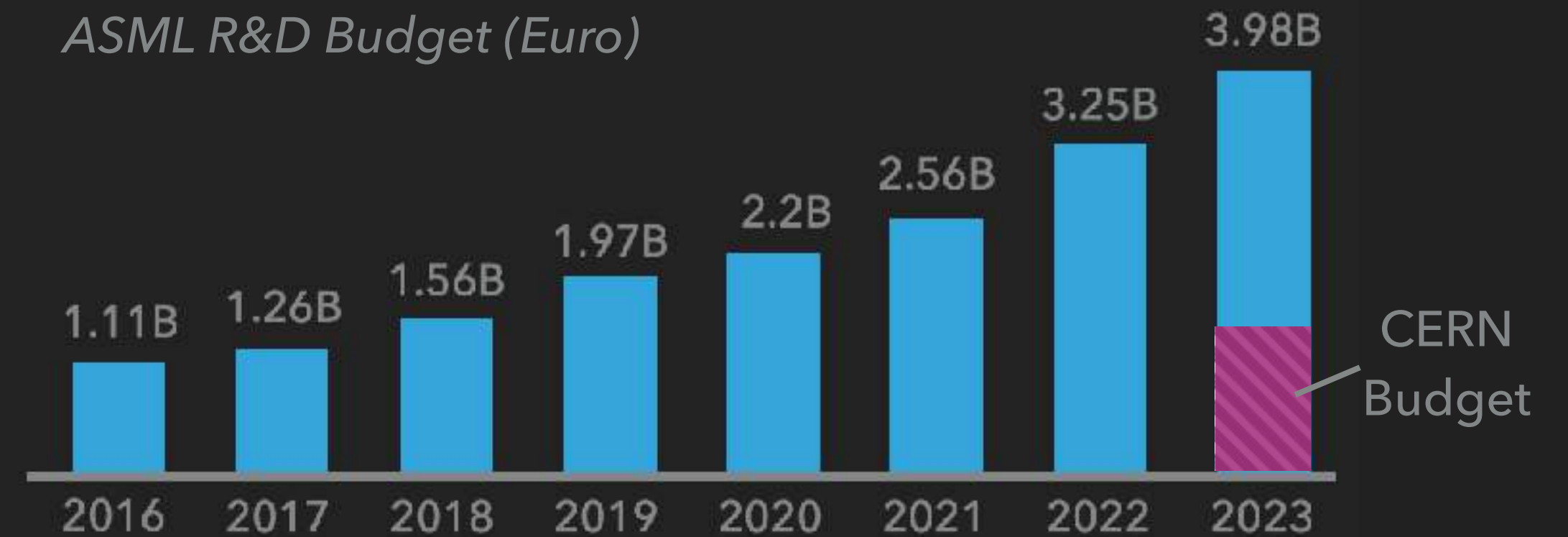
- ▶ Founded by Philipps in 1984
- ▶ Today worth \$400B

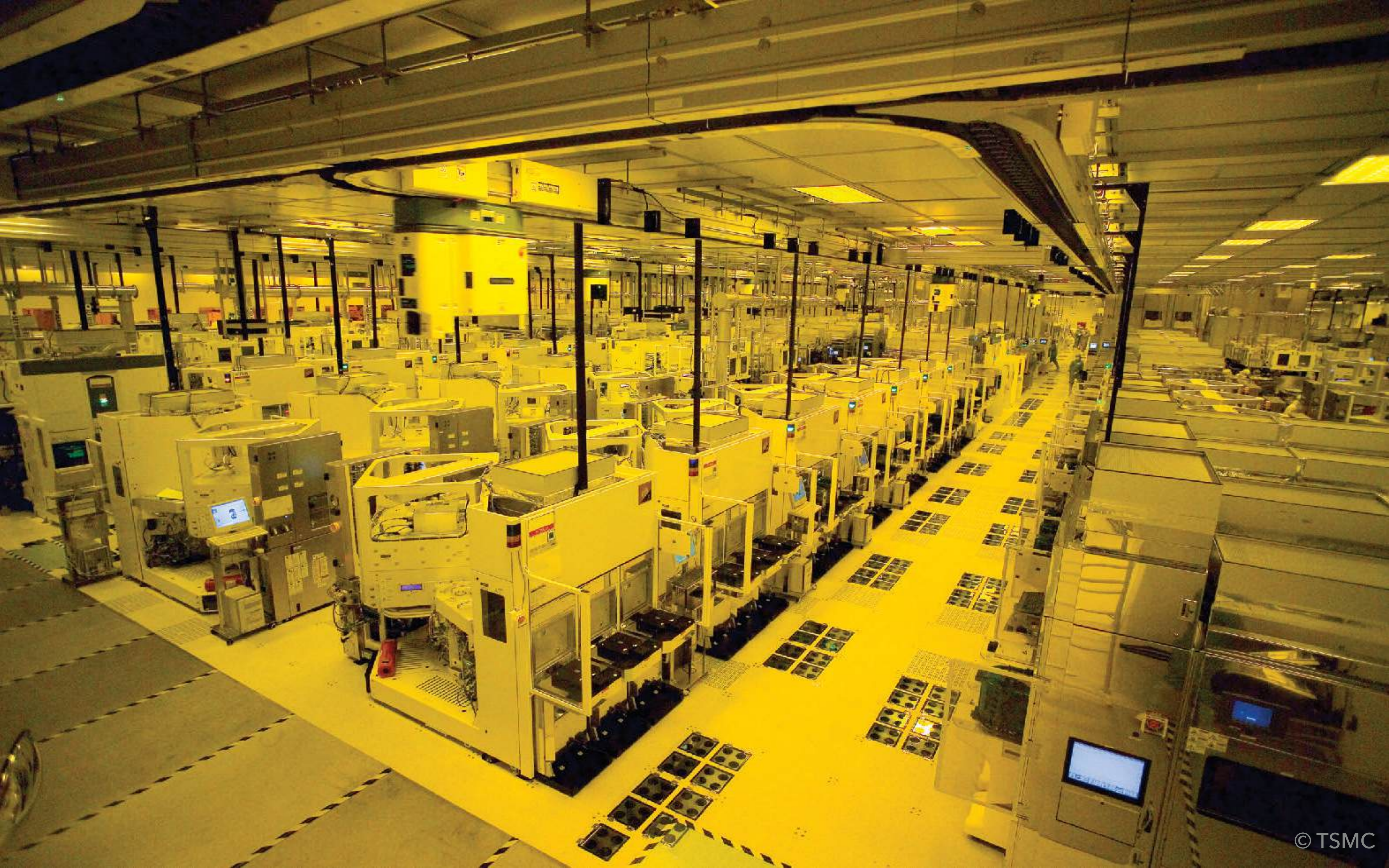


## ASML

- ▶ Founded by Philipps in 1984
- ▶ Today worth \$400B
- ▶ Lithography market share  $\approx$  90%
- ▶ Single source for EUV technology

ASML R&D Budget (Euro)





# ECONOMICS OF SEMICONDUCTOR FABRICATION

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# TSMC

FAB 21, Arizona, USA



## TSMC – GIGAFABS™

- ▶ FAB Cost: \$20B
- ▶ FAB Power Consumption: 1GW
- ▶ FAB Water Consumption: 8M l/day
- ▶ FAB Output: 120k Wafer per month

FAB 20, Hsinchu, Taiwan



FAB 18, Tainan, Taiwan



FAB 14, Tainan, Taiwan

# TSMC – DOMINATING GLOBAL CHIP SUPPLY

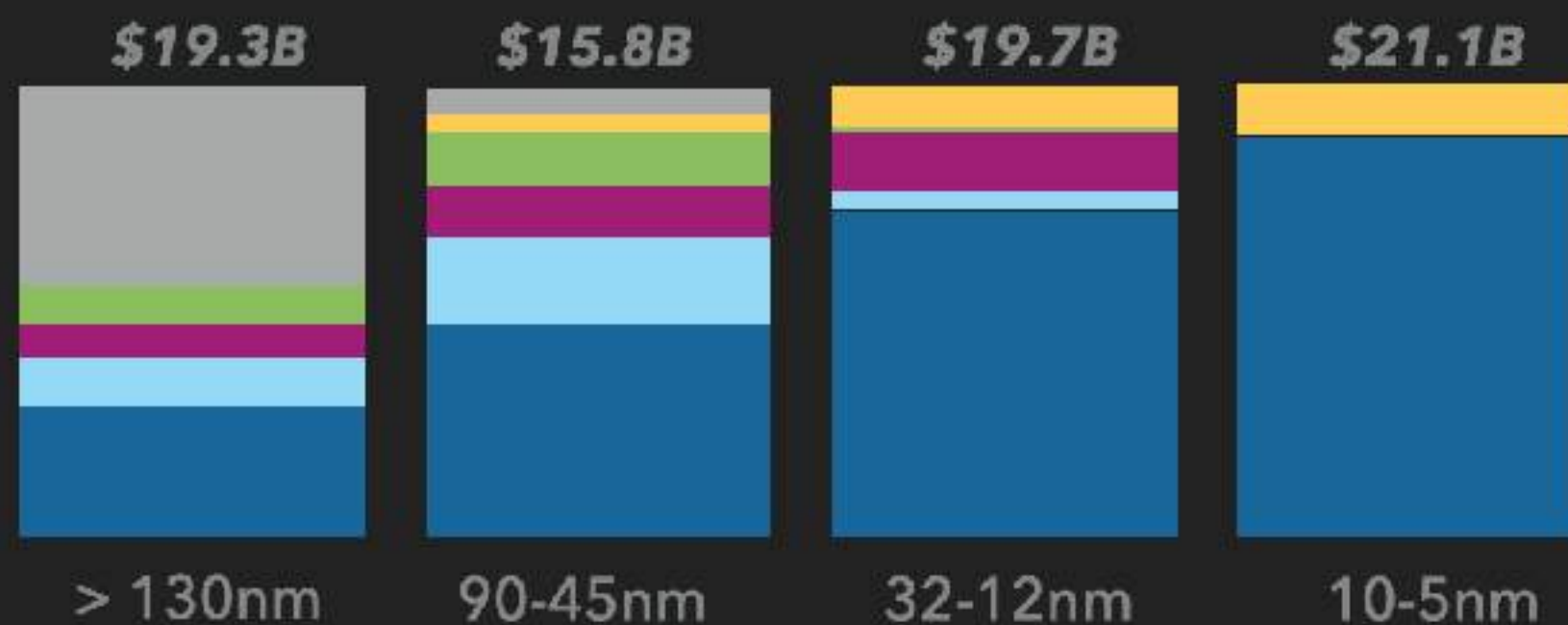
FAB 20, Hsinchu, Taiwan



- ▶ 60% market share of all chips
- ▶ 92% market share of advanced chips
- ▶ Capital Expenditure ≈ \$30B / year
- ▶ R&D budget ≈ \$6B / year



Total Market Size (2020):

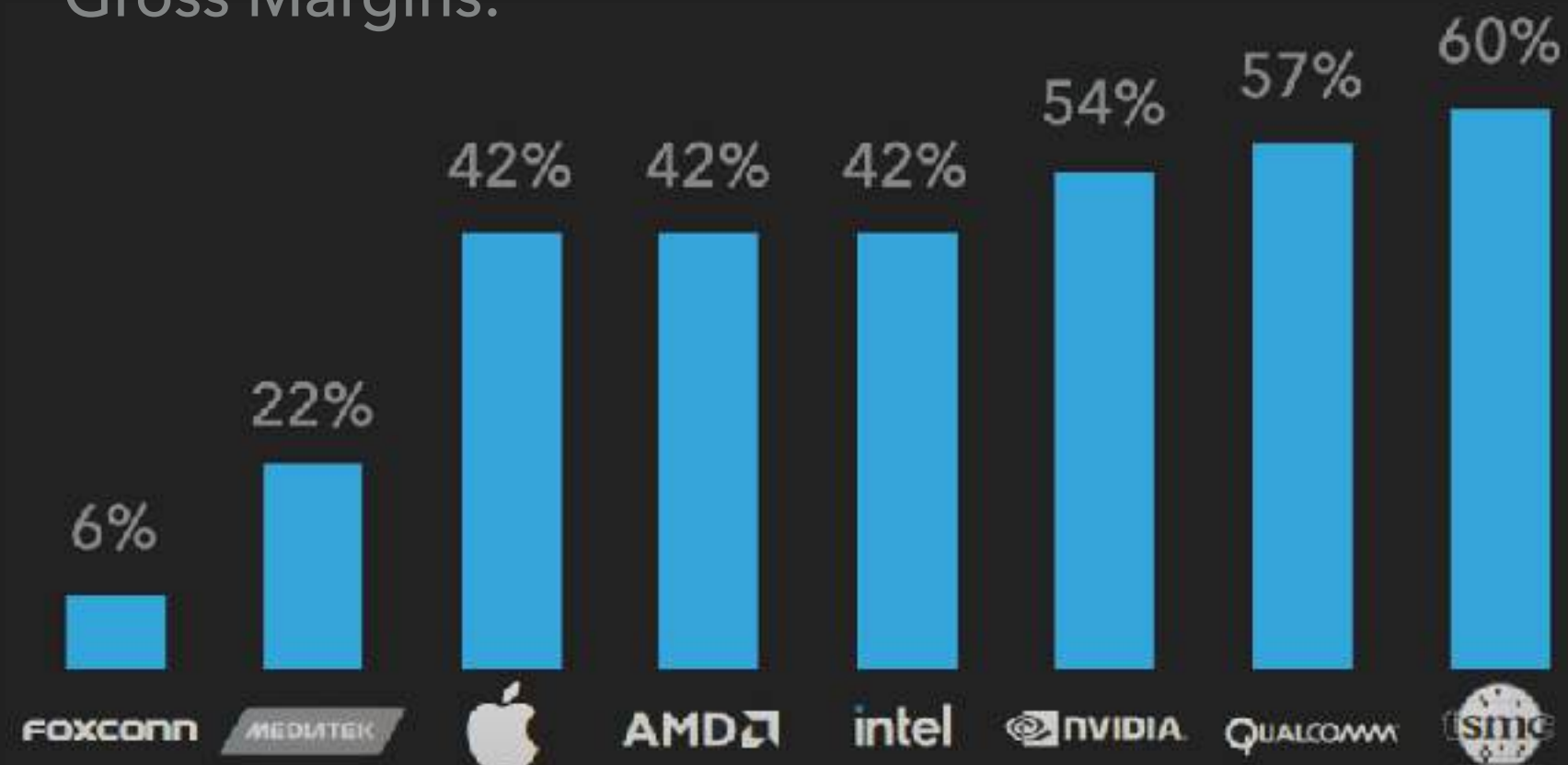


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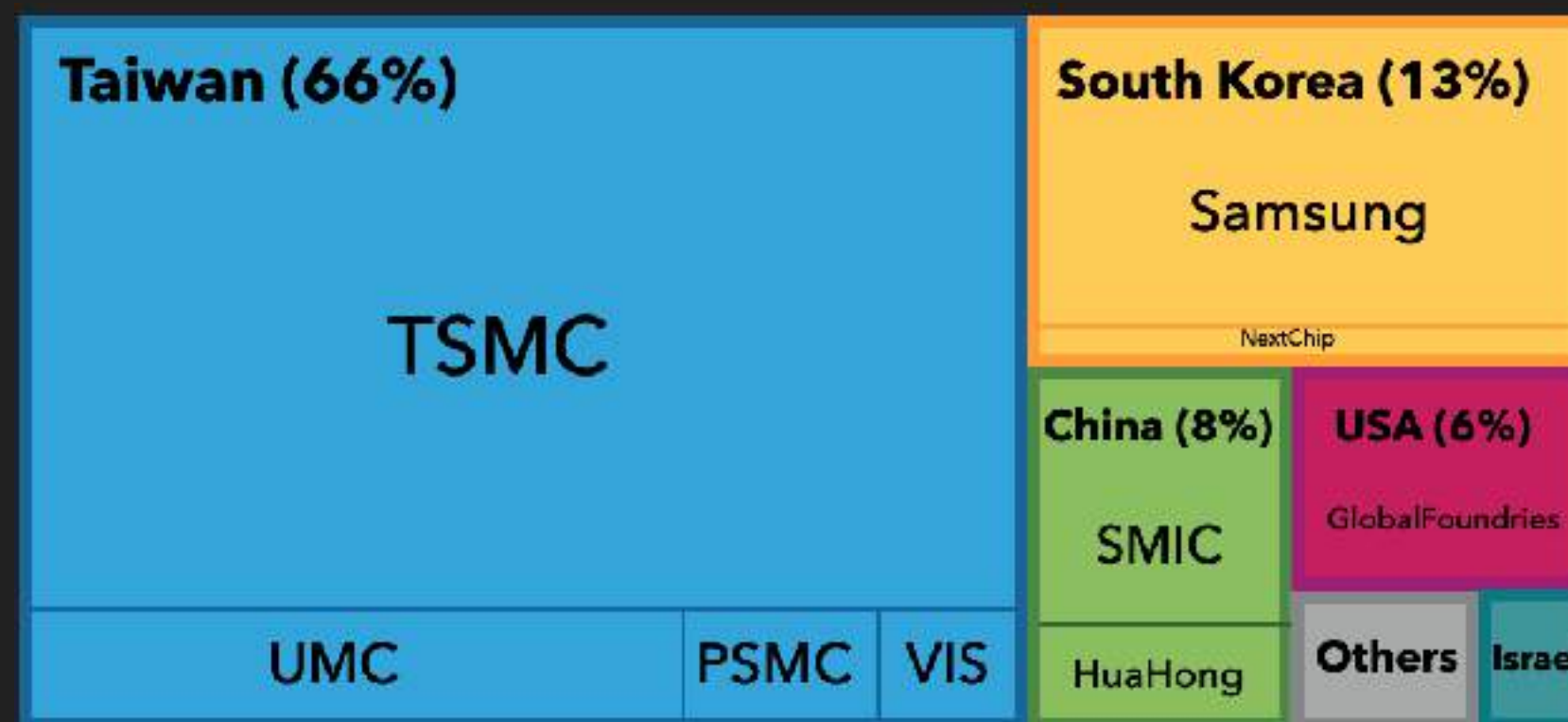
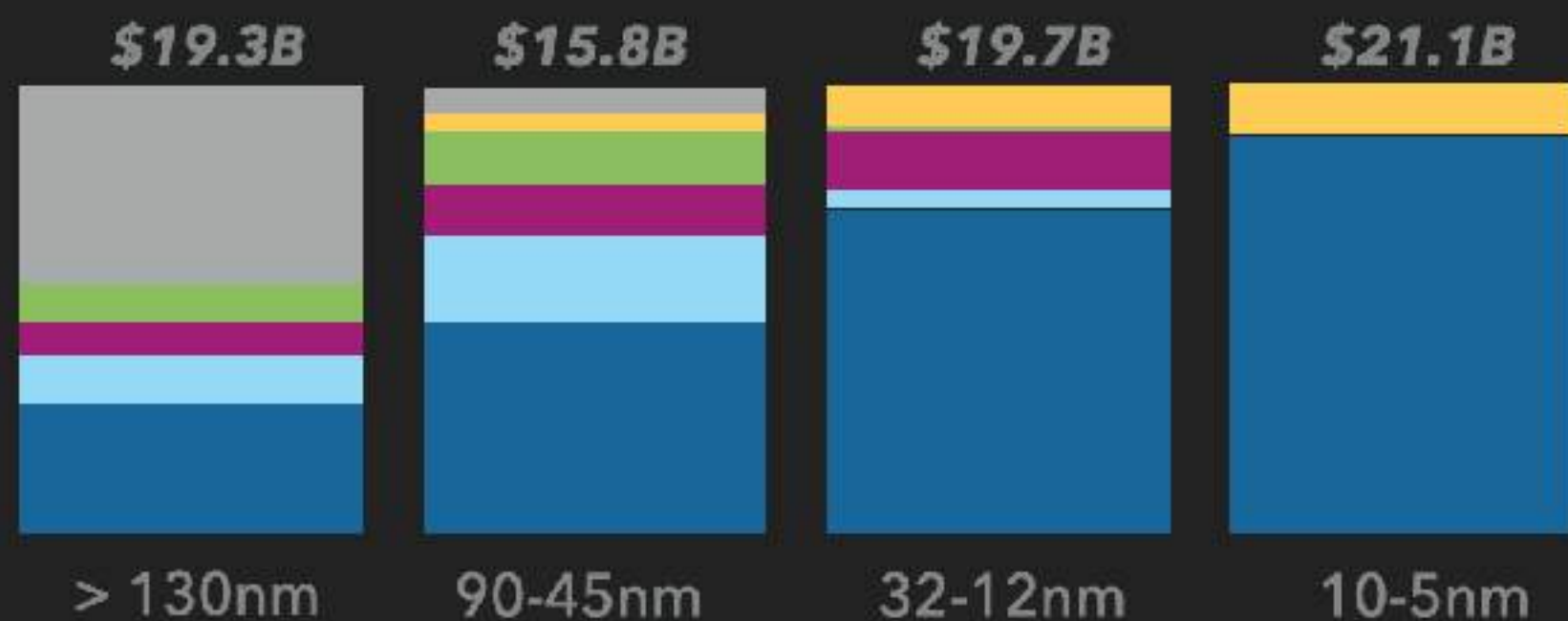
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Gross Margins:



Total Market Size (2020):

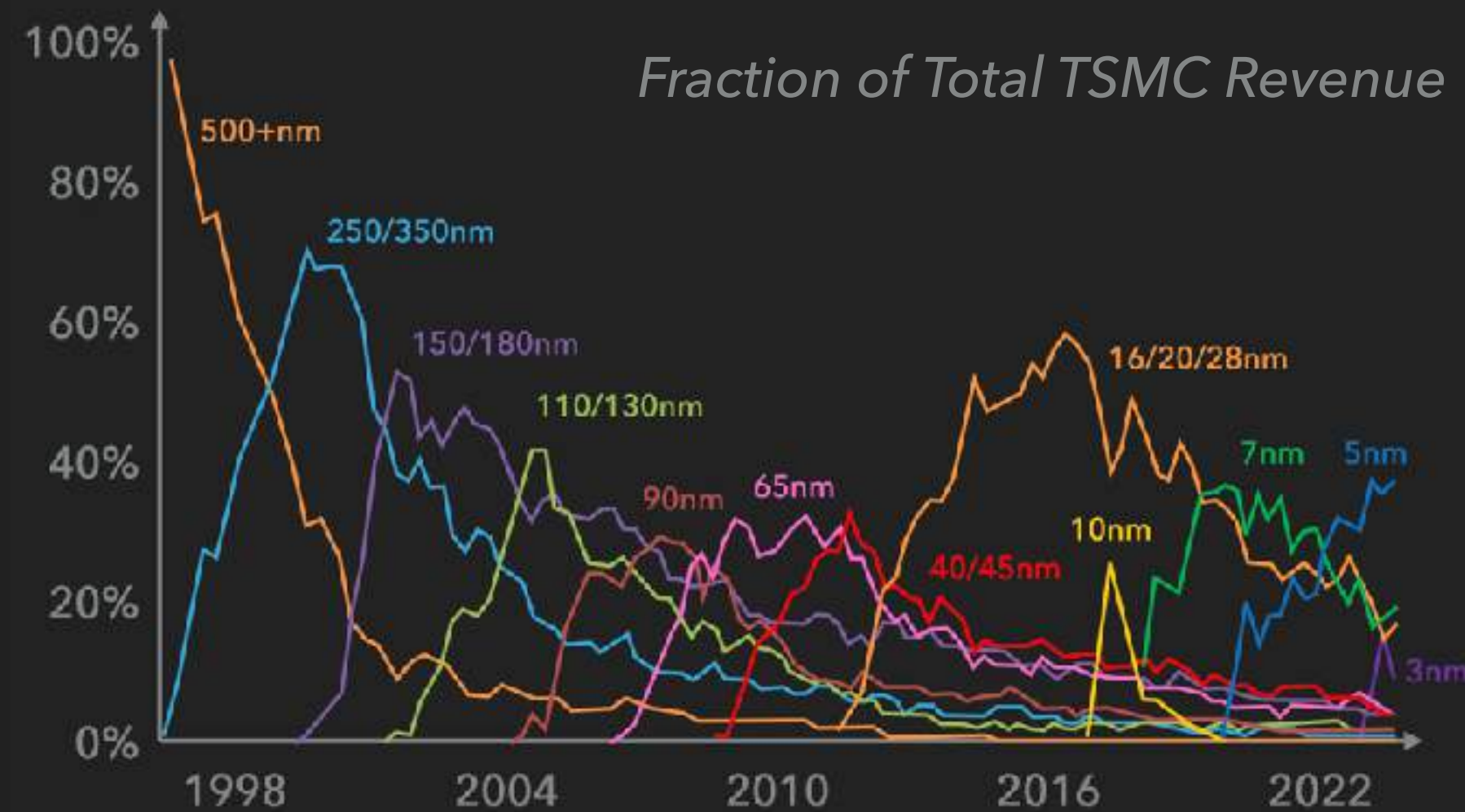


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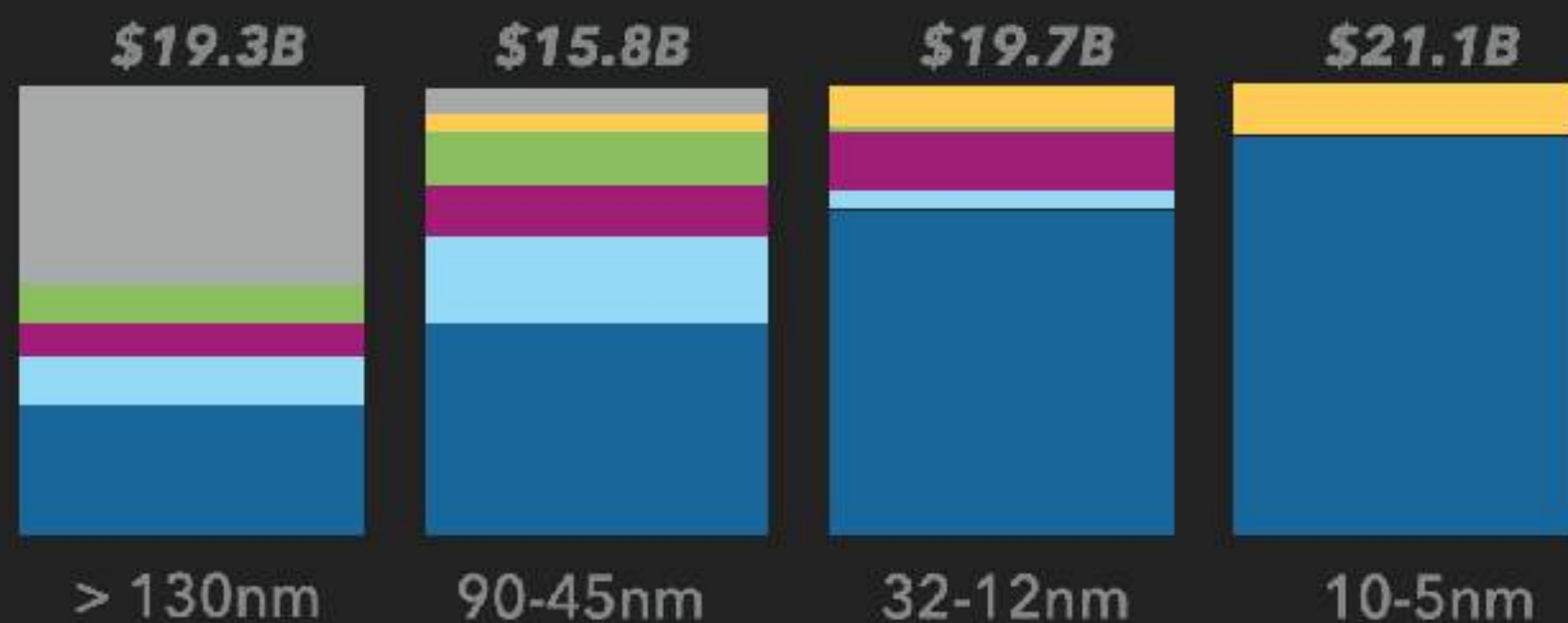


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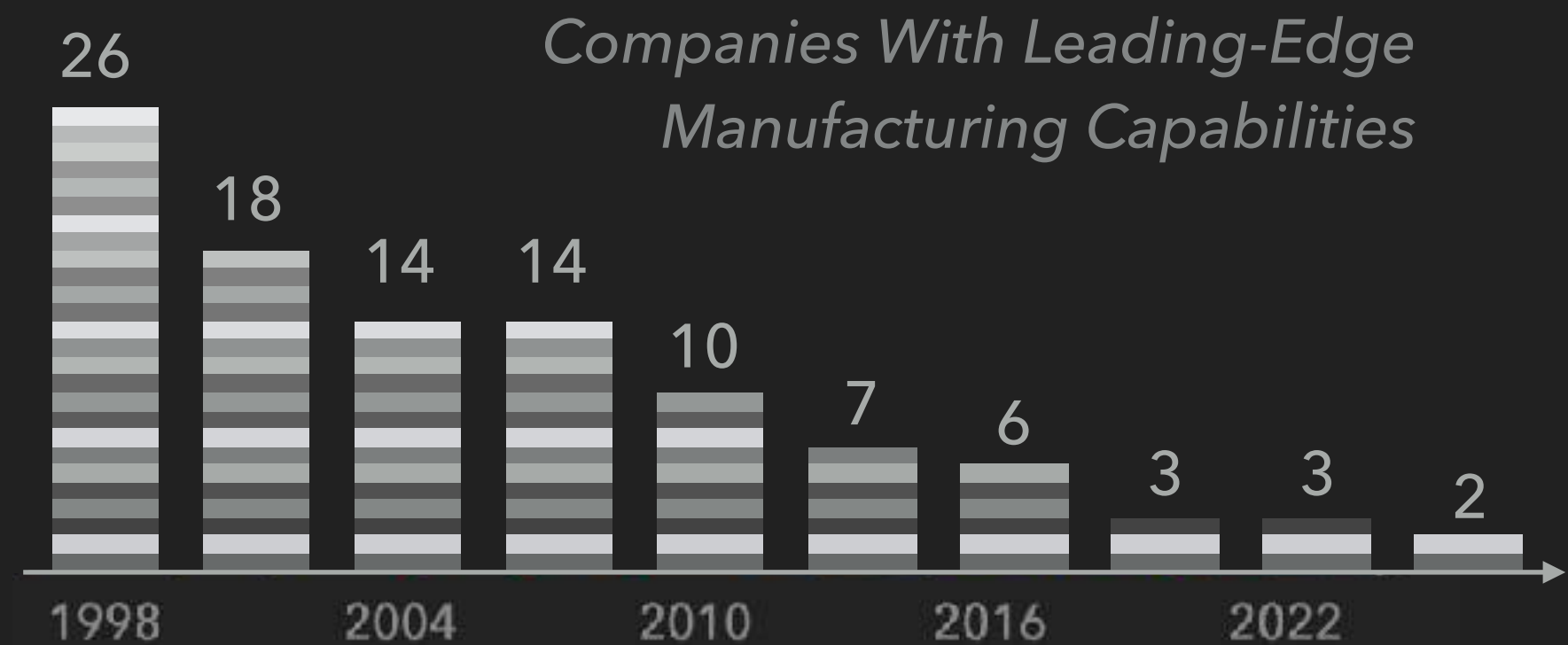
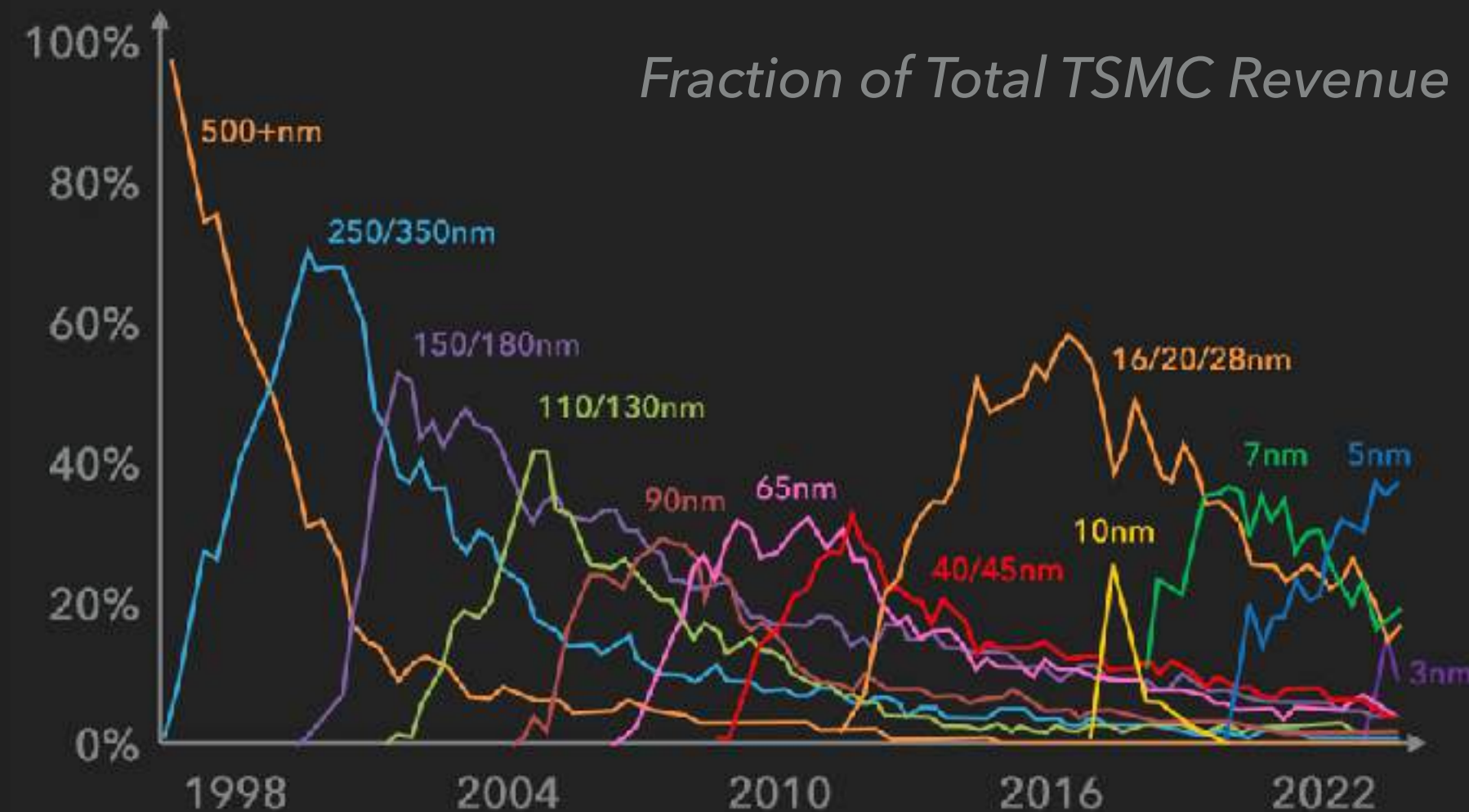
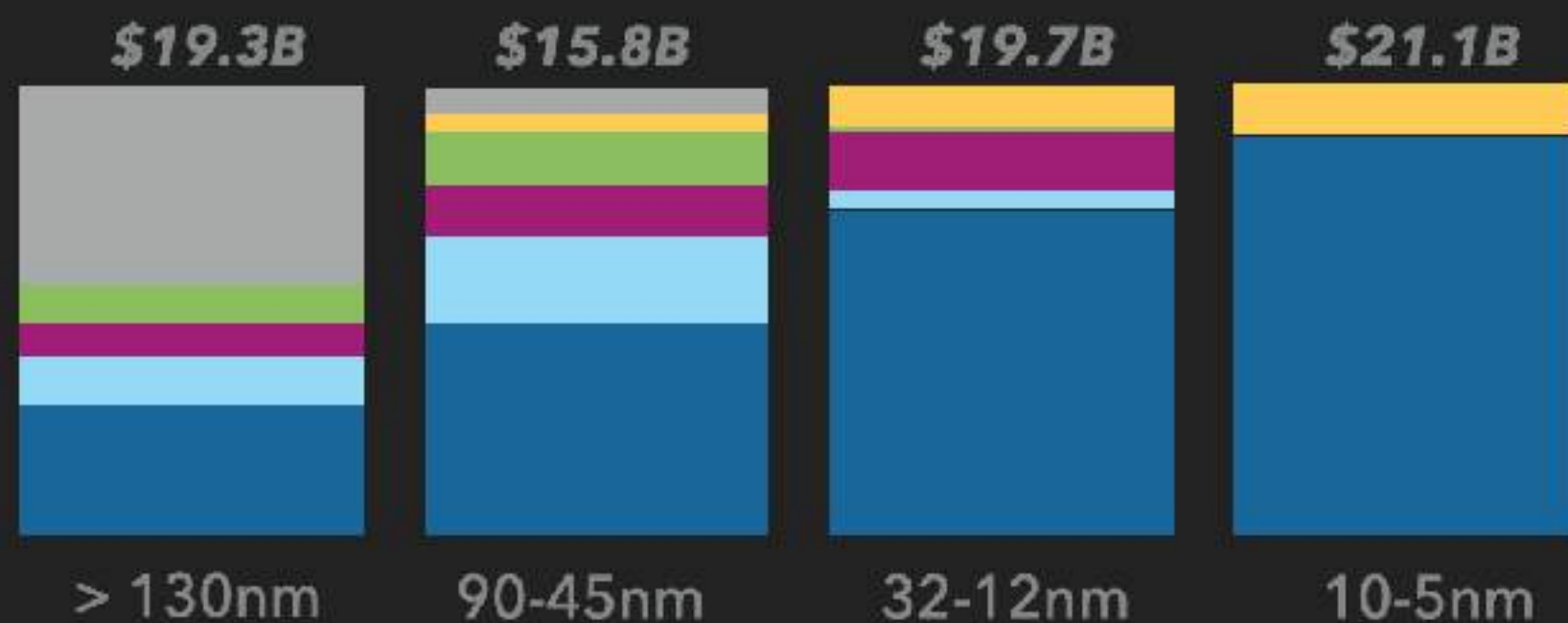
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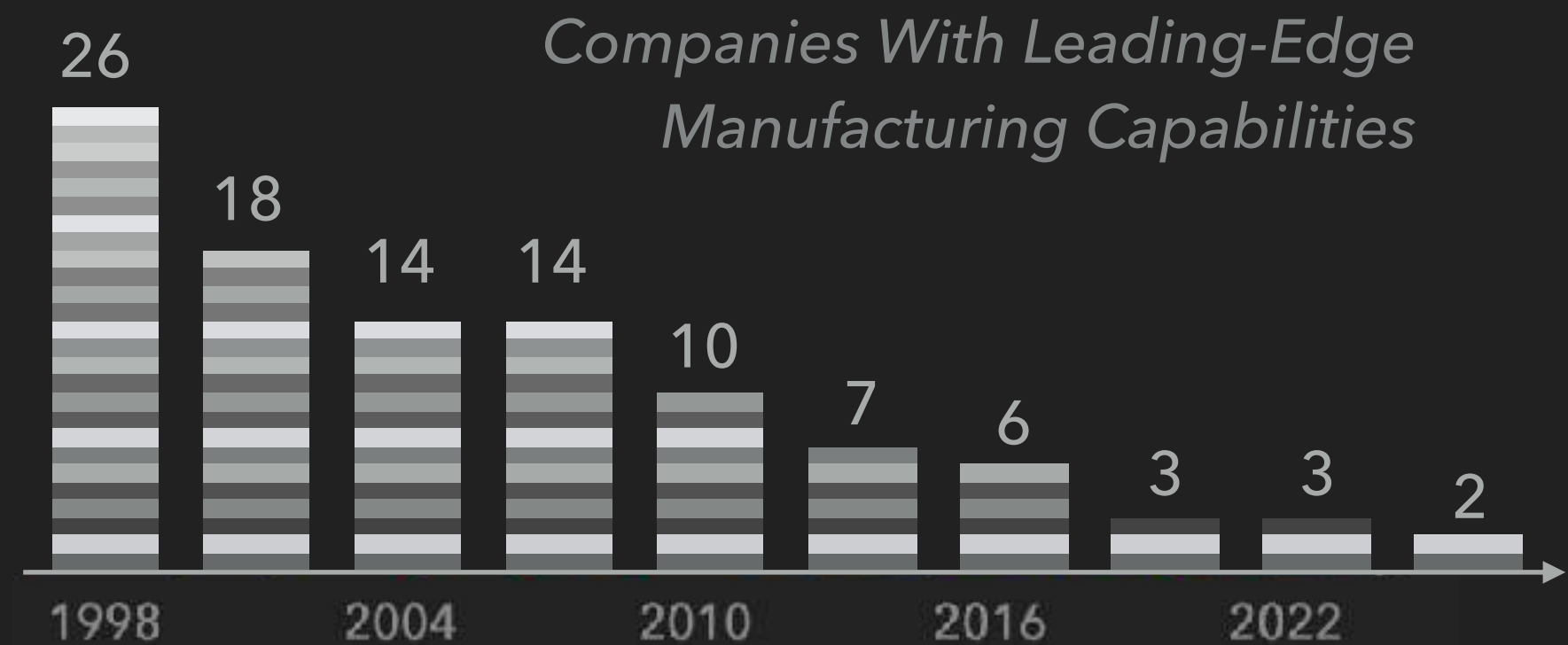
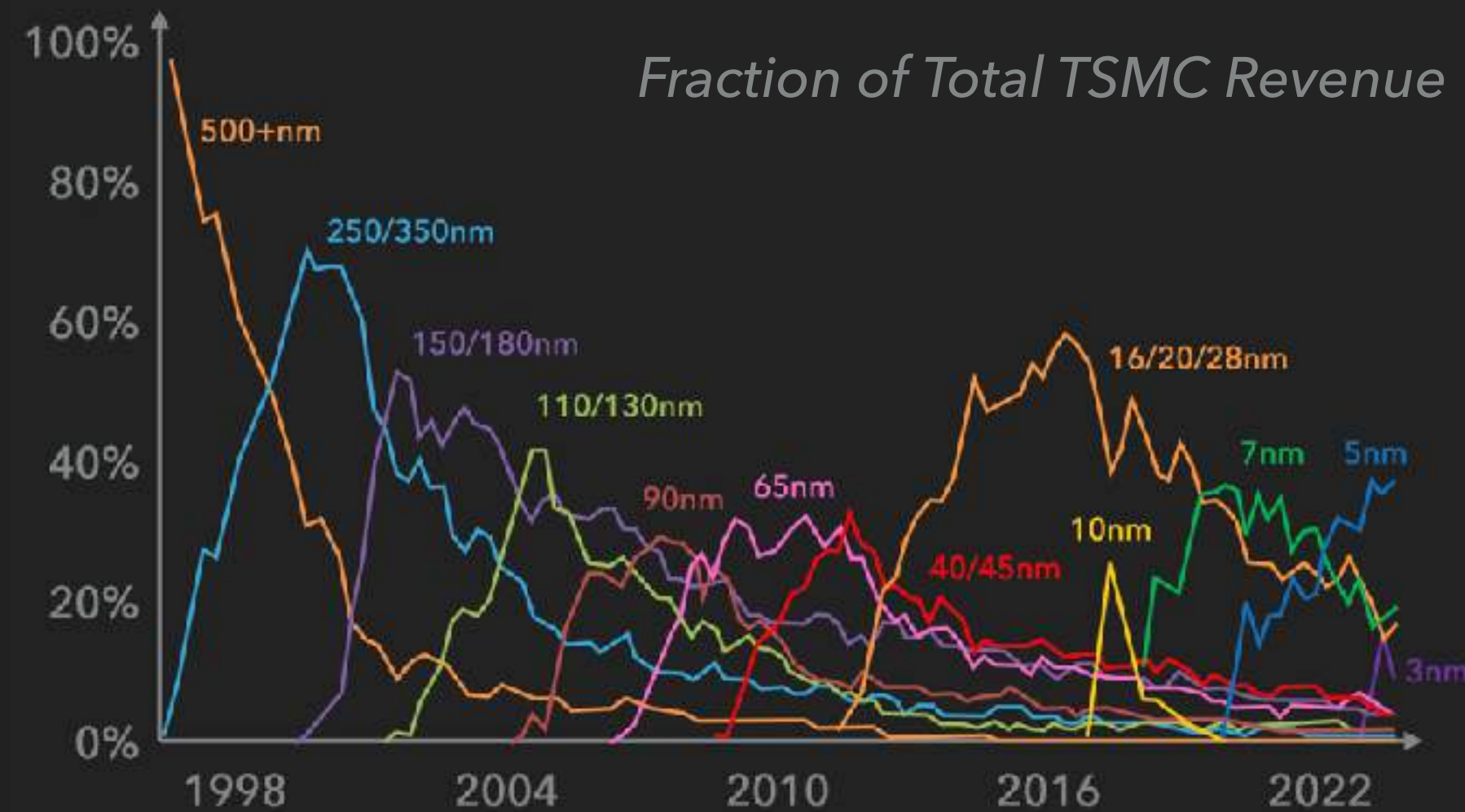
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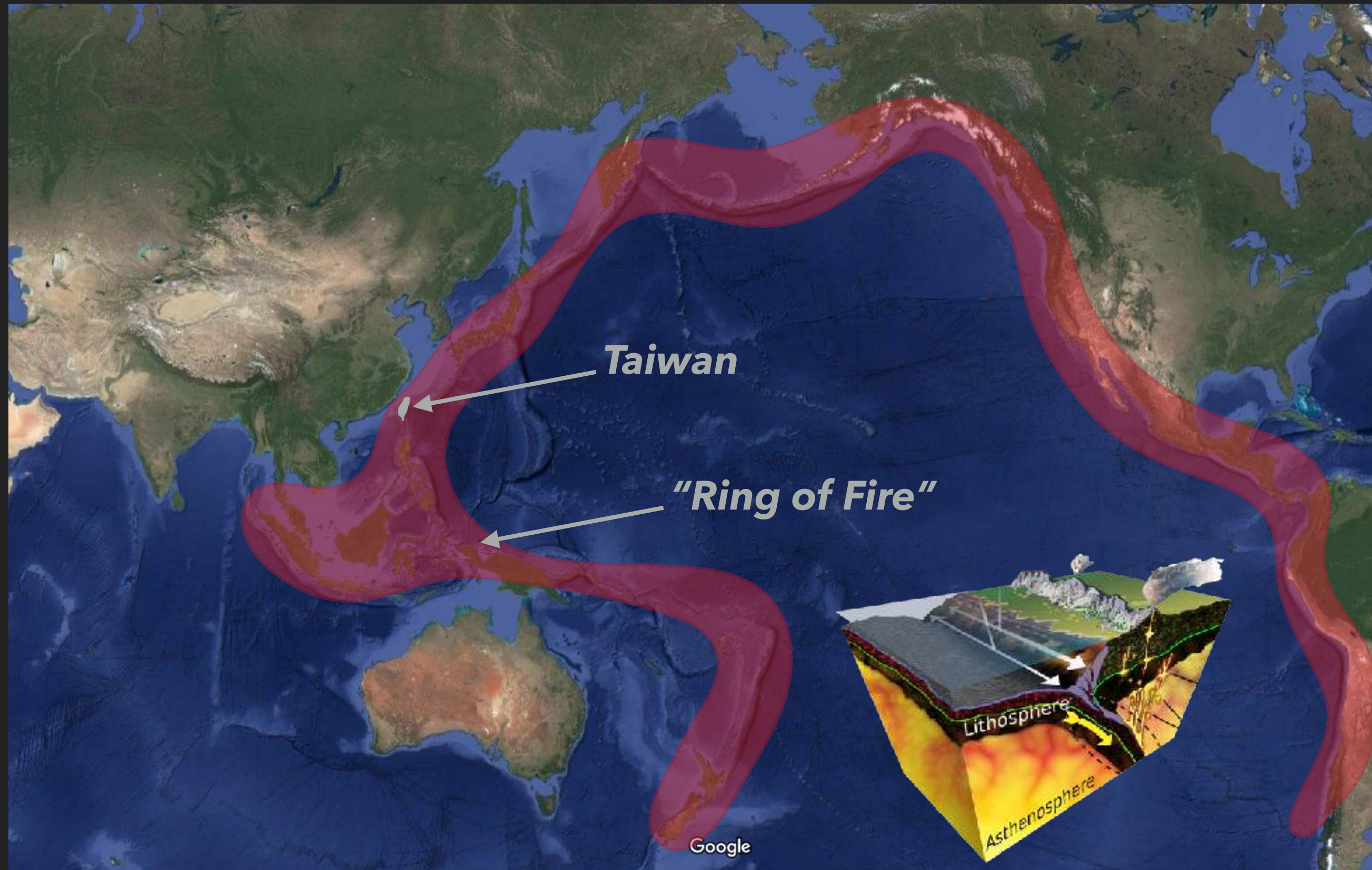
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# THE CHIHSHANG FAULT



20:08 Dec 15, 2024

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**Magnitude 4.1 earthquake**  
12 miles from Hualien City, Hualien County,  
Taiwan · Dec 11, 2:05 PM

More on earthquake.usgs.gov

Note: Official reports of new earthquakes may be delayed  
All times are in Central Time · Sources: U.S. Geological Survey, ready.gov

What to do after an earthquake

# THE CHIHSHANG FAULT

3. April 2024 - 7.2 earthquake

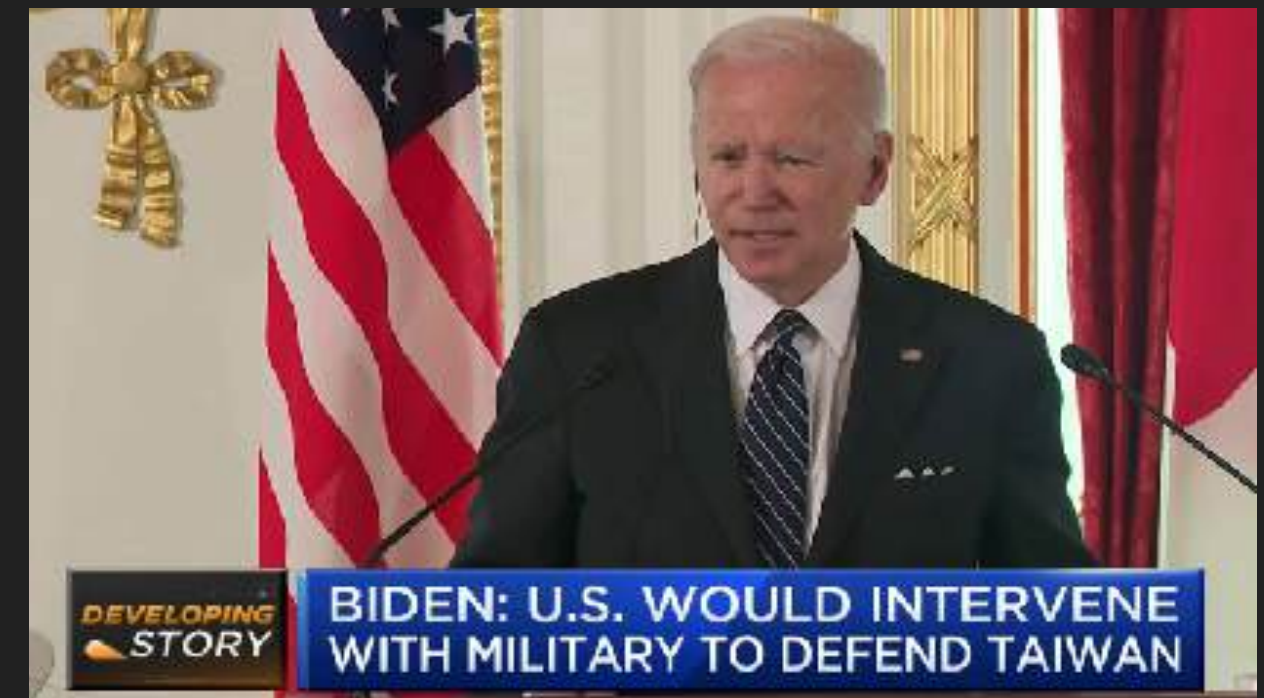


# PIECE IN THE TAIWAN STRAIGHT



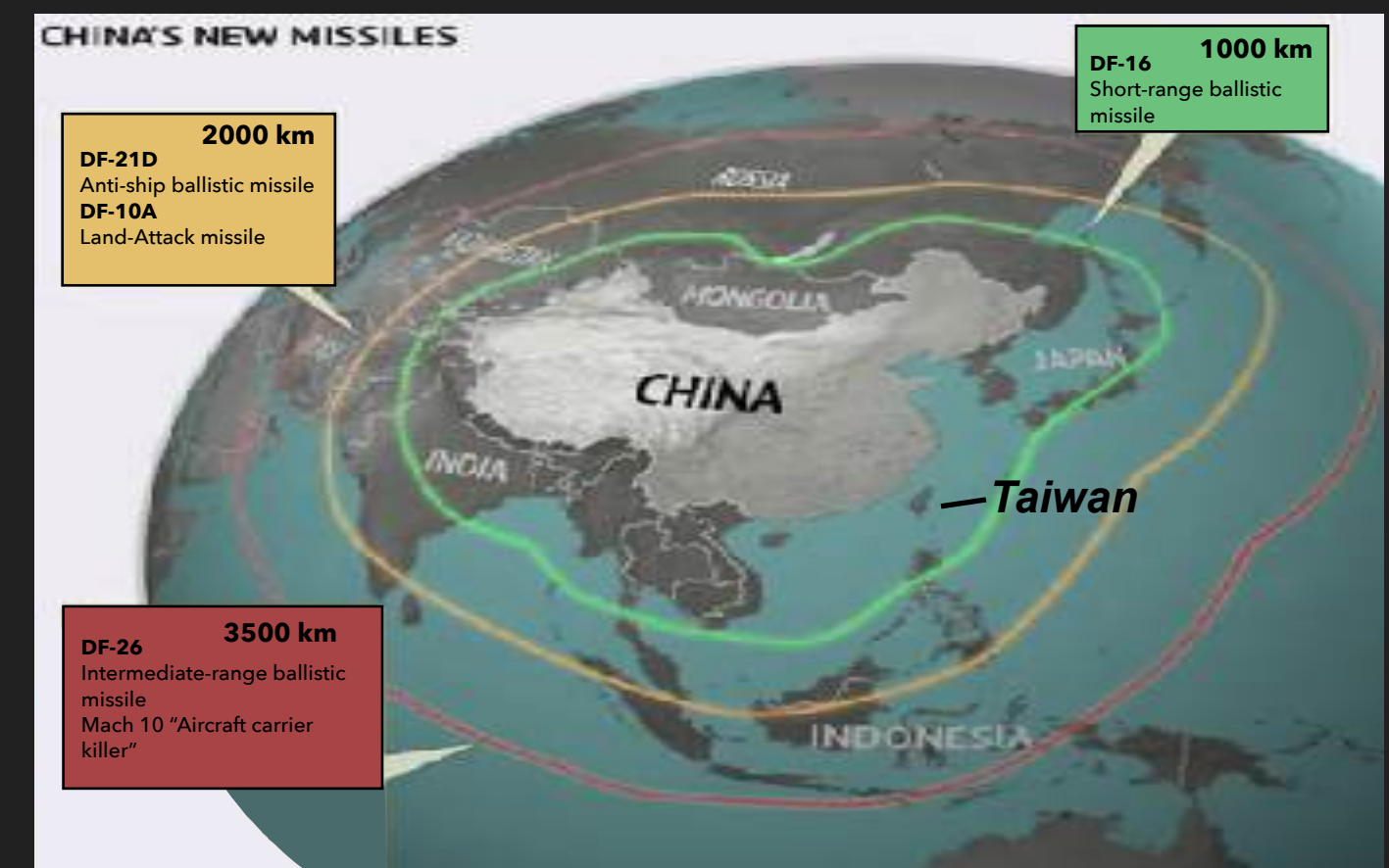
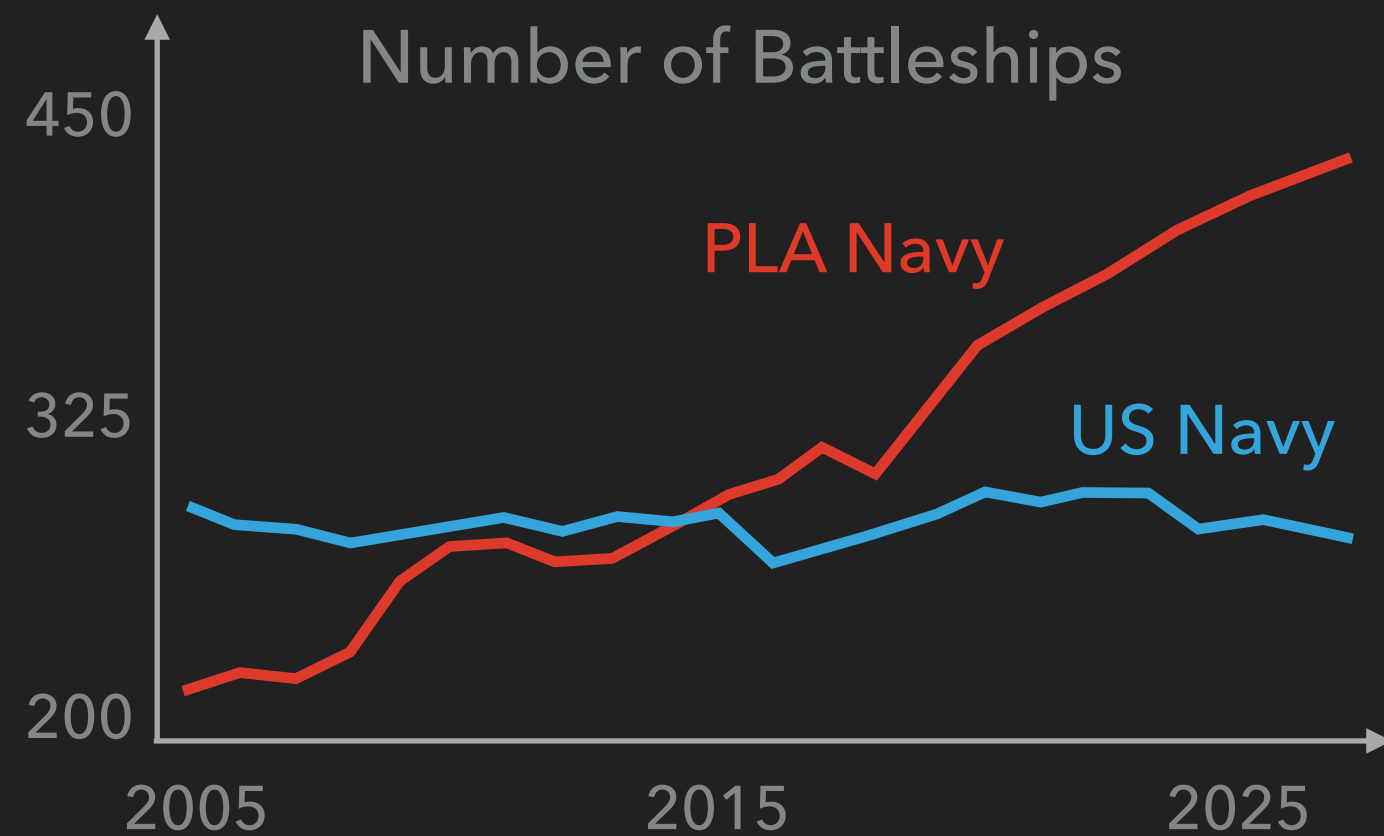
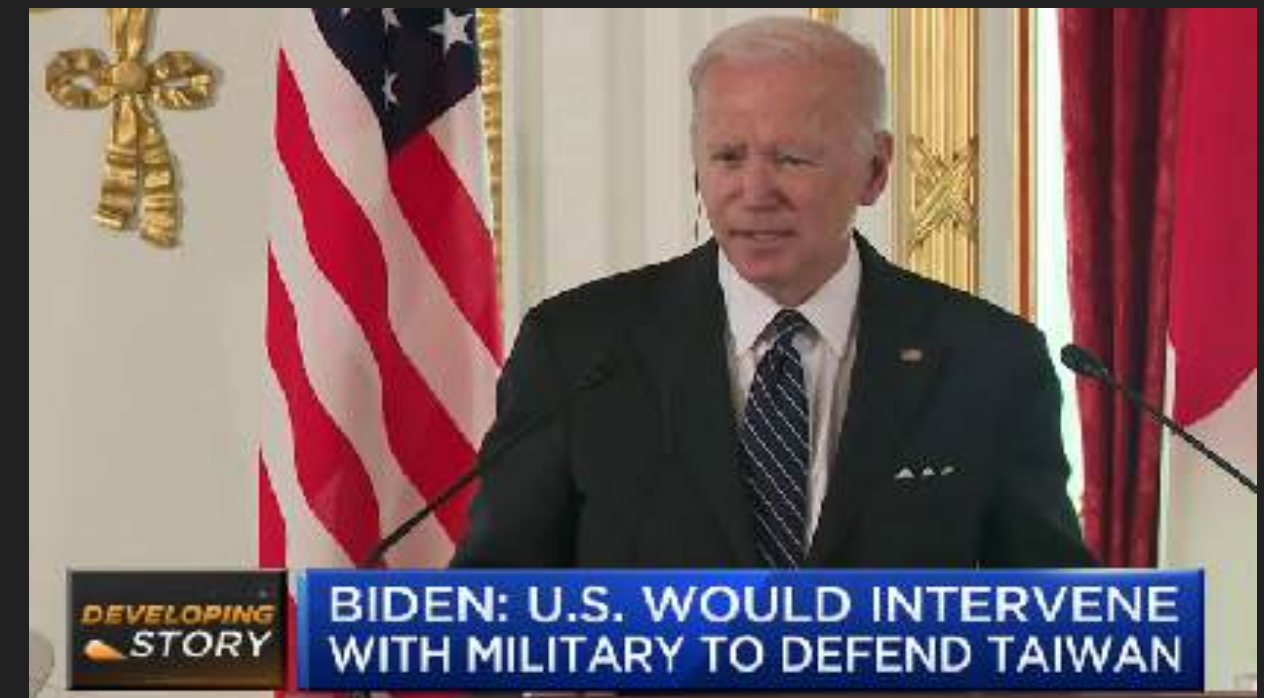
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CHINA MINISTRY OF FOREIGN AFFAIRS (OCT '24)



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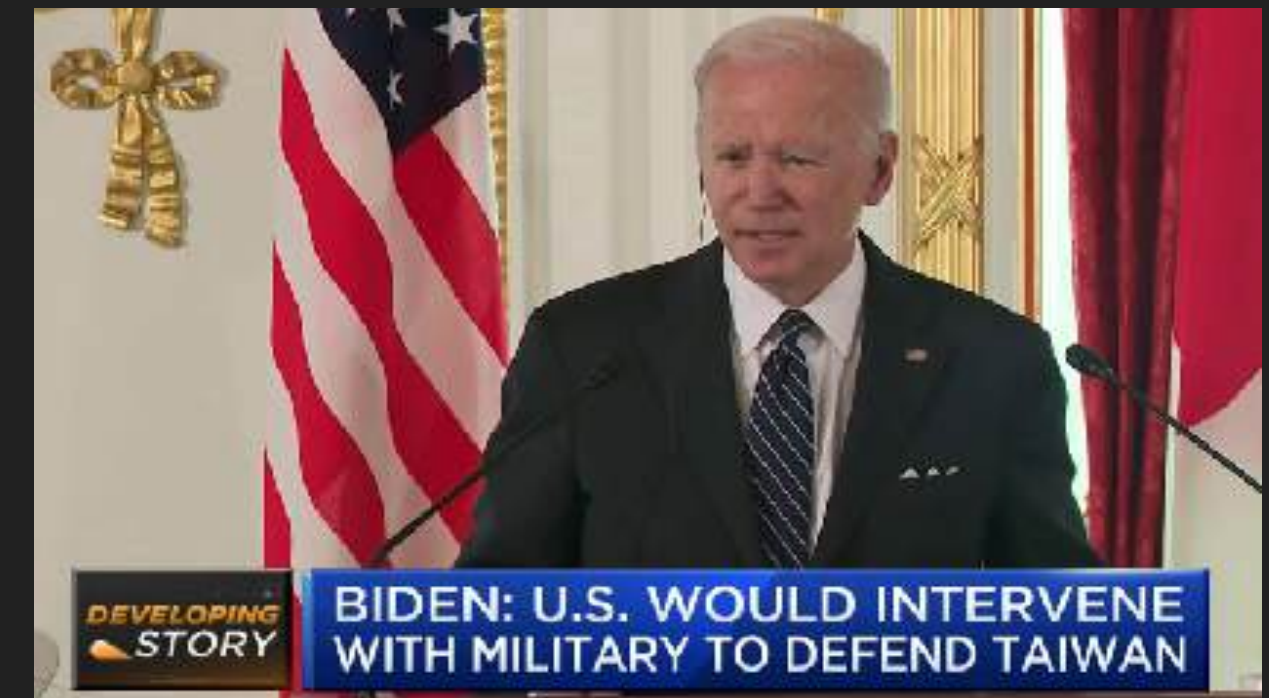


Chris Miller: "Chip War: The Fight for the World's Most Critical Technology" <https://thesoundingline.com/chart-chinese-navy-vs-us-navy/>  
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<https://worldview.stratfor.com/article/chinese-drills-around-taiwan-practice-blockade-even-if-one-isnt-imminent>  
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May '24

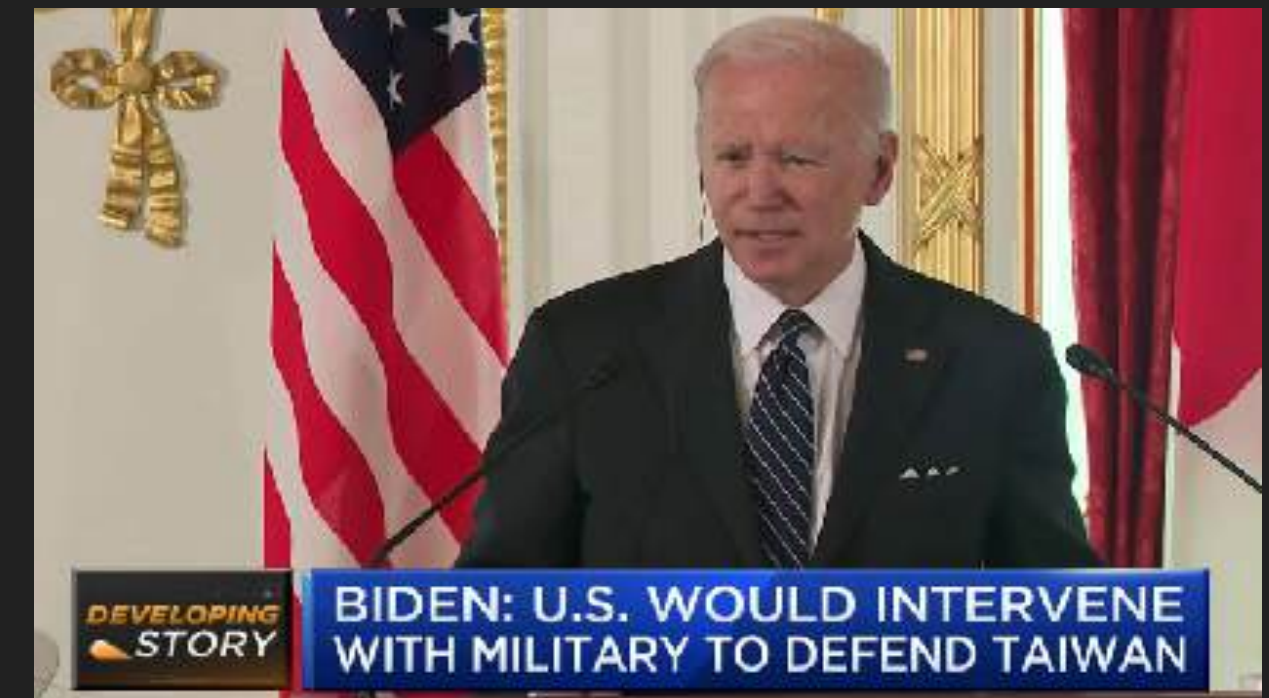
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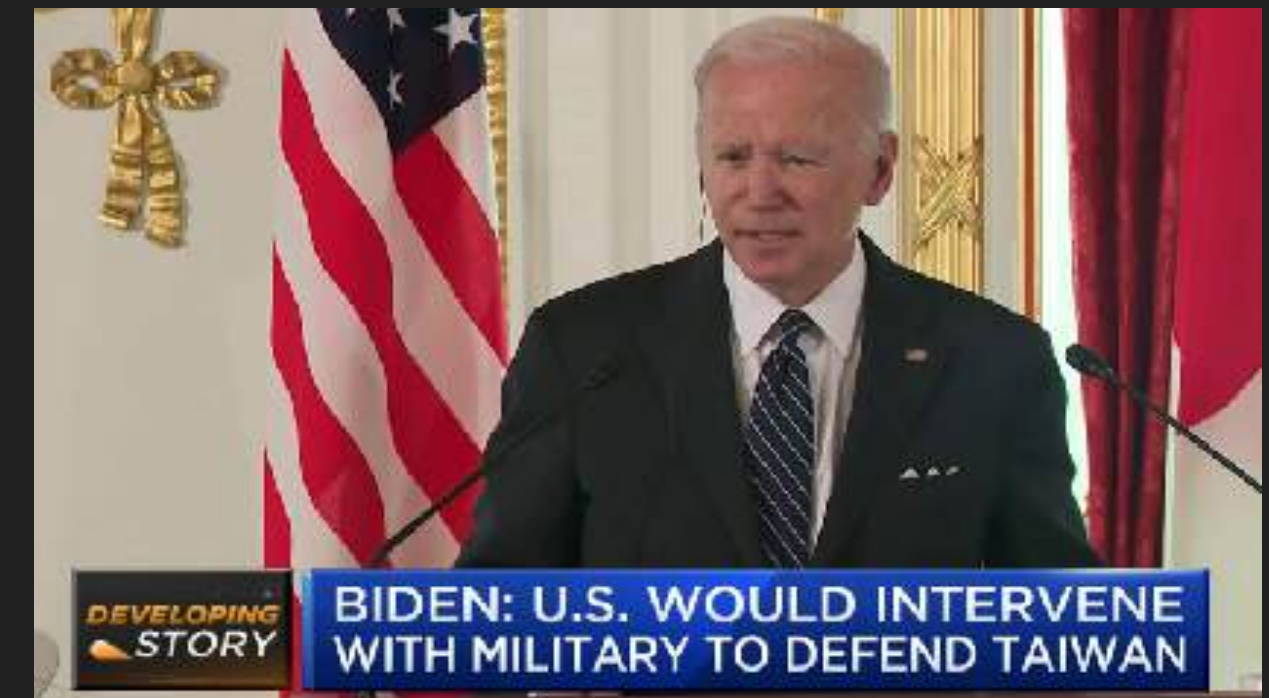
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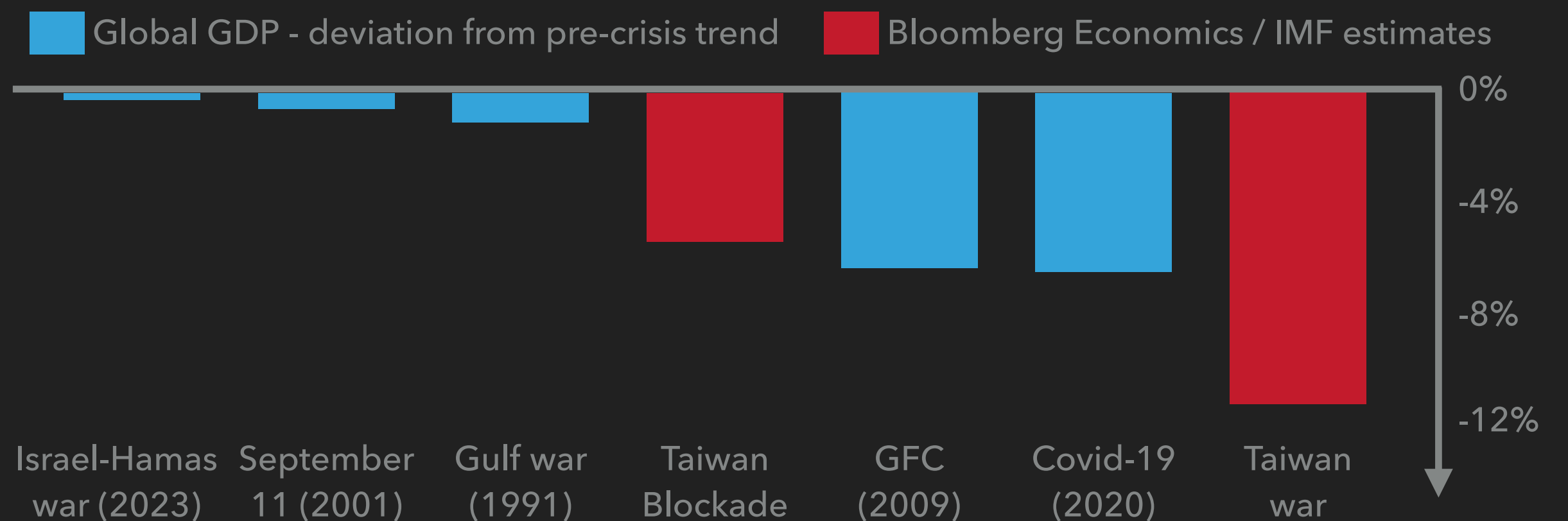
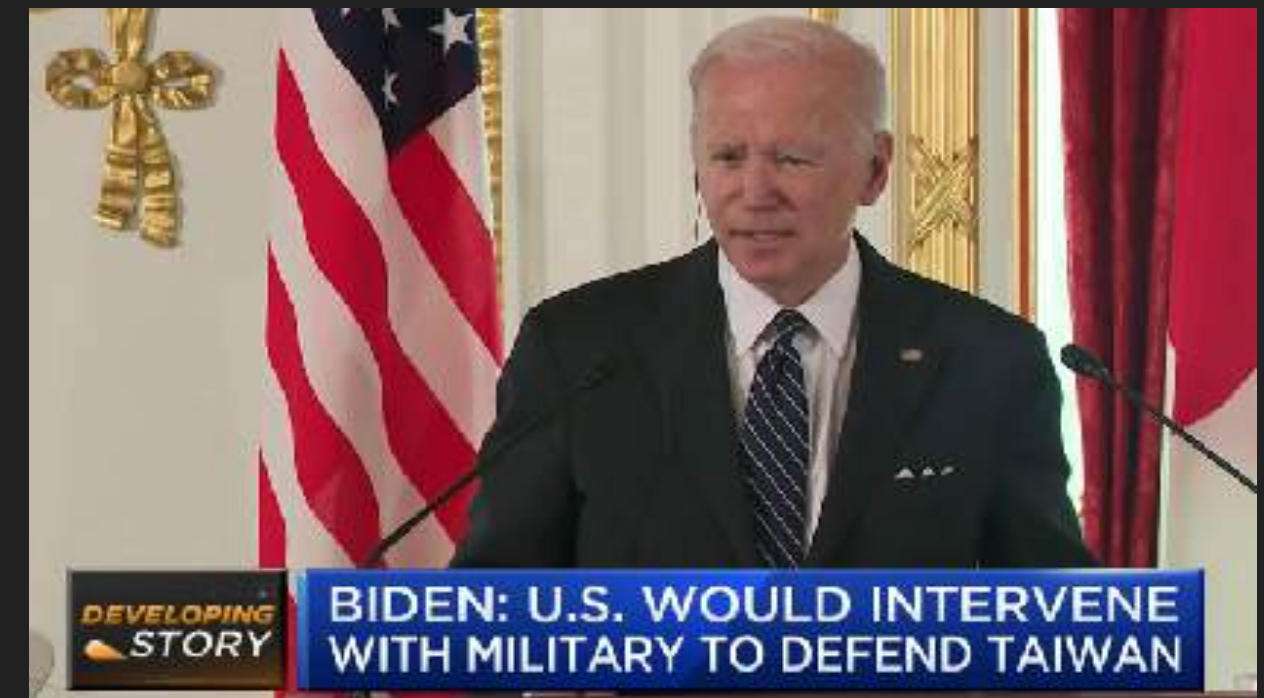
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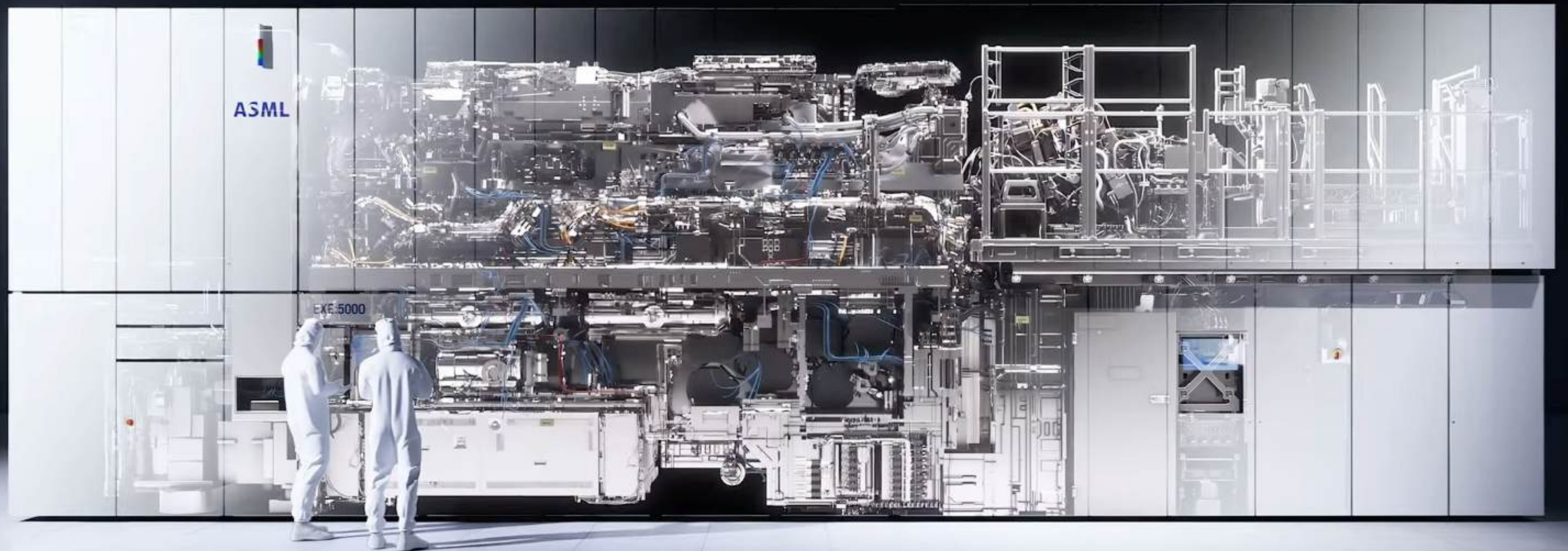
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Chris Miller: "Chip War: The Fight for the World's Most Critical Technology"

Contact: [thellert@lbl.gov](mailto:thellert@lbl.gov)