



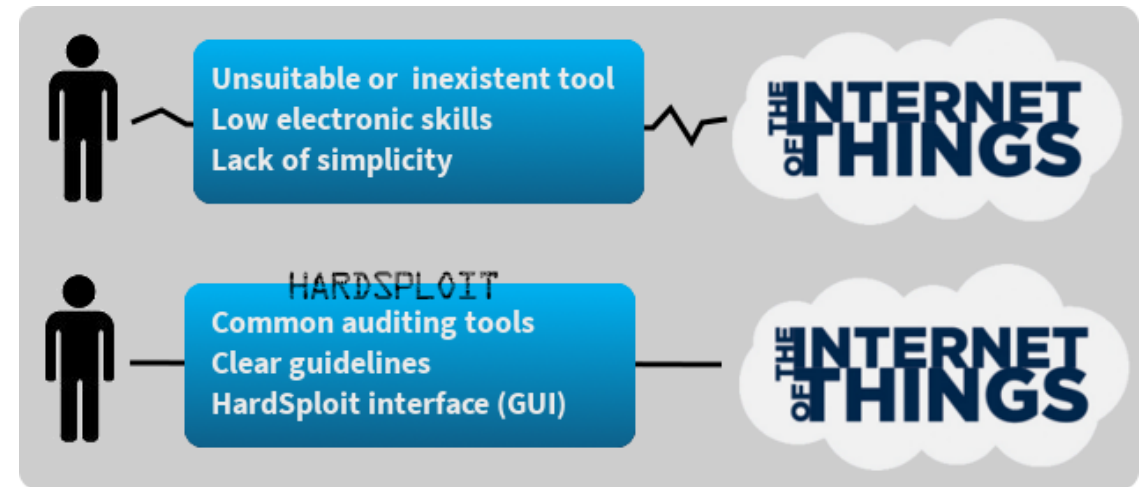
Hardsploit (Hardsploit.io)

Like Metasploit but For Hardware Hacking

32C3CFP Submission

What is Harsploit?

- A Framework for Hardware Pentest or electronic designers
- Open Source
- Hardware + Software
- More details on Hardsploit.io

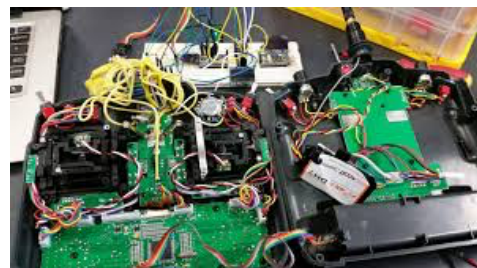


Why we choose to create HardSploit? (1/2)

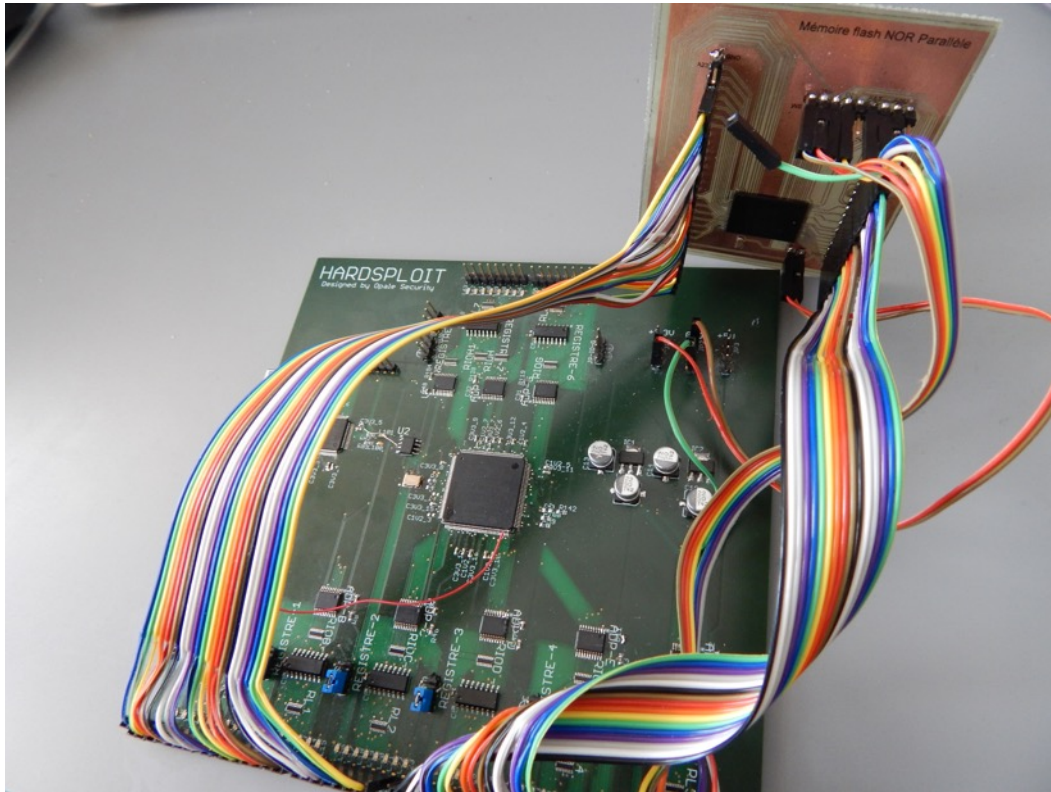
- Facilitate the audit of electronic systems for industry ‘security’ workers
 - Consultant, Auditor, Pentesters, Product designer etc.
- Increase the level of security (and trust!) of new communicating products designed by industry

Why we choose to create HardSploit? (2/2)

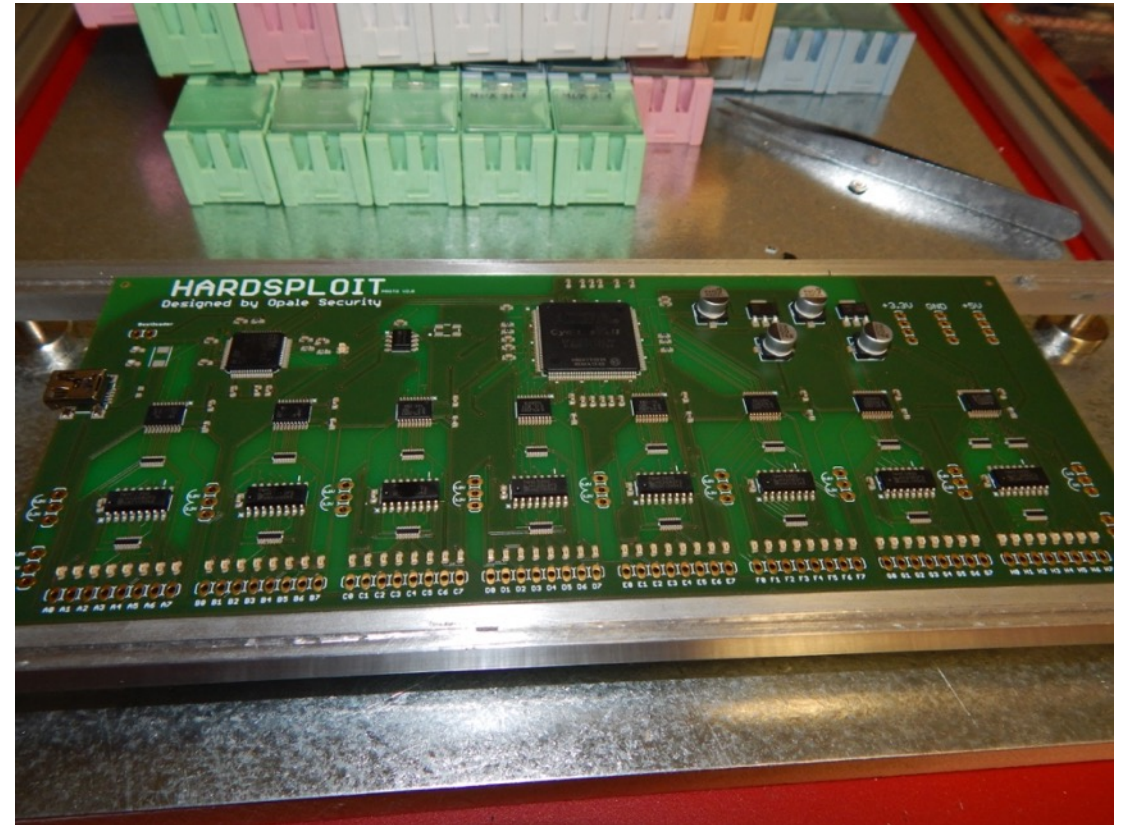
- To create a « all in one tools » for Hardware Hacking



Some Hardsploit prototypes photos



Proto V1



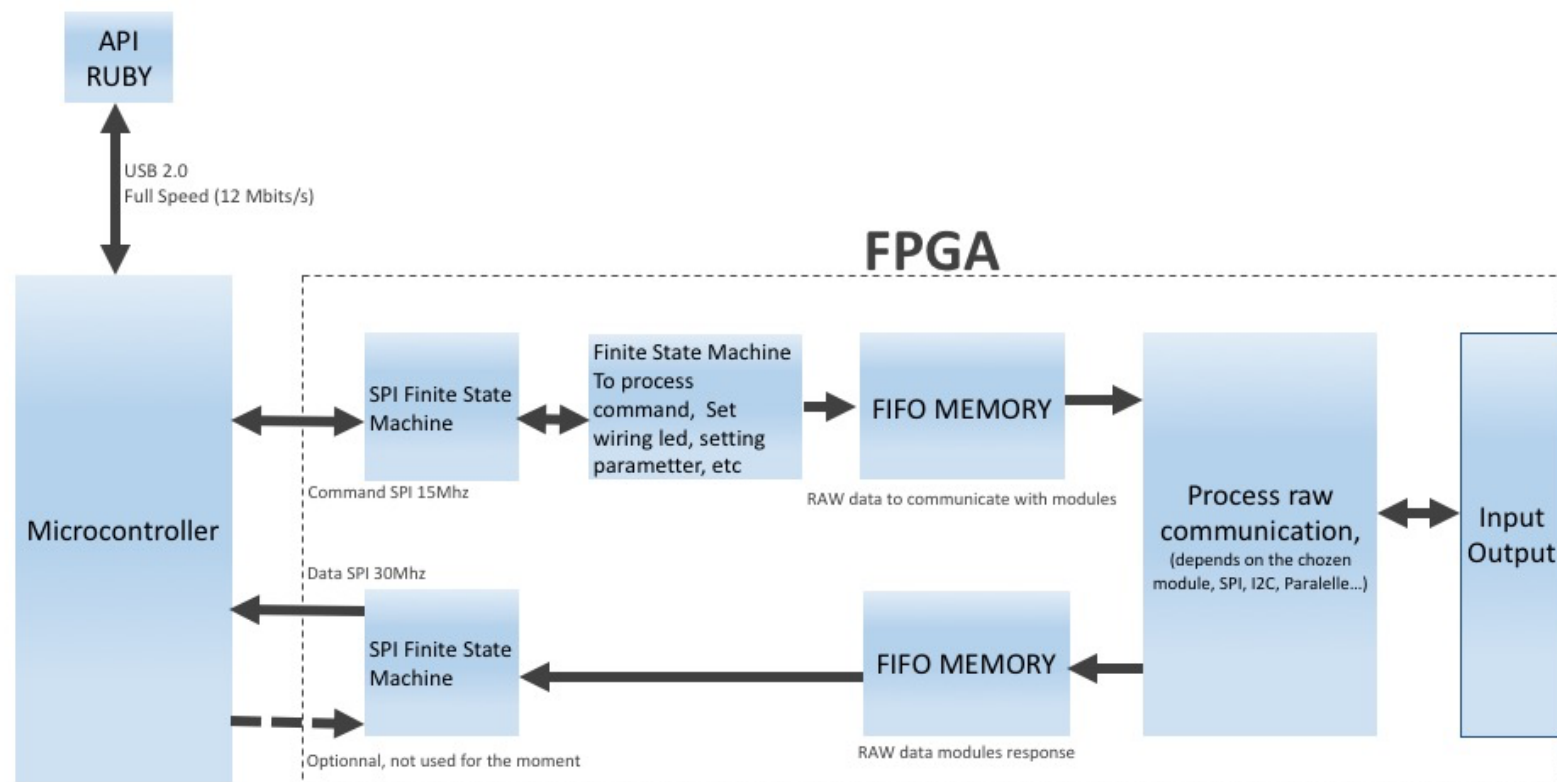
Final form factor on 20 06 2015

Hardware Features

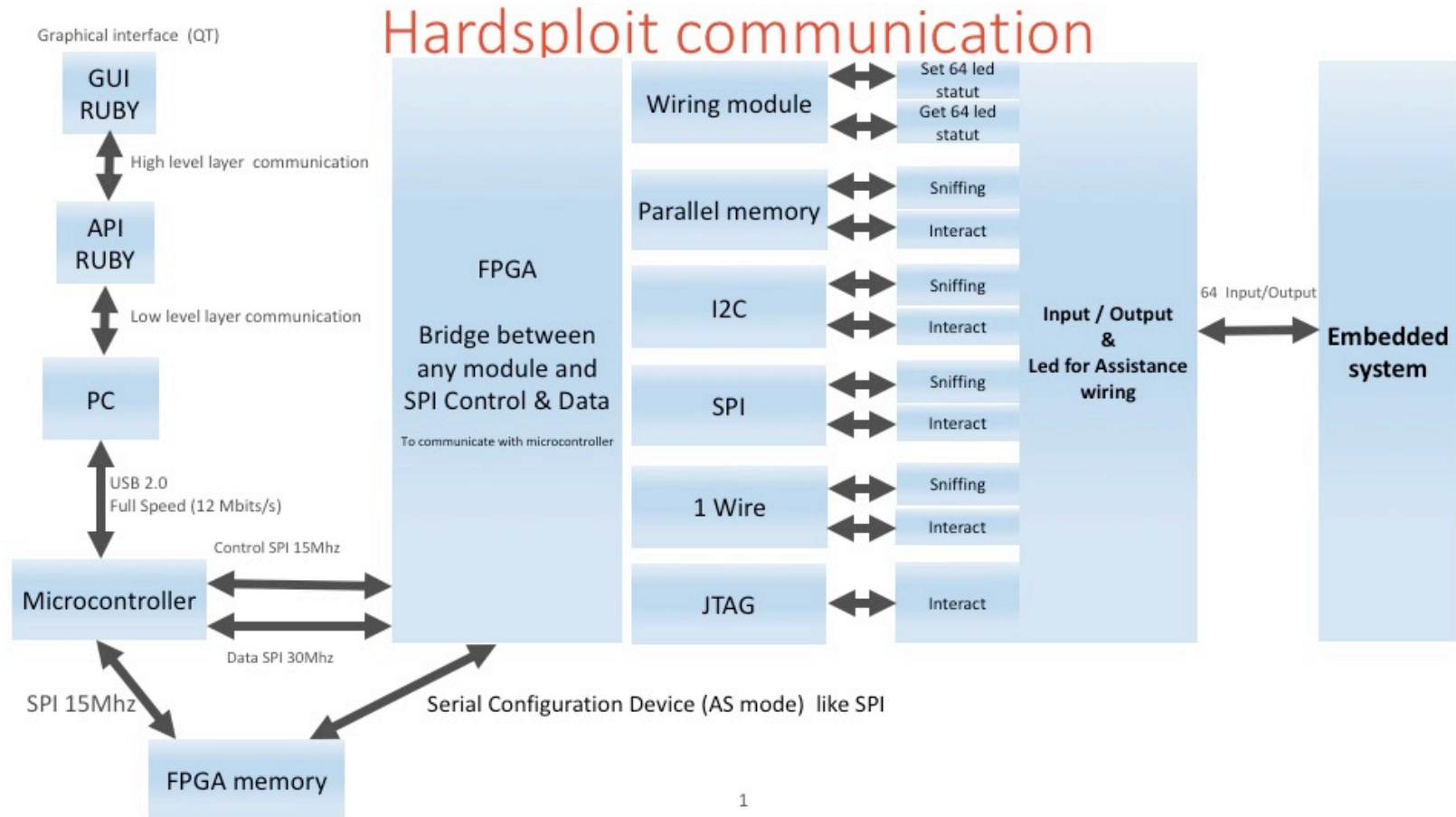
- All-in-one tool dedicated for Hardware Hacking
- 64 I/O channels
- Adjustable target voltage for level translation: 3,3V & 5V
- FGPA Cyclone II for versatile and powerfull electronic hardware hacking modules
- USB interface for direct connection to GUI
- Easy-to-use GUI & Console mode integrated in the Metasploit Framework

Internal design (1/2)

Hardsploit internal communication



Internal design (2/2)



Hardsploit GUI



Hardsploit - Chip Management

Search filters:

Manufacturer... | Type...

Ref	Type	Manufacturer	
1	P33-65nm	PARALLEL MEMORY	Numonyx
2	Test	MEMORY	Numonyx

Hardsploit - Chip creation

Package | Characteristics | Pins | Memory | Misc

Existing package

Not in the list ? Create a new one :

Name

Pin number

Geometric shape Rectangular Square

To complete this form, please report to the datasheet.

Hardsploit - Wiring Wizard

Choose a bus / signal in the list or click directly on a pin / number

Bus... | Signal... |

Your chip:

P33-65nm

A16	1	56	WAIT
A15	2	55	A17
A14	3	54	DQ15
A13	4	53	DQ7
A12	5	52	DQ14
A11	6	51	DQ6
A10	7	50	DQ13
A9	8	49	DQ5
A23	9	48	DQ12
A22	10	47	DQ4
A21	11	46	ADV#
VSS	12	45	CLK
NC	13	44	RST#
WE#	14	43	VPP
WP#	15	42	DQ11
A20	16	41	DQ3
A19	17	40	DQ10
A18	18	39	DQ2
A8	19	38	VCCQ
A7	20	37	DQ9
A6	21	36	DQ1
A5	22	35	DQ8
A4	23	34	DQ0
A3	24	33	VCC
A2	25	32	OE#
NC	26	31	VSS
NC	27	30	CE#
VSS	28	29	A1

Hardsploit - Parallel M

Parallel memory dump

Start

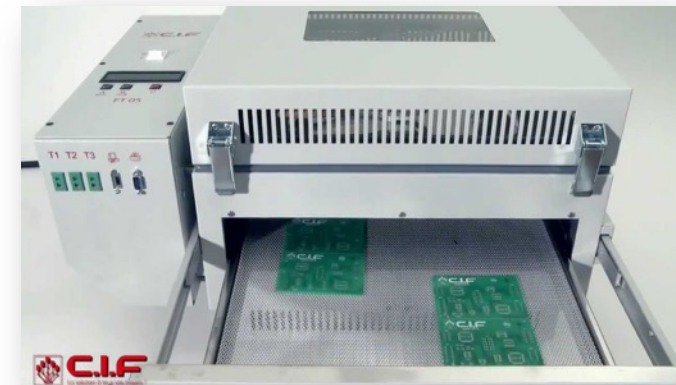
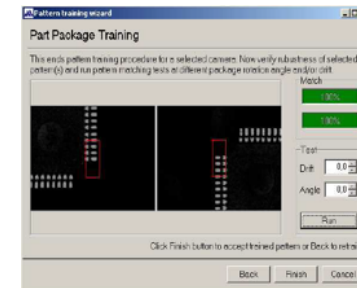
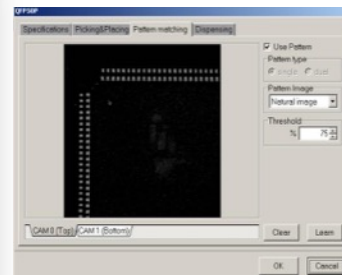
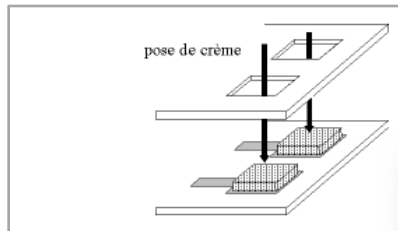
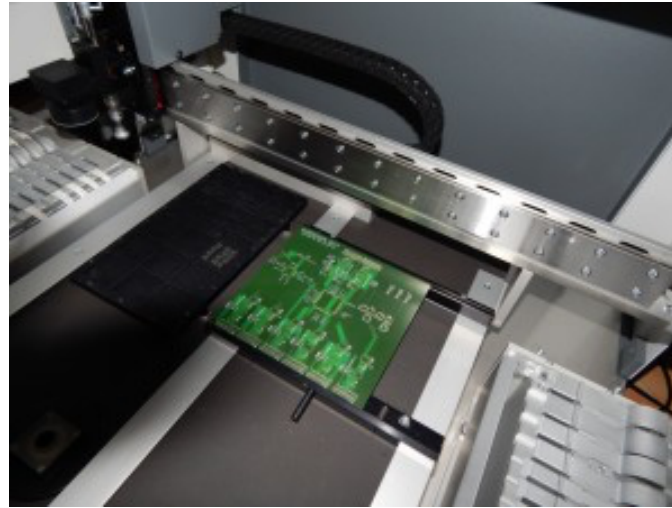
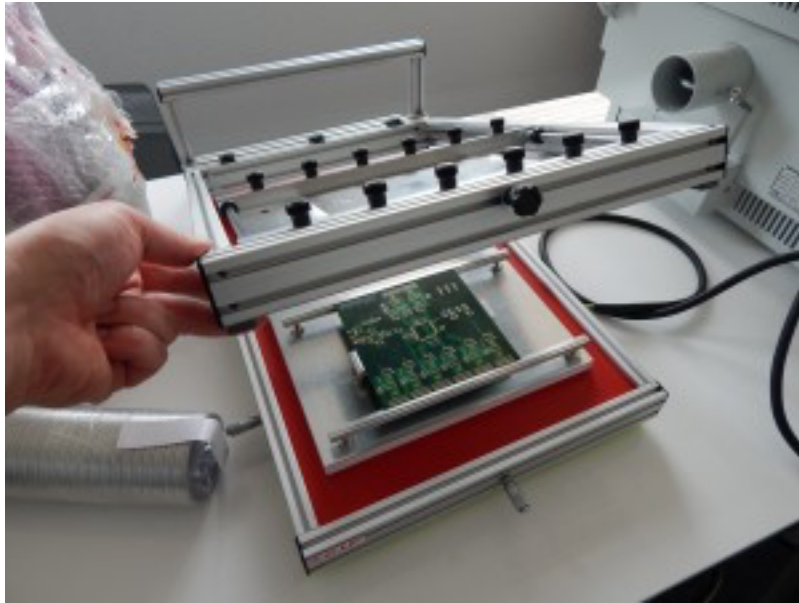
Stop

Parameters

Latency

Word size 8 bits 16 bits

How we create Hardsploit Board !



Hardsploit modules & Framework

- Hardsploit is a tool with software and electronic aspects
- This is a technical and modular platform (using FPGA)
- To perform security tests on electronic communications interfaces of embedded devices
- It's a Framework !
- *All-in-one tool for Hardware pentest*

Features

- The main Hardware security audit functions are
 - Sniffer,
 - Scanner,
 - Interact,
 - Dump memory (even paralleles ones)
 - ...
- Hardsploit modules will let hardware pentester intercept, replay and/or and send data via each type of electronic bus used by the target. The level of interaction that pen-testers will depend on the electronic bus features...

Hardsploit modules

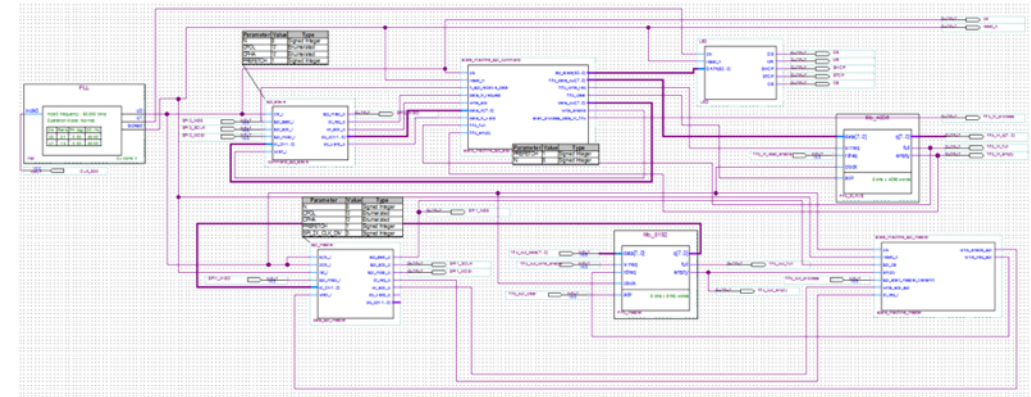
- Hardsploit 's modules enable you to analyse all sort of electronic bus (serial and parallel type)
- JTAG, SPI, I2C's,
- Parallel address & data bus on chip,
- and more others to come in the futur...

Assisted visual wiring function

- *No more stress with that tremendous part of Hardware pen testing : You will know what need to be connected and where !*
- We have integrate into the tool an assisted visual wiring function to help you connect easily all wires to the hardware target:
 - GUI will display the pin organization (Pin OUT) of the targeted chip.
 - GUI will guide you throughout the wiring process between Hardsploit connectors and the target
 - GUI will control a set of LED that will turn ON / OFF to let you find the right Hardsploit pin to connect to your target

How a module is designed : parallel memory dump example (1/2)

- We have created a FPGA module that is able to dump most of parallel memory chip.
- It will help security pentesters to dump firmware or all content contained in such memory in an easy way.
- Easier than if creating a dumping function each time ... No more arduino like board with plenty of wiring difficulties to connect to your chip, no more trouble to find the right memory command to be able to dump the component in front of you... The GUI will help you achieve that in few click only.
- Faster, as we use high speed FPGA buses and machine state to achieved the dump.



1st result : only 5 to 10 min to read a embedded linux rom of 128MB.

Conclusion

- Hope our modest submission could interest your selection committee and attendees
- Contact : +33 6 45 45 33 81
- Mail : yann.allain@opale-security.com

